UUT acq2106_340
UUT Serial No CE4160340
UUT Text
Site 0
EpicsPV Prefix acq2106_340:
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
0		AI:TW:01								Raw Transient Waveform CH01
0		AI:TW:02								Raw Transient Waveform CH02
0		AI:TW:03								Raw Transient Waveform CH03
0		AI:TW:04								Raw Transient Waveform CH04
0		AI:TW:05								Raw Transient Waveform CH05
0		AI:TW:06								Raw Transient Waveform CH06
0		AI:TW:07								Raw Transient Waveform CH07
0		AI:TW:08								Raw Transient Waveform CH08
0		AI:TW:09								Raw Transient Waveform CH09
0		AI:TW:10								Raw Transient Waveform CH10
0		AI:TW:11								Raw Transient Waveform CH11
0		AI:TW:12								Raw Transient Waveform CH12
0		AI:TW:13								Raw Transient Waveform CH13
0		AI:TW:14								Raw Transient Waveform CH14
0		AI:TW:15								Raw Transient Waveform CH15
0		AI:TW:16								Raw Transient Waveform CH16
0		AI:TW:17								Raw Transient Waveform CH17
0		AI:TW:18								Raw Transient Waveform CH18
0		AI:TW:19								Raw Transient Waveform CH19
0		AI:TW:20								Raw Transient Waveform CH20
0		AI:TW:21								Raw Transient Waveform CH21
0		AI:TW:22								Raw Transient Waveform CH22
0		AI:TW:23								Raw Transient Waveform CH23
0		AI:TW:24								Raw Transient Waveform CH24
0		AI:TW:25								Raw Transient Waveform CH25
0		AI:TW:26								Raw Transient Waveform CH26
0		AI:TW:27								Raw Transient Waveform CH27

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
0		AI:TW:28								Raw Transient Waveform CH28
0		AI:TW:29								Raw Transient Waveform CH29
0		AI:TW:30								Raw Transient Waveform CH30
0		AI:TW:31								Raw Transient Waveform CH31
0		AI:TW:32								Raw Transient Waveform CH32
0		AI:TW:33								Raw Transient Waveform CH33
0		AI:TW:34								Raw Transient Waveform CH34
0		AI:TW:35								Raw Transient Waveform CH35
0		AI:TW:36								Raw Transient Waveform CH36
0		AI:TW:37								Raw Transient Waveform CH37
0		AI:TW:38								Raw Transient Waveform CH38
0		AI:TW:39								Raw Transient Waveform CH39
0		AI:TW:40								Raw Transient Waveform CH40
0		AI:TW:41								Raw Transient Waveform CH41
0		AI:TW:42								Raw Transient Waveform CH42
0		AI:TW:43								Raw Transient Waveform CH43
0		AI:TW:44								Raw Transient Waveform CH44
0		AI:TW:45								Raw Transient Waveform CH45
0		AI:TW:46								Raw Transient Waveform CH46
0		AI:TW:47								Raw Transient Waveform CH47
0		AI:TW:48								Raw Transient Waveform CH48
0		asyn:NP	time_long			rw	0	0		EPICS ASYN channel record (expert debug ONLY)
0		asyn:S0	time_long			rw	0	0		EPICS ASYN channel record (expert debug ONLY)
0		asyn:S0A	time_long			rw	0	0		EPICS ASYN channel record (expert debug ONLY)
0		asyn:S0B	time_long			rw	0	0		EPICS ASYN channel record (expert debug ONLY)
0		asyn:S0C	time_long			rw	0	0		EPICS ASYN channel record (expert debug ONLY)
0		asyn:S0R	time_long			rw	0	0		EPICS ASYN channel record (expert debug ONLY)

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
0		asyn:S0S	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S1	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S1M	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S1_480	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S2	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S2_480	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S3	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S3_480	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S4	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S4_480	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S5	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S5_480	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S6	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:S6_480	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:SA	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:SB	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:SC	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:SD	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:SG0	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)

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0		asyn:SI	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:SPEV	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:SS	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0		asyn:ST	time_long			rw		0	0	EPICS ASYN channel record (expert debug ONLY)
0	IOC_READY	IOC_READY	time_enum		0, 1	rw	1			EPICS IO Controller Ready
0		LIVE:LEN	time_long			rw	4096	0	0	Current Live status count LEN
0		LIVE:MODE	time_enum		off, free-run, pre-post	rw	off			Current Live status count MODE
0		LIVE:POST	time_long			rw	2048	0	0	Current Live status count POST
0		LIVE:PRE	time_long			rw	2048	0	0	Current Live status count PRE
0		LIVE:PREPCT	time_long			rw	50	100	0	Current Live status count PREPCT
0		MGT:HG:A:PULL_DATA	time_long			rw		0	0	
0		MGT:HG:A:PULL_DESC	time_long			rw		0	0	
0		MGT:HG:A:PUSH_DATA	time_long			rw		0	0	
0		MGT:HG:A:PUSH_DESC	time_long			rw		0	0	
0		MGT:HG:B:PULL_DATA	time_long			rw		0	0	
0		MGT:HG:B:PULL_DESC	time_long			rw		0	0	
0		MGT:HG:B:PUSH_DATA	time_long			rw		0	0	
0		MGT:HG:B:PUSH_DESC	time_long			rw		0	0	
0		MGT:HG:C:PULL_DATA	time_long			rw		0	0	
0		MGT:HG:C:PULL_DESC	time_long			rw		0	0	
0		MGT:HG:C:PUSH_DATA	time_long			rw		0	0	
0		MGT:HG:C:PUSH_DESC	time_long			rw		0	0	
0		MGT:HG:D:PULL_DATA	time_long			rw		0	0	
0		MGT:HG:D:PULL_DESC	time_long			rw		0	0	
0		MGT:HG:D:PUSH_DATA	time_long			rw		0	0	
0		MGT:HG:D:PUSH_DESC	time_long			rw		0	0	
0		MGT:SFP:1:LOS	time_enum		,	rw				SFP 1 status LOS

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
0		MGT:SFP:1:PRESENT	time_enum		,	rw				SFP 1 status PRESENT
0		MGT:SFP:2:LOS	time_enum		,	rw				SFP 2 status LOS
0		MGT:SFP:2:PRESENT	time_enum		,	rw				SFP 2 status PRESENT
0		MGT:SFP:3:LOS	time_enum		,	rw				SFP 3 status LOS
0		MGT:SFP:3:PRESENT	time_enum		,	rw				SFP 3 status PRESENT
0		MGT:SFP:4:LOS	time_enum		,	rw				SFP 4 status LOS
0		MGT:SFP:4:PRESENT	time_enum		,	rw				SFP 4 status PRESENT
0		MGT:SFP:A:CLRSTATS	time_enum		,	rw				
0		MGT:SFP:B:CLRSTATS	time_enum		,	rw				
0		MGT:SFP:C:CLRSTATS	time_enum		,	rw				
0		MGT:SFP:D:CLRSTATS	time_enum		,	rw				
0	MGTPORTS	MGTPORTS	time_long			rw		0	0	
0	MIRROR_EN	MIRROR_EN	time_enum		,	rw				PV Mirror control EN
0	MIRROR_HOST	MIRROR_HOST	time_string			rw				PV Mirror control HOST
0		MOD:1:FAIL	time_enum		,	rw				Module Site 1 Status FAIL
0		MOD:1:GOOD	time_enum		,	rw				Module Site 1 Status GOOD
0		MOD:1:LED_RR	time_double			rw		0	0	Module Site 1 Status LED_RR
0		MOD:1:WARN	time_enum		,	rw				Module Site 1 Status WARN
0		MOD:2:FAIL	time_enum		,	rw				Module Site 2 Status FAIL
0		MOD:2:GOOD	time_enum		,	rw				Module Site 2 Status GOOD
0		MOD:2:LED_RR	time_double			rw		0	0	Module Site 2 Status LED_RR
0		MOD:2:WARN	time_enum		,	rw				Module Site 2 Status WARN
0		MOD:3:FAIL	time_enum		,	rw				Module Site 3 Status FAIL
0		MOD:3:GOOD	time_enum		,	rw				Module Site 3 Status GOOD
0		MOD:3:LED_RR	time_double			rw		0	0	Module Site 3 Status LED_RR
0		MOD:3:WARN	time_enum		,	rw				Module Site 3 Status WARN
0		MOD:4:FAIL	time_enum		,	rw				Module Site 4 Status FAIL
0		MOD:4:GOOD	time_enum		,	rw				Module Site 4 Status GOOD
0		MOD:4:LED_RR	time_double			rw		0	0	Module Site 4 Status LED_RR
0		MOD:4:WARN	time_enum		,	rw				Module Site 4 Status WARN

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0		MOD:5:FAIL	time_enum		,	rw				Module Site 5 Status FAIL
0		MOD:5:GOOD	time_enum		,	rw				Module Site 5 Status GOOD
0		MOD:5:LED_RR	time_double			rw		0	0	Module Site 5 Status LED_RR
0		MOD:5:WARN	time_enum		,	rw				Module Site 5 Status WARN
0		MOD:6:FAIL	time_enum		,	rw				Module Site 6 Status FAIL
0		MOD:6:GOOD	time_enum		,	rw				Module Site 6 Status GOOD
0		MOD:6:LED_RR	time_double			rw		0	0	Module Site 6 Status LED_RR
0		MOD:6:WARN	time_enum		,	rw				Module Site 6 Status WARN
0		MODE:CONTINUOUS	time_enum		stop, start	rw	stop			Mode setting CONTINUOUS
0		MODE:CONTINUOUS:STATE	time_enum		IDLE, ARM, RUN, RUN2, POST_PROCESS, CLEA	rw	IDLE			Mode setting CONTINUOUS:STATE
0		MODE:CONTINUOUS:STATUS	time_string			rw				Mode setting CONTINUOUS:STATUS
0		MODE:TRANSIENT	time_enum		,	rw				Mode setting TRANSIENT
0		MODE:TRANSIENT:DELAYMS	time_long			rw	1000	0	0	Mode setting TRANSIENT:DELAYMS
0		MODE:TRANSIENT:OSAM	time_long	Samples		rw	1	256	0	Mode setting TRANSIENT:OSAM
0		MODE:TRANSIENT:POST	time_long	Samples		rw		100000000	0	Mode setting TRANSIENT:POST
0		MODE:TRANSIENT:PRE	time_long	Samples		rw		100000000	0	Mode setting TRANSIENT:PRE
0		MODE:TRANSIENT:REPEAT	time_long			rw		99999	-1	Mode setting TRANSIENT:REPEAT
0		MODE:TRANSIENT:SET_ABORT	time_enum		,	rw				Mode setting TRANSIENT:SET_ABORT
0		MODE:TRANSIENT:SET_ARM	time_enum		,	rw				Mode setting TRANSIENT:SET_ARM
0		MODE:TRANSIENT:SOFT_TRIGGER	time_long	Samples		rw	1	1	0	Mode setting TRANSIENT:SOFT_TRIGGER
0		MODE:TRANS_ACT:FIND_EV:CUR	time_long			rw		0	0	Transient Mode Actual value FIND_EV:CUR
0		MODE:TRANS_ACT:FIND_EV:NBU	time_long			rw		0	0	Transient Mode Actual value FIND_EV:NBU
0		MODE:TRANS_ACT:FIND_EV:STA	time_enum		IDLE, SEARCH, FOUND, NOT FOUND, FAIL	rw	IDLE			Transient Mode Actual value FIND_EV:STA
0		MODE:TRANS_ACT:POST	time_long			rw		0	0	Transient Mode Actual value POST
0		MODE:TRANS_ACT:POST:MDSPUTCH	time_long			rw	-1	0	0	Transient Mode Actual value POST:MDSPUTCH
0		MODE:TRANS_ACT:PRE	time_long			rw		0	0	Transient Mode Actual value PRE
0		MODE:TRANS_ACT:STATE	time_enum		IDLE, ARM, RUN_PRE, RUN_POST, POST_PROCE	rw	IDLE			Transient Mode Actual value STATE

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0		MODE:TRANS_ACT:STATE_NOT_IDLE	time_double			rw		0	0	Transient Mode Actual value STATE_NOT_IDLE
0		MODE:TRANS_ACT:TOTSAM	time_long			rw		0	0	Transient Mode Actual value TOTSAM
0	SHOT_COMPLETE	SHOT_COMPLETE	time_long			rw		0	0	shot is complete
0		SIG:SOFT_TRIGGER	time_enum		,	rw				pulse SOFT_TRIGGER
0		SIG:SYNC_BUS:EVT:D0	time_enum		LO, HI	rw	HI			Signal Status SYNC_BUS EVT
0		SIG:SYNC_BUS:EVT:D1	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS EVT
0		SIG:SYNC_BUS:EVT:D2	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS EVT
0		SIG:SYNC_BUS:EVT:D3	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS EVT
0		SIG:SYNC_BUS:EVT:D4	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS EVT
0		SIG:SYNC_BUS:EVT:D5	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS EVT
0		SIG:SYNC_BUS:EVT:D6	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS EVT
0		SIG:SYNC_BUS:EVT:D7	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS EVT
0		SIG:SYNC_BUS:TRG:D0	time_enum		LO, HI	rw	HI			Signal Status SYNC_BUS TRG
0		SIG:SYNC_BUS:TRG:D1	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS TRG
0		SIG:SYNC_BUS:TRG:D2	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS TRG
0		SIG:SYNC_BUS:TRG:D3	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS TRG
0		SIG:SYNC_BUS:TRG:D4	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS TRG
0		SIG:SYNC_BUS:TRG:D5	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS TRG
0		SIG:SYNC_BUS:TRG:D6	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS TRG
0		SIG:SYNC_BUS:TRG:D7	time_enum		LO, HI	rw	LO			Signal Status SYNC_BUS TRG
0	SITELIST	SITELIST	time_string			rw	216,1=482,2=482,3=482,4=482,5=482,6=482			Lists sites in system
0		SYS:0:TEMP	time_double	C		rw	26	99	0	Temperature sensor site 0
0		SYS:1:1V8P	time_double	V		rw	1.75	5	0	site 1 voltage 1V8
0		SYS:1:3V3P	time_double	V		rw	3.33	5	0	site 1 voltage 3V3
0		SYS:1:TEMP	time_double	C		rw	42	99	0	Temperature sensor site 1
0		SYS:1:VAN	time_double			rw	-5.8	0	-15	Sets main analog PSU N rail volts for site 1
0		SYS:1:VAP	time_double	V		rw	4.99	15	0	Sets main analog PSU P rail volts for site 1
0		SYS:2:TEMP	time_double	C		rw		99	0	Temperature sensor site 2
0		SYS:3:1V8P	time_double	V		rw	1.75	5	0	site 3 voltage 1V8
0		SYS:3:3V3P	time_double	V		rw	3.32	5	0	site 3 voltage 3V3

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0		SYS:3:TEMP	time_double	C		rw	40	99	0	Temperature sensor site 3
0		SYS:3:VAN	time_double			rw	-5.7	0	-15	Sets main analog PSU N rail volts for site 3
0		SYS:3:VAP	time_double	V		rw	5.03	15	0	Sets main analog PSU P rail volts for site 3
0		SYS:4:TEMP	time_double	C		rw		99	0	Temperature sensor site 4
0		SYS:5:1V8P	time_double	V		rw	1.75	5	0	site 5 voltage 1V8
0		SYS:5:3V3P	time_double	V		rw	3.32	5	0	site 5 voltage 3V3
0		SYS:5:TEMP	time_double	C		rw	38	99	0	Temperature sensor site 5
0		SYS:5:VAN	time_double			rw	-5.7	0	-15	Sets main analog PSU N rail volts for site 5
0		SYS:5:VAP	time_double	V		rw	4.99	15	0	Sets main analog PSU P rail volts for site 5
0		SYS:5VP	time_double	V		rw	4.97	15	0	System 5V rail value
0		SYS:6:TEMP	time_double	C		rw		99	0	Temperature sensor site 6
0		SYS:CLK:BYPASS	time_enum		,	rw				
0		SYS:CLK:C1B	time_enum		,	rw				
0		SYS:CLK:C2B	time_enum		,	rw				
0		SYS:CLK:CONFIG	time_string			rw	-1.00e+06- -2.00e+07			
0		SYS:CLK:FPMUX	time_enum		OFF, XCLK, FPCLK, ZCLK	rw	ZCLK			clock front panel MUX selector
0		SYS:CLK:LOL	time_double			rw		0	0	
0		SYS:CLK:OE_CLK1_ELF1	time_enum		,	rw				
0		SYS:CLK:OE_CLK1_ELF2	time_enum		,	rw				
0		SYS:CLK:OE_CLK1_ELF3	time_enum		,	rw				
0		SYS:CLK:OE_CLK1_ELF4	time_enum		,	rw				
0		SYS:CLK:OE_CLK1_ELF5	time_enum		,	rw				
0		SYS:CLK:OE_CLK1_ELF6	time_enum		,	rw				
0		SYS:CLK:OE_CLK1_ZYNQ	time_enum		,	rw				
0		SYS:CLK:Si5326:PLAN	time_enum		10M, 20M, 24M, 40M, 50M, 80M	rw	10M			

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0		SYS:CLK:Si5326:PLAN_EN	time_double			rw		0	0	
0		SYS:HB	time_double			rw		0	0	
0		SYS:IFCONFIG:ETH0	time_string			rw	10.12.197.9			Ethernet eth0 ip address Initial Value: 10.12.199.1 PVType: time_string
0		SYS:IFCONFIG:ETH1	time_string			rw				Ethernet eth1 ip address Initial Value: 10.12.199.1 PVType: time_string
0		SYS:INTMON:IRQ:16:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:16:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:16:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:17:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:17:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:17:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:18:ACTIVE	time_double			rw	1.0	0	0	
0		SYS:INTMON:IRQ:18:COUNT	time_double			rw	4e+05	0	0	
0		SYS:INTMON:IRQ:18:FREQ	time_double	Hz		rw	1394	0	0	
0		SYS:INTMON:IRQ:19:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:19:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:19:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:20:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:20:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:20:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:21:ACTIVE	time_double			rw	1.0	0	0	
0		SYS:INTMON:IRQ:21:COUNT	time_double			rw	112	0	0	
0		SYS:INTMON:IRQ:21:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:23:ACTIVE	time_double			rw	1.0	0	0	
0		SYS:INTMON:IRQ:23:COUNT	time_double			rw	29968	0	0	
0		SYS:INTMON:IRQ:23:FREQ	time_double	Hz		rw	106	0	0	
0		SYS:INTMON:IRQ:25:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:25:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:25:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:27:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:27:COUNT	time_double			rw	13	0	0	

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0		SYS:INTMON:IRQ:27:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:28:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:28:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:28:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:29:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:29:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:29:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:30:ACTIVE	time_double			rw	1.0	0	0	
0		SYS:INTMON:IRQ:30:COUNT	time_double			rw	52741	0	0	
0		SYS:INTMON:IRQ:30:FREQ	time_double	Hz		rw	428	0	0	
0		SYS:INTMON:IRQ:31:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:31:COUNT	time_double			rw	15	0	0	
0		SYS:INTMON:IRQ:31:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:32:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:32:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:32:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:33:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:33:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:33:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:34:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:34:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:34:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:35:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:35:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:35:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:36:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:36:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:36:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:37:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:37:COUNT	time_double			rw		0	0	

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
0		SYS:INTMON:IRQ:37:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:38:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:38:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:38:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:39:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:39:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:39:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:40:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:40:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:40:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:41:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:41:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:41:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:47:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:47:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:47:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:51:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:51:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:51:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:52:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:52:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:52:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:53:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:53:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:53:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:54:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:54:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:54:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:55:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:55:COUNT	time_double			rw		0	0	

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
0		SYS:INTMON:IRQ:55:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:56:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:56:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:56:FREQ	time_double	Hz		rw		0	0	
0		SYS:INTMON:IRQ:81:ACTIVE	time_double			rw		0	0	
0		SYS:INTMON:IRQ:81:COUNT	time_double			rw		0	0	
0		SYS:INTMON:IRQ:81:FREQ	time_double	Hz		rw		0	0	
0		SYS:IOCUPTIME	time_double			rw	273	0	0	Time that the IOC has been running for.
0		SYS:LED:ACT_A	time_double			rw		0	0	LED ACT_A
0		SYS:LED:ACT_G	time_enum		OFF, ON	rw	OFF			LED ACT_G
0		SYS:LED:ACT_GG	time_double			rw		0	0	LED ACT_GG
0		SYS:LED:ACT_R	time_enum		OFF, ON	rw	OFF			LED ACT_R
0		SYS:LED:ACT_RR	time_double			rw		0	0	LED ACT_RR
0		SYS:LED:CLK	time_enum		,	rw				LED CLK
0		SYS:LED:CLK:FAULT	time_double			rw		0	0	LED CLK
0		SYS:LED:CLK_A	time_double			rw		0	0	CLK source select A
0		SYS:LED:CLK_GG	time_double			rw	1	0	0	CLK source select GG
0		SYS:LED:CLK_R	time_enum		,	rw				CLK source select R
0		SYS:LED:CLK_RR	time_double			rw		0	0	CLK source select RR
0		SYS:LED:TRG	time_enum		,	rw				LED TRG
0		SYS:LED:TRG:FAULT	time_double			rw		0	0	LED TRG
0		SYS:PSU:POT:VAN	time_double	V		rw	-13	0	0	
0		SYS:PSU:POT:VAP	time_double	V		rw	13	0	0	
0		SYS:REBOOT	time_long			rw		0	0	initiate a reboot
0		SYS:SYNC_ROLE	time_string			rw	rpmaster 20000000			SYNC ROLE selector
0		SYS:UPTIME	time_long			rw	435	0	0	Uptime in seconds
0		SYS:VADJ	time_double	V		rw	1.77	3	0	VADJ value
0		SYS:VAN	time_double			rw	-5.1	0	-15	sets main analog PSU N rail volts
0		SYS:VAP	time_double	V		rw	5.05	15	0	sets main analog PSU P rail volts

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
0		SYS:VERSION:FPGA	time_string			rw	216_08_08_08_08_08_08_9091_64B			Firmware FPGA version.
0		SYS:VERSION:SW	time_string			rw	acq400-449-20211117094532			Firmware SW version.
0		SYS:Z:TEMP	time_double	C		rw	54	99	0	ZYNQ die temperature
0	TEST_DESCR	TEST_DESCR	time_string			rw				Test Description: advertise what we are doing
0		TRANSIENT:ACT:DEMUX	time_long			rw		0	0	transient parameter :ACT:DEMUX
0		TRANSIENT:ACT:ERROR	time_string			rw				transient parameter :ACT:ERROR
0	USER	USER	time_string			rw				Show UUT user: stack your claim

Site **cA**
EpicsPV Prefix **acq2106_340:A:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cA	AGGREGATOR	AGGREGATOR	time_long			rw	16515073	0	0	define set of sites that contribute to this data stream
cA		AGGREGATOR:ON	time_enum		,	rw				
cA		AGGREGATOR:SITES	time_string			rw	1,2,3,4,5,6			
cA		AS:CRC:ACTIVE	time_double			rw		0	0	
cA		AS:CRC:COUNT	time_double			rw		0	0	
cA		AS:CRC:FREQ	time_double	Hz		rw		0	0	
cA		AS:CRC:RESET	time_enum		,	rw				
cA		AS:FE:ACTIVE	time_double			rw		0	0	
cA		AS:FE:COUNT	time_double			rw		0	0	
cA		AS:FE:FREQ	time_double	Hz		rw		0	0	
cA		AS:FE:RESET	time_enum		,	rw				
cA		AS:HE:ACTIVE	time_double			rw		0	0	
cA		AS:HE:COUNT	time_double			rw	1	0	0	
cA		AS:HE:FREQ	time_double	Hz		rw		0	0	
cA		AS:HE:RESET	time_enum		,	rw				
cA		AS:RTY:ACTIVE	time_double			rw		0	0	
cA		AS:RTY:COUNT	time_double			rw		0	0	
cA		AS:RTY:FREQ	time_double	Hz		rw		0	0	
cA		AS:RTY:RESET	time_enum		,	rw				
cA		AS:SE:ACTIVE	time_double			rw		0	0	
cA		AS:SE:COUNT	time_double			rw	1	0	0	
cA		AS:SE:FREQ	time_double	Hz		rw		0	0	
cA		AS:SE:RESET	time_enum		,	rw				
cA		AS:UPC:ACTIVE	time_double			rw		0	0	
cA		AS:UPC:COUNT	time_double			rw		0	0	
cA		AS:UPC:FREQ	time_double	Hz		rw		0	0	
cA		AS:UPC:RESET	time_enum		,	rw				
cA		AURORA:ERR	time_long			rw		0	0	

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cA		AURORA:UP	time_enum		DOWN, LANE UP, CHANNEL UP, UP	rw	DOWN			
cA	alat_avg					r	00000000 0 0			
cA	alat_min_max					r	0000ffff 0 65535			
cA	astats1					r	00010100 0 1 1 0			
cA	astats2					r	00000000 0 0			
cA	aurora_enable					rw				
cA	aurora_errors					r				
cA	aurora_lane_up					r				
cA	auto_dma					rw				
cA	clear_stats					rw				
cA	dev					r	236			
cA	dma_stat_data_pull					r	0 0			
cA	dma_stat_data_push					r	0 3			
cA	dma_stat_desc_pull					r	0 1			
cA	dma_stat_desc_push					r	0 1			
cA	enable					r	1			
cA		FIFO:DMA_STAT_DATA_PULL:FLAGS								
cA		FIFO:DMA_STAT_DATA_PUSH:FLAGS								
cA		FIFO:DMA_STAT_DESC_PULL:FLAGS								
cA		FIFO:DMA_STAT_DESC_PUSH:FLAGS								
cA	heartbeat					r	1820059572			
cA	ident					rw	0x21a60154			
cA	MANUFACTURER					r	D-TACQ Solutions			Show module Manufacturer
cA	MODEL					r	MGT482			Module MODEL
cA	MTYPE					r	90			Module Type Code
cA	NCHAN					r	4			Module number of channels
cA	name					r	mgt400.A			

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cA	PART_NUM					r	MGT482-SFP4 N=4 M=90			Module full part number
cA	prompt						None			prompt {0|1} changes command prompt
cA	pull_buffer_count					r				
cA	pull_buffer_count_lw					r				
cA	pull_status					r				
cA	push_buffer_count					r				
cA	push_buffer_count_lw					r				
cA	push_status					r				
cA	RW32_debug					rw				
cA	SERIAL					r	AM4820237			Module Serial Number
cA		SIG:MGT:HB:ACTIVE	time_double			rw	1.0	0	0	Signal Status MGT HB
cA		SIG:MGT:HB:COUNT	time_double			rw	2e+10	0	0	Signal Status MGT HB
cA		SIG:MGT:HB:FREQ	time_double	Hz		rw	1.5e+08	0	0	Signal Status MGT HB
cA		SIG:MGT:HB:RESET	time_enum		,	rw				Signal Status MGT HB
cA		SIG:MGT:PULL:BUFS:ACTIVE	time_double			rw		0	0	Signal Status MGT PULL
cA		SIG:MGT:PULL:BUFS:COUNT	time_double			rw		0	0	Signal Status MGT PULL
cA		SIG:MGT:PULL:BUFS:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PULL
cA		SIG:MGT:PULL:BUFS:RESET	time_enum		,	rw				Signal Status MGT PULL
cA		SIG:MGT:PULL:BYTES:ACTIVE	time_double			rw		0	0	Signal Status MGT PULL
cA		SIG:MGT:PULL:BYTES:COUNT	time_double			rw		0	0	Signal Status MGT PULL
cA		SIG:MGT:PULL:BYTES:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PULL
cA		SIG:MGT:PULL:BYTES:RESET	time_enum		,	rw				Signal Status MGT PULL
cA		SIG:MGT:PUSH:BUFS:ACTIVE	time_double			rw		0	0	Signal Status MGT PUSH
cA		SIG:MGT:PUSH:BUFS:COUNT	time_double			rw		0	0	Signal Status MGT PUSH
cA		SIG:MGT:PUSH:BUFS:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PUSH
cA		SIG:MGT:PUSH:BUFS:RESET	time_enum		,	rw				Signal Status MGT PUSH
cA		SIG:MGT:PUSH:BYTES:ACTIVE	time_double			rw		0	0	Signal Status MGT PUSH
cA		SIG:MGT:PUSH:BYTES:COUNT	time_double			rw		0	0	Signal Status MGT PUSH
cA		SIG:MGT:PUSH:BYTES:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PUSH

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cA		SIG:MGT:PUSH:BYTES:RESET	time_enum		,	rw				Signal Status MGT PUSH
cA	SIG_MGT_HB_ACTIVE	SIG:MGT:HB:ACTIVE	time_double			rx	1.0	0	0	
cA	SIG_MGT_HB_COUNT	SIG:MGT:HB:COUNT	time_double			rx	94181636599	0	0	
cA	SIG_MGT_HB_FREQ	SIG:MGT:HB:FREQ	time_double	Hz		rx	150006796.000	0	0	
cA	SIG_MGT_HB_RESET	SIG:MGT:HB:RESET	time_enum		,	rwx				
cA	SIG_MGT_PULL_BUFS_ACTIVE	SIG:MGT:PULL:BUFS:ACTIVE	time_double			rx	0.0	0	0	
cA	SIG_MGT_PULL_BUFS_COUNT	SIG:MGT:PULL:BUFS:COUNT	time_double			rx		0	0	
cA	SIG_MGT_PULL_BUFS_FREQ	SIG:MGT:PULL:BUFS:FREQ	time_double	Hz		rx	0.000	0	0	
cA	SIG_MGT_PULL_BUFS_RESET	SIG:MGT:PULL:BUFS:RESET	time_enum		,	rwx				
cA	SIG_MGT_PULL_BYTES_ACTIVE	SIG:MGT:PULL:BYTES:ACTIVE	time_double			rx	0.0	0	0	
cA	SIG_MGT_PULL_BYTES_COUNT	SIG:MGT:PULL:BYTES:COUNT	time_double			rx		0	0	
cA	SIG_MGT_PULL_BYTES_FREQ	SIG:MGT:PULL:BYTES:FREQ	time_double	Hz		rx	0.000	0	0	
cA	SIG_MGT_PULL_BYTES_RESET	SIG:MGT:PULL:BYTES:RESET	time_enum		,	rwx				
cA	SIG_MGT_PUSH_BUFS_ACTIVE	SIG:MGT:PUSH:BUFS:ACTIVE	time_double			rx	0.0	0	0	
cA	SIG_MGT_PUSH_BUFS_COUNT	SIG:MGT:PUSH:BUFS:COUNT	time_double			rx		0	0	
cA	SIG_MGT_PUSH_BUFS_FREQ	SIG:MGT:PUSH:BUFS:FREQ	time_double	Hz		rx	0.000	0	0	
cA	SIG_MGT_PUSH_BUFS_RESET	SIG:MGT:PUSH:BUFS:RESET	time_enum		,	rwx				
cA	SIG_MGT_PUSH_BYTES_ACTIVE	SIG:MGT:PUSH:BYTES:ACTIVE	time_double			rx	0.0	0	0	
cA	SIG_MGT_PUSH_BYTES_COUNT	SIG:MGT:PUSH:BYTES:COUNT	time_double			rx		0	0	
cA	SIG_MGT_PUSH_BYTES_FREQ	SIG:MGT:PUSH:BYTES:FREQ	time_double	Hz		rx	0.000	0	0	
cA	SIG_MGT_PUSH_BYTES_RESET	SIG:MGT:PUSH:BYTES:RESET	time_enum		,	rwx				
cA	uevent					rw	DRIVER=mgt400			

Site **cB**
EpicsPV Prefix **acq2106_340:B:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cB	AGGREGATOR	AGGREGATOR	time_long			rw	16515073	0	0	define set of sites that contribute to this data stream
cB		AGGREGATOR:ON	time_enum		,	rw				
cB		AGGREGATOR:SITES	time_string			rw	1,2,3,4,5,6			
cB		AS:CRC:ACTIVE	time_double			rw		0	0	
cB		AS:CRC:COUNT	time_double			rw		0	0	
cB		AS:CRC:FREQ	time_double	Hz		rw		0	0	
cB		AS:CRC:RESET	time_enum		,	rw				
cB		AS:FE:ACTIVE	time_double			rw		0	0	
cB		AS:FE:COUNT	time_double			rw		0	0	
cB		AS:FE:FREQ	time_double	Hz		rw		0	0	
cB		AS:FE:RESET	time_enum		,	rw				
cB		AS:HE:ACTIVE	time_double			rw		0	0	
cB		AS:HE:COUNT	time_double			rw	1	0	0	
cB		AS:HE:FREQ	time_double	Hz		rw		0	0	
cB		AS:HE:RESET	time_enum		,	rw				
cB		AS:RTY:ACTIVE	time_double			rw		0	0	
cB		AS:RTY:COUNT	time_double			rw		0	0	
cB		AS:RTY:FREQ	time_double	Hz		rw		0	0	
cB		AS:RTY:RESET	time_enum		,	rw				
cB		AS:SE:ACTIVE	time_double			rw		0	0	
cB		AS:SE:COUNT	time_double			rw	1	0	0	
cB		AS:SE:FREQ	time_double	Hz		rw		0	0	
cB		AS:SE:RESET	time_enum		,	rw				
cB		AS:UPC:ACTIVE	time_double			rw		0	0	
cB		AS:UPC:COUNT	time_double			rw		0	0	
cB		AS:UPC:FREQ	time_double	Hz		rw		0	0	
cB		AS:UPC:RESET	time_enum		,	rw				
cB		AURORA:ERR	time_long			rw		0	0	

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cB		AURORA:UP	time_enum		DOWN, LANE UP, CHANNEL UP, UP	rw	DOWN			
cB	alat_avg					r	00000000 0 0			
cB	alat_min_max					r	0000ffff 0 65535			
cB	astats1					r	00010100 0 1 1 0			
cB	astats2					r	00000000 0 0			
cB	aurora_enable					rw				
cB	aurora_errors					r				
cB	aurora_lane_up					r				
cB	auto_dma					rw				
cB	clear_stats					rw				
cB	dev					r	237			
cB	dma_stat_data_pull					r	0 0			
cB	dma_stat_data_push					r	0 3			
cB	dma_stat_desc_pull					r	0 1			
cB	dma_stat_desc_push					r	0 1			
cB	enable					r	1			
cB		FIFO:DMA_STAT_DATA_PULL:FLAGS								
cB		FIFO:DMA_STAT_DATA_PUSH:FLAGS								
cB		FIFO:DMA_STAT_DESC_PULL:FLAGS								
cB		FIFO:DMA_STAT_DESC_PUSH:FLAGS								
cB	heartbeat					r	1790565944			
cB	ident					rw	0x21b60154			
cB	MANUFACTURER					r	D-TACQ Solutions			Show module Manufacturer
cB	MODEL					r	MGT482			Module MODEL
cB	MTYPE					r	90			Module Type Code
cB	NCHAN					r	4			Module number of channels
cB	name					r	mgt400.B			

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cB	PART_NUM					r	MGT482-SFP4 N=4 M=90			Module full part number
cB	prompt						None			prompt {0|1} changes command prompt
cB	pull_buffer_count					r				
cB	pull_buffer_count_lw					r				
cB	pull_status					r				
cB	push_buffer_count					r				
cB	push_buffer_count_lw					r				
cB	push_status					r				
cB	RW32_debug					rw				
cB	SERIAL					r	AM4820237			Module Serial Number
cB		SIG:MGT:HB:ACTIVE	time_double			rw	1.0	0	0	Signal Status MGT HB
cB		SIG:MGT:HB:COUNT	time_double			rw	2e+10	0	0	Signal Status MGT HB
cB		SIG:MGT:HB:FREQ	time_double	Hz		rw	1.49e+08	0	0	Signal Status MGT HB
cB		SIG:MGT:HB:RESET	time_enum		,	rw				Signal Status MGT HB
cB		SIG:MGT:PULL:BUFS:ACTIVE	time_double			rw		0	0	Signal Status MGT PULL
cB		SIG:MGT:PULL:BUFS:COUNT	time_double			rw		0	0	Signal Status MGT PULL
cB		SIG:MGT:PULL:BUFS:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PULL
cB		SIG:MGT:PULL:BUFS:RESET	time_enum		,	rw				Signal Status MGT PULL
cB		SIG:MGT:PULL:BYTES:ACTIVE	time_double			rw		0	0	Signal Status MGT PULL
cB		SIG:MGT:PULL:BYTES:COUNT	time_double			rw		0	0	Signal Status MGT PULL
cB		SIG:MGT:PULL:BYTES:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PULL
cB		SIG:MGT:PULL:BYTES:RESET	time_enum		,	rw				Signal Status MGT PULL
cB		SIG:MGT:PUSH:BUFS:ACTIVE	time_double			rw		0	0	Signal Status MGT PUSH
cB		SIG:MGT:PUSH:BUFS:COUNT	time_double			rw		0	0	Signal Status MGT PUSH
cB		SIG:MGT:PUSH:BUFS:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PUSH
cB		SIG:MGT:PUSH:BUFS:RESET	time_enum		,	rw				Signal Status MGT PUSH
cB		SIG:MGT:PUSH:BYTES:ACTIVE	time_double			rw		0	0	Signal Status MGT PUSH
cB		SIG:MGT:PUSH:BYTES:COUNT	time_double			rw		0	0	Signal Status MGT PUSH
cB		SIG:MGT:PUSH:BYTES:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PUSH

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cB		SIG:MGT:PUSH:BYTES:RESET	time_enum		,	rw				Signal Status MGT PUSH
cB	SIG_MGT_HB_ACTIVE	SIG:MGT:HB:ACTIVE	time_double			rx	1.0	0	0	
cB	SIG_MGT_HB_COUNT	SIG:MGT:HB:COUNT	time_double			rx	92006325917	0	0	
cB	SIG_MGT_HB_FREQ	SIG:MGT:HB:FREQ	time_double	Hz		rx	149953530.000	0	0	
cB	SIG_MGT_HB_RESET	SIG:MGT:HB:RESET	time_enum		,	rwx				
cB	SIG_MGT_PULL_BUFS_ACTIVE	SIG:MGT:PULL:BUFS:ACTIVE	time_double			rx	0.0	0	0	
cB	SIG_MGT_PULL_BUFS_COUNT	SIG:MGT:PULL:BUFS:COUNT	time_double			rx		0	0	
cB	SIG_MGT_PULL_BUFS_FREQ	SIG:MGT:PULL:BUFS:FREQ	time_double	Hz		rx	0.000	0	0	
cB	SIG_MGT_PULL_BUFS_RESET	SIG:MGT:PULL:BUFS:RESET	time_enum		,	rwx				
cB	SIG_MGT_PULL_BYTES_ACTIVE	SIG:MGT:PULL:BYTES:ACTIVE	time_double			rx	0.0	0	0	
cB	SIG_MGT_PULL_BYTES_COUNT	SIG:MGT:PULL:BYTES:COUNT	time_double			rx		0	0	
cB	SIG_MGT_PULL_BYTES_FREQ	SIG:MGT:PULL:BYTES:FREQ	time_double	Hz		rx	0.000	0	0	
cB	SIG_MGT_PULL_BYTES_RESET	SIG:MGT:PULL:BYTES:RESET	time_enum		,	rwx				
cB	SIG_MGT_PUSH_BUFS_ACTIVE	SIG:MGT:PUSH:BUFS:ACTIVE	time_double			rx	0.0	0	0	
cB	SIG_MGT_PUSH_BUFS_COUNT	SIG:MGT:PUSH:BUFS:COUNT	time_double			rx		0	0	
cB	SIG_MGT_PUSH_BUFS_FREQ	SIG:MGT:PUSH:BUFS:FREQ	time_double	Hz		rx	0.000	0	0	
cB	SIG_MGT_PUSH_BUFS_RESET	SIG:MGT:PUSH:BUFS:RESET	time_enum		,	rwx				
cB	SIG_MGT_PUSH_BYTES_ACTIVE	SIG:MGT:PUSH:BYTES:ACTIVE	time_double			rx	0.0	0	0	
cB	SIG_MGT_PUSH_BYTES_COUNT	SIG:MGT:PUSH:BYTES:COUNT	time_double			rx		0	0	
cB	SIG_MGT_PUSH_BYTES_FREQ	SIG:MGT:PUSH:BYTES:FREQ	time_double	Hz		rx	0.000	0	0	
cB	SIG_MGT_PUSH_BYTES_RESET	SIG:MGT:PUSH:BYTES:RESET	time_enum		,	rwx				
cB	uevent					rw	DRIVER=mgt400			

Site **cC**
EpicsPV Prefix **acq2106_340:C:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cC	AGGREGATOR	AGGREGATOR	time_long			rw	16515073	0	0	define set of sites that contribute to this data stream
cC		AGGREGATOR:ON	time_enum		,	rw				
cC		AGGREGATOR:SITES	time_string			rw	1,2,3,4,5,6			
cC		AS:CRC:ACTIVE	time_double			rw		0	0	
cC		AS:CRC:COUNT	time_double			rw		0	0	
cC		AS:CRC:FREQ	time_double	Hz		rw		0	0	
cC		AS:CRC:RESET	time_enum		,	rw				
cC		AS:FE:ACTIVE	time_double			rw		0	0	
cC		AS:FE:COUNT	time_double			rw		0	0	
cC		AS:FE:FREQ	time_double	Hz		rw		0	0	
cC		AS:FE:RESET	time_enum		,	rw				
cC		AS:HE:ACTIVE	time_double			rw		0	0	
cC		AS:HE:COUNT	time_double			rw	1	0	0	
cC		AS:HE:FREQ	time_double	Hz		rw		0	0	
cC		AS:HE:RESET	time_enum		,	rw				
cC		AS:RTY:ACTIVE	time_double			rw		0	0	
cC		AS:RTY:COUNT	time_double			rw		0	0	
cC		AS:RTY:FREQ	time_double	Hz		rw		0	0	
cC		AS:RTY:RESET	time_enum		,	rw				
cC		AS:SE:ACTIVE	time_double			rw		0	0	
cC		AS:SE:COUNT	time_double			rw	1	0	0	
cC		AS:SE:FREQ	time_double	Hz		rw		0	0	
cC		AS:SE:RESET	time_enum		,	rw				
cC		AS:UPC:ACTIVE	time_double			rw		0	0	
cC		AS:UPC:COUNT	time_double			rw		0	0	
cC		AS:UPC:FREQ	time_double	Hz		rw		0	0	
cC		AS:UPC:RESET	time_enum		,	rw				
cC		AURORA:ERR	time_long			rw		0	0	

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cC		AURORA:UP	time_enum		DOWN, LANE UP, CHANNEL UP, UP	rw	DOWN			
cC		FIFO:DMA_STAT_DATA_PULL:FLAGS								
cC		FIFO:DMA_STAT_DATA_PUSH:FLAGS								
cC		FIFO:DMA_STAT_DESC_PULL:FLAGS								
cC		FIFO:DMA_STAT_DESC_PUSH:FLAGS								
cC		SIG:MGT:HB:ACTIVE	time_double			rw	1.0	0	0	Signal Status MGT HB
cC		SIG:MGT:HB:COUNT	time_double			rw	2e+10	0	0	Signal Status MGT HB
cC		SIG:MGT:HB:FREQ	time_double	Hz		rw	1.54e+08	0	0	Signal Status MGT HB
cC		SIG:MGT:HB:RESET	time_enum		,	rw				Signal Status MGT HB
cC		SIG:MGT:PULL:BUFS:ACTIVE	time_double			rw		0	0	Signal Status MGT PULL
cC		SIG:MGT:PULL:BUFS:COUNT	time_double			rw		0	0	Signal Status MGT PULL
cC		SIG:MGT:PULL:BUFS:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PULL
cC		SIG:MGT:PULL:BUFS:RESET	time_enum		,	rw				Signal Status MGT PULL
cC		SIG:MGT:PULL:BYTES:ACTIVE	time_double			rw		0	0	Signal Status MGT PULL
cC		SIG:MGT:PULL:BYTES:COUNT	time_double			rw		0	0	Signal Status MGT PULL
cC		SIG:MGT:PULL:BYTES:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PULL
cC		SIG:MGT:PULL:BYTES:RESET	time_enum		,	rw				Signal Status MGT PULL
cC		SIG:MGT:PUSH:BUFS:ACTIVE	time_double			rw		0	0	Signal Status MGT PUSH
cC		SIG:MGT:PUSH:BUFS:COUNT	time_double			rw		0	0	Signal Status MGT PUSH
cC		SIG:MGT:PUSH:BUFS:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PUSH
cC		SIG:MGT:PUSH:BUFS:RESET	time_enum		,	rw				Signal Status MGT PUSH
cC		SIG:MGT:PUSH:BYTES:ACTIVE	time_double			rw		0	0	Signal Status MGT PUSH
cC		SIG:MGT:PUSH:BYTES:COUNT	time_double			rw		0	0	Signal Status MGT PUSH
cC		SIG:MGT:PUSH:BYTES:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PUSH
cC		SIG:MGT:PUSH:BYTES:RESET	time_enum		,	rw				Signal Status MGT PUSH

Site **cD**
EpicsPV Prefix **acq2106_340:D:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cD	AGGREGATOR	AGGREGATOR	time_long			rw	16515073	0	0	define set of sites that contribute to this data stream
cD		AGGREGATOR:ON	time_enum		,	rw				
cD		AGGREGATOR:SITES	time_string			rw	1,2,3,4,5,6			
cD		AS:CRC:ACTIVE	time_double			rw		0	0	
cD		AS:CRC:COUNT	time_double			rw		0	0	
cD		AS:CRC:FREQ	time_double	Hz		rw		0	0	
cD		AS:CRC:RESET	time_enum		,	rw				
cD		AS:FE:ACTIVE	time_double			rw		0	0	
cD		AS:FE:COUNT	time_double			rw		0	0	
cD		AS:FE:FREQ	time_double	Hz		rw		0	0	
cD		AS:FE:RESET	time_enum		,	rw				
cD		AS:HE:ACTIVE	time_double			rw		0	0	
cD		AS:HE:COUNT	time_double			rw	1	0	0	
cD		AS:HE:FREQ	time_double	Hz		rw		0	0	
cD		AS:HE:RESET	time_enum		,	rw				
cD		AS:RTY:ACTIVE	time_double			rw		0	0	
cD		AS:RTY:COUNT	time_double			rw		0	0	
cD		AS:RTY:FREQ	time_double	Hz		rw		0	0	
cD		AS:RTY:RESET	time_enum		,	rw				
cD		AS:SE:ACTIVE	time_double			rw		0	0	
cD		AS:SE:COUNT	time_double			rw	1	0	0	
cD		AS:SE:FREQ	time_double	Hz		rw		0	0	
cD		AS:SE:RESET	time_enum		,	rw				
cD		AS:UPC:ACTIVE	time_double			rw		0	0	
cD		AS:UPC:COUNT	time_double			rw		0	0	
cD		AS:UPC:FREQ	time_double	Hz		rw		0	0	
cD		AS:UPC:RESET	time_enum		,	rw				
cD		AURORA:ERR	time_long			rw		0	0	

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
cD		AURORA:UP	time_enum		DOWN, LANE UP, CHANNEL UP, UP	rw	DOWN			
cD		FIFO:DMA_STAT_DATA_PULL:FLAGS								
cD		FIFO:DMA_STAT_DATA_PUSH:FLAGS								
cD		FIFO:DMA_STAT_DESC_PULL:FLAGS								
cD		FIFO:DMA_STAT_DESC_PUSH:FLAGS								
cD		SIG:MGT:HB:ACTIVE	time_double			rw	1.0	0	0	Signal Status MGT HB
cD		SIG:MGT:HB:COUNT	time_double			rw	2e+10	0	0	Signal Status MGT HB
cD		SIG:MGT:HB:FREQ	time_double	Hz		rw	1.5e+08	0	0	Signal Status MGT HB
cD		SIG:MGT:HB:RESET	time_enum		,	rw				Signal Status MGT HB
cD		SIG:MGT:PULL:BUFS:ACTIVE	time_double			rw		0	0	Signal Status MGT PULL
cD		SIG:MGT:PULL:BUFS:COUNT	time_double			rw		0	0	Signal Status MGT PULL
cD		SIG:MGT:PULL:BUFS:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PULL
cD		SIG:MGT:PULL:BUFS:RESET	time_enum		,	rw				Signal Status MGT PULL
cD		SIG:MGT:PULL:BYTES:ACTIVE	time_double			rw		0	0	Signal Status MGT PULL
cD		SIG:MGT:PULL:BYTES:COUNT	time_double			rw		0	0	Signal Status MGT PULL
cD		SIG:MGT:PULL:BYTES:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PULL
cD		SIG:MGT:PULL:BYTES:RESET	time_enum		,	rw				Signal Status MGT PULL
cD		SIG:MGT:PUSH:BUFS:ACTIVE	time_double			rw		0	0	Signal Status MGT PUSH
cD		SIG:MGT:PUSH:BUFS:COUNT	time_double			rw		0	0	Signal Status MGT PUSH
cD		SIG:MGT:PUSH:BUFS:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PUSH
cD		SIG:MGT:PUSH:BUFS:RESET	time_enum		,	rw				Signal Status MGT PUSH
cD		SIG:MGT:PUSH:BYTES:ACTIVE	time_double			rw		0	0	Signal Status MGT PUSH
cD		SIG:MGT:PUSH:BYTES:COUNT	time_double			rw		0	0	Signal Status MGT PUSH
cD		SIG:MGT:PUSH:BYTES:FREQ	time_double	Hz		rw		0	0	Signal Status MGT PUSH
cD		SIG:MGT:PUSH:BYTES:RESET	time_enum		,	rw				Signal Status MGT PUSH

Site **s0**
EpicsPV Prefix **acq2106_340:0:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lo
s0	AGGREGATOR	AGGREGATOR	time_long			rw	16547840	0	
s0		AGGREGATOR:ON	time_enum		0, 1	rw			
s0		AGGREGATOR:SITES	time_string			rw	1,2,3,4,5,6		
s0	acq480_force_training					rw	1		
s0	awg_abort					rwx			
s0	CONTINUOUS					rwx	CONTINUOUS stop		
s0	CONTINUOUS_STATE	CONTINUOUS:STATE				rwx	IDLE		
s0	CONTINUOUS_STATUS	CONTINUOUS:STATUS				rwx			
s0	COUNTER_LATCH	COUNTER_LATCH	time_enum		0, 1	rw			
s0		COUNTER_LATCH:REQ	time_enum		0, 1	rw			
s0	channel_mapping					rwx	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,25,26,27,28,29,30,31,32,33,34,35,36,37,38,39,40,41,42,4		
s0	counter_latch					rw			
s0	DISTRIBUTOR	DISTRIBUTOR	time_long			rw		0	
s0		DISTRIBUTOR:COMMS	time_enum		CPU, COMMSB, COMMSA, CPU	rw	CPU		
s0		DISTRIBUTOR:ON	time_enum		0, 1	rw			
s0		DISTRIBUTOR:PAD	time_long			rw		0	
s0		DISTRIBUTOR:SITES	time_string			rw	none		
s0	DSSB	DSSB	time_long			rw	4	0	
s0	data32					rw			
s0	data_engine_2					rw	reg=0x000007fd sites=1,2,3 aggregator=0 DATA_MOVER_EN=on		
s0	data_engine_3					rw	reg=0x00000000 sites=none aggregator=0 DATA_MOVER_EN=off		

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	di4_2					rw				di4 .. 4 bit direct digital input bit 2
s0	di4_3					rw				di4 .. 4 bit direct digital input bit 3
s0	do4_0					rw				do4 .. 4 bit direct digital output bit 0
s0	do4_1					rw				do4 .. 4 bit direct digital output bit 1
s0	do4_2					rw				do4 .. 4 bit direct digital output bit 2
s0	do4_3					rw				do4 .. 4 bit direct digital output bit 3
s0	evt_src_d0					rw				event source d0
s0	evt_src_d1					rw				event source d1
s0	evt_src_d2					rw				event source d2
s0	evt_src_d3					rw				event source d3
s0	evt_src_d4					rw				event source d4
s0	evt_src_d5					rw				event source d5
s0	evt_src_d6					rw				event source d6
s0	evt_src_d7					rw				event source d7
s0	GPG_CLK	GPG_CLK	time_enum		internal, external	rw	internal			Initial Value: GPG_CLK internal
s0	GPG_CLK_DX	GPG_CLK:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			GPG Gate Pulse Generator: Clock select DX
s0	GPG_CLK_SENSE	GPG_CLK:SENSE	time_enum		falling, rising	rw	falling			GPG Gate Pulse Generator: Clock select SENSE
s0	GPG_DBG_CTR	GPG:DBG:CTR	time_long			rw		0	0	GPG Gate Pulse Generator Status CTR
s0	GPG_DBG_DEF	GPG:DBG:DEF	time_string			rw	ERROR:pulse_def" not found			GPG Gate Pulse Generator Status DEF
s0	GPG_DBG_FIN	GPG:DBG:FIN	time_long			rw		0	0	GPG Gate Pulse Generator Status FIN
s0	GPG_DBG_GPGTOP	GPG:DBG:GPGTOP	time_long			rw	2	0	0	GPG Gate Pulse Generator Status GPGTOP

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	GPG_DBG_OSTATE	GPG:DBG:OSTATE	time_long			rw		0	0	GPG Gate Pulse Generator Status OSTATE
s0	GPG_DBG_PTR	GPG:DBG:PTR	time_long			rw		0	0	GPG Gate Pulse Generator Status PTR
s0	GPG_DBG_RSTATE	GPG:DBG:RSTATE	time_long			rw		0	0	GPG Gate Pulse Generator Status RSTATE
s0	GPG_DBG_STATE	GPG:DBG:STATE	time_enum		IDLE, RESET_CNT, WAIT_TRG, WAIT_SYN, RUN	rw	IDLE			GPG Gate Pulse Generator Status STATE
s0	GPG_DBG_UBX	GPG:DBG:UBX	time_long			rw		0	0	GPG Gate Pulse Generator Status UBX
s0	GPG_DBG_UNTIL	GPG:DBG:UNTIL	time_long			rw		0	0	GPG Gate Pulse Generator Status UNTIL
s0	GPG_ENABLE	GPG:ENABLE	time_enum		disabled, enabled	rw	disabled			GPG Gate Pulse Generator: ENABLE
s0	GPG_MODE	GPG:MODE	time_enum		ONCE, n/a, LOOP, LOOPWAIT	rw	ONCE			GPG Gate Pulse Generator: MODE set
s0	GPG_SYNC	GPG_SYNC	time_enum		internal, external	rw	internal			GPG Gate Pulse Generator: SYNC select
s0	GPG_SYNC_DX	GPG_SYNC:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			GPG Gate Pulse Generator: SYNC select DX
s0	GPG_SYNC_SENSE	GPG_SYNC:SENSE	time_enum		falling, rising	rw	falling			GPG Gate Pulse Generator: SYNC select SENSE
s0	GPG_TRG	GPG_TRG	time_enum		internal, external	rw	internal			GPG Gate Pulse Generator: TRG select
s0	GPG_TRG_DX	GPG_TRG:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			GPG Gate Pulse Generator: TRG select DX
s0	GPG_TRG_SENSE	GPG_TRG:SENSE	time_enum		falling, rising	rw	falling			GPG Gate Pulse Generator: TRG select SENSE
s0	HN					rw	acq2106_340			Host Name
s0	has_dsp					rw	none			FPGA has DSP capability
s0	has_mgt					rw	12 13			FPGA has MGT (Multi Gigabit Transceiver) support

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	has_mgtdram					rw	none			
s0	has_wr					rw	none			FPGA has White Rabbit support
s0	IOC_READY					rx	IOC_READY 1			EPICS IO Controller Ready
s0	is_tiga					rw	none			
s0	LLC_instrument_latency					rwx				
s0	load.si5326					rwx	load.si5326 [-o file] regmap			
s0	MODEL	MODEL	time_string			rw	acq2106sfp			Module MODEL
s0	MTYPE	MTYPE	time_string			rw	acq2106			Module Type Code
s0	NCHAN	NCHAN	time_long			rw	48			Module number of channels
s0	OVERSAMPLING					rw	-1			Oversampling is enabled
s0	PART_NUM	PART_NUM	time_string			rw	acq2106sfp			Module full part number
s0	PCOMP	PCOMP	time_double			rw		0	0	
s0	play0					rwx	using default sitelist 1,2,3,4,5,6,C sitelist: 1,2,3,4,5,6,C sites: 1 2 3 4 5 6 C site:1 d32: ncc:8 d32c:0 d16c:8 AGIX=0			configure distributor for AO, DO
s0	prompt						None			prompt {0|1} changes command prompt
s0		QS:BUSY	time_double			rw		0	0	Queue Status BUSY indication of system loading
s0		QS:DROPS	time_long			rw	2	0	0	Queue Status DROPS indication of system loading
s0		QS:EMPTY	time_long			rw	126	0	0	Queue Status EMPTY indication of system loading
s0		QS:FILLING	time_long			rw		0	0	Queue Status FILLING indication of system loading
s0		QS:FULL	time_long			rw		0	0	Queue Status FULL indication of system loading
s0		QS:FULL_APP	time_long			rw		0	0	Queue Status FULL_APP indication of system loading
s0		QS:XOFF	time_enum		OFF, ON	rw	OFF			Queue Status XOFF indication of system loading
s0	ROLE	ROLE	time_string			rw	MOTHERBOARD			
s0	RTM_FIT	RTM_FIT	time_long			rw		0	0	Repeating Transient Mode FIT control
s0	rt_status					r				

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	run0					rwx	using default sitelist 1,2,3,4,5,6,C sitelist: 1,2,3,4,5,6,C sites: 1 2 3 4 5 6 C site:1 d32: ncc:8 d32c:0 d16c:8 AGIX=0			configure active sitelist on boot. MUST be in /mnt/local/sysconfig/transient.init
s0	SERIAL	SERIAL	time_string			rw	CE4160340			Module Serial Number
s0	SIG_CLK_EXT_ACTIVE	SIG:CLK_EXT:ACTIVE	time_double			rw	1.0	0	0	CLK counter signal EXT value ACTIVE
s0	SIG_CLK_EXT_COUNT	SIG:CLK_EXT:COUNT	time_double			rw	3e+08	0	0	CLK counter signal EXT value COUNT
s0	SIG_CLK_EXT_FREQ	SIG:CLK_EXT:FREQ	time_double	Hz		rw	1e+06	0	0	CLK counter signal EXT value FREQ
s0	SIG_CLK_EXT_RESET	SIG:CLK_EXT:RESET	time_enum		,	rw				CLK counter signal EXT value RESET
s0	SIG_CLK_MB_ACTIVE	SIG:CLK_MB:ACTIVE	time_double			rw	1.0	0	0	CLK counter signal MB value ACTIVE
s0	SIG_CLK_MB_COUNT	SIG:CLK_MB:COUNT	time_double			rw	6e+09	0	0	CLK counter signal MB value COUNT
s0	SIG_CLK_MB_FIN	SIG:CLK_MB:FIN	time_long	Hz		rw	1000000	200000000	0	User specified seed clock value for MB CLK
s0	SIG_CLK_MB_FREQ	SIG:CLK_MB:FREQ	time_double	Hz		rw	2e+07	0	0	CLK counter signal MB value FREQ
s0	SIG_CLK_MB_READY	SIG:CLK_MB:READY	time_enum		0, 1	rw	1			CLK counter signal MB value READY
s0	SIG_CLK_MB_RESET	SIG:CLK_MB:RESET	time_enum		,	rw				CLK counter signal MB value RESET
s0	SIG_CLK_MB_SET	SIG:CLK_MB:SET	time_long	Hz		rw	20000000	100000000	1000000	CLK counter signal MB value SET
s0	SIG_CLK_S1_ACTIVE	SIG:CLK_S1:ACTIVE	time_double			rw	1.0	0	0	CLK counter signal S1 value ACTIVE
s0	SIG_CLK_S1_COUNT	SIG:CLK_S1:COUNT	time_double			rw	6e+09	0	0	CLK counter signal S1 value COUNT
s0	SIG_CLK_S1_FREQ	SIG:CLK_S1:FREQ	time_double	Hz		rw	2e+07	0	0	CLK counter signal S1 value FREQ
s0	SIG_CLK_S1_RESET	SIG:CLK_S1:RESET	time_enum		,	rw				CLK counter signal S1 value RESET
s0	SIG_CLK_S2_ACTIVE	SIG:CLK_S2:ACTIVE	time_double			rw	1.0	0	0	CLK counter signal S2 value ACTIVE
s0	SIG_CLK_S2_COUNT	SIG:CLK_S2:COUNT	time_double			rw	6e+09	0	0	CLK counter signal S2 value COUNT

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SIG_CLK_S2_FREQ	SIG:CLK_S2:FREQ	time_double	Hz		rw	2e+07	0	0	CLK counter signal S2 value FREQ
s0	SIG_CLK_S2_RESET	SIG:CLK_S2:RESET	time_enum		,	rw				CLK counter signal S2 value RESET
s0	SIG_CLK_S3_ACTIVE	SIG:CLK_S3:ACTIVE	time_double			rw	1.0	0	0	CLK source select S3_ACTIVE
s0	SIG_CLK_S3_COUNT	SIG:CLK_S3:COUNT	time_double			rw	6e+09	0	0	CLK source select S3_COUNT
s0	SIG_CLK_S3_FREQ	SIG:CLK_S3:FREQ	time_double	Hz		rw	2e+07	0	0	CLK source select S3_FREQ
s0	SIG_CLK_S3_RESET	SIG:CLK_S3:RESET	time_enum		,	rw				CLK source select S3_RESET
s0	SIG_CLK_S4_ACTIVE	SIG:CLK_S4:ACTIVE	time_double			rw	1.0	0	0	CLK source select S4_ACTIVE
s0	SIG_CLK_S4_COUNT	SIG:CLK_S4:COUNT	time_double			rw	6e+09	0	0	CLK source select S4_COUNT
s0	SIG_CLK_S4_FREQ	SIG:CLK_S4:FREQ	time_double	Hz		rw	2e+07	0	0	CLK source select S4_FREQ
s0	SIG_CLK_S4_RESET	SIG:CLK_S4:RESET	time_enum		,	rw				CLK source select S4_RESET
s0	SIG_CLK_S5_ACTIVE	SIG:CLK_S5:ACTIVE	time_double			rw	1.0	0	0	CLK source select S5_ACTIVE
s0	SIG_CLK_S5_COUNT	SIG:CLK_S5:COUNT	time_double			rw	6e+09	0	0	CLK source select S5_COUNT
s0	SIG_CLK_S5_FREQ	SIG:CLK_S5:FREQ	time_double	Hz		rw	2e+07	0	0	CLK source select S5_FREQ
s0	SIG_CLK_S5_RESET	SIG:CLK_S5:RESET	time_enum		,	rw				CLK source select S5_RESET
s0	SIG_CLK_S6_ACTIVE	SIG:CLK_S6:ACTIVE	time_double			rw	1.0	0	0	CLK source select S6_ACTIVE
s0	SIG_CLK_S6_COUNT	SIG:CLK_S6:COUNT	time_double			rw	6e+09	0	0	CLK source select S6_COUNT
s0	SIG_CLK_S6_FREQ	SIG:CLK_S6:FREQ	time_double	Hz		rw	2e+07	0	0	CLK source select S6_FREQ
s0	SIG_CLK_S6_RESET	SIG:CLK_S6:RESET	time_enum		,	rw				CLK source select S6_RESET
s0	SIG_CLK_TRAIN_REQ	SIG:CLK:TRAIN_REQ	time_enum		0, 1	rw	1			Clock Training Request

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SIG_EVENT_SRC_0	SIG:EVENT_SRC:0	time_enum		TRG, GPG, FP_GPIO, MOD	rw	TRG			Select Event Source d0
s0	SIG_EVENT_SRC_1	SIG:EVENT_SRC:1	time_enum		TRG, GPG, HDMI_GPIO, MOD	rw	TRG			Select Event Source d1
s0	SIG_EVENT_SRC_2	SIG:EVENT_SRC:2	time_enum		TRG, GPG, HDMI_TRG, MOD	rw	TRG			Select Event Source d2
s0	SIG_EVENT_SRC_3	SIG:EVENT_SRC:3	time_enum		TRG, GPG, HDMI_SYNC, MOD	rw	TRG			Select Event Source d3
s0	SIG_EVENT_SRC_4	SIG:EVENT_SRC:4	time_enum		TRG, GPG, FP_GPIO, MOD	rw	TRG			Select Event Source d4
s0	SIG_EVENT_SRC_5	SIG:EVENT_SRC:5	time_enum		TRG, GPG, HDMI_GPIO, MOD	rw	TRG			Select Event Source d5
s0	SIG_EVENT_SRC_6	SIG:EVENT_SRC:6	time_enum		TRG, GPG, HDMI_TRG, MOD	rw	TRG			Select Event Source d6
s0	SIG_EVENT_SRC_7	SIG:EVENT_SRC:7	time_enum		TRG, GPG, HDMI_SYNC, MOD	rw	TRG			Select Event Source d7
s0	SIG_EVT_EXT_ACTIVE	SIG:EVT_EXT:ACTIVE	time_double			rw	1.0	0	0	Event Counter Type EXT Value ACTIVE
s0	SIG_EVT_EXT_COUNT	SIG:EVT_EXT:COUNT	time_double			rw	3e+05	0	0	Event Counter Type EXT Value COUNT
s0	SIG_EVT_EXT_FREQ	SIG:EVT_EXT:FREQ	time_double	Hz		rw	1000.000	0	0	Event Counter Type EXT Value FREQ
s0	SIG_EVT_EXT_RESET	SIG:EVT_EXT:RESET	time_enum		,	rw				Event Counter Type EXT Value RESET
s0	SIG_EVT_MB_ACTIVE	SIG:EVT_MB:ACTIVE	time_double			rw	0.0	0	0	Event Counter Type MB Value ACTIVE
s0	SIG_EVT_MB_COUNT	SIG:EVT_MB:COUNT	time_double			rw		0	0	Event Counter Type MB Value COUNT
s0	SIG_EVT_MB_FREQ	SIG:EVT_MB:FREQ	time_double	Hz		rw	0.000	0	0	Event Counter Type MB Value FREQ

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SIG_EVT_MB_RESET	SIG:EVT_MB:RESET	time_enum		,	rw				Event Counter Type MB Value RESET
s0	SIG_EVT_S1_ACTIVE	SIG:EVT_S1:ACTIVE	time_double			rw	0.0	0	0	Event Counter Type S1 Value ACTIVE
s0	SIG_EVT_S1_COUNT	SIG:EVT_S1:COUNT	time_double			rw		0	0	Event Counter Type S1 Value COUNT
s0	SIG_EVT_S1_FREQ	SIG:EVT_S1:FREQ	time_double	Hz		rw	0.000	0	0	Event Counter Type S1 Value FREQ
s0	SIG_EVT_S1_RESET	SIG:EVT_S1:RESET	time_enum		,	rw				Event Counter Type S1 Value RESET
s0	SIG_EVT_S2_ACTIVE	SIG:EVT_S2:ACTIVE	time_double			rw	0.0	0	0	Event Counter Type S2 Value ACTIVE
s0	SIG_EVT_S2_COUNT	SIG:EVT_S2:COUNT	time_double			rw		0	0	Event Counter Type S2 Value COUNT
s0	SIG_EVT_S2_FREQ	SIG:EVT_S2:FREQ	time_double	Hz		rw	0.000	0	0	Event Counter Type S2 Value FREQ
s0	SIG_EVT_S2_RESET	SIG:EVT_S2:RESET	time_enum		,	rw				Event Counter Type S2 Value RESET
s0	SIG_EVT_S3_ACTIVE	SIG:EVT_S3:ACTIVE	time_double			rw	0.0	0	0	Event Counter Type S3 Value ACTIVE
s0	SIG_EVT_S3_COUNT	SIG:EVT_S3:COUNT	time_double			rw		0	0	Event Counter Type S3 Value COUNT
s0	SIG_EVT_S3_FREQ	SIG:EVT_S3:FREQ	time_double	Hz		rw	0.000	0	0	Event Counter Type S3 Value FREQ
s0	SIG_EVT_S3_RESET	SIG:EVT_S3:RESET	time_enum		,	rw				Event Counter Type S3 Value RESET
s0	SIG_EVT_S4_ACTIVE	SIG:EVT_S4:ACTIVE	time_double			rw	0.0	0	0	Event Counter Type S4 Value ACTIVE
s0	SIG_EVT_S4_COUNT	SIG:EVT_S4:COUNT	time_double			rw		0	0	Event Counter Type S4 Value COUNT
s0	SIG_EVT_S4_FREQ	SIG:EVT_S4:FREQ	time_double	Hz		rw	0.000	0	0	Event Counter Type S4 Value FREQ
s0	SIG_EVT_S4_RESET	SIG:EVT_S4:RESET	time_enum		,	rw				Event Counter Type S4 Value RESET
s0	SIG_EVT_S5_ACTIVE	SIG:EVT_S5:ACTIVE	time_double			rw	0.0	0	0	Event Counter Type S5 Value ACTIVE
s0	SIG_EVT_S5_COUNT	SIG:EVT_S5:COUNT	time_double			rw		0	0	Event Counter Type S5 Value COUNT

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SIG_EVT_S5_FREQ	SIG:EVT_S5:FREQ	time_double	Hz		rw	0.000	0	0	Event Counter Type S5 Value FREQ
s0	SIG_EVT_S5_RESET	SIG:EVT_S5:RESET	time_enum		,	rw				Event Counter Type S5 Value RESET
s0	SIG_EVT_S6_ACTIVE	SIG:EVT_S6:ACTIVE	time_double			rw	0.0	0	0	Event Counter Type S6 Value ACTIVE
s0	SIG_EVT_S6_COUNT	SIG:EVT_S6:COUNT	time_double			rw		0	0	Event Counter Type S6 Value COUNT
s0	SIG_EVT_S6_FREQ	SIG:EVT_S6:FREQ	time_double	Hz		rw	0.000	0	0	Event Counter Type S6 Value FREQ
s0	SIG_EVT_S6_RESET	SIG:EVT_S6:RESET	time_enum		,	rw				Event Counter Type S6 Value RESET
s0	SIG_FP_CLKOUT	SIG:FP:CLKOUT	time_enum		INPUT, CLK0, CLK1, CLK2	rw	INPUT			Front Panel Signal CLKOUT selector
s0	SIG_FP_GPIO	SIG:FP:GPIO	time_enum		INPUT, EVT0, EVT1, EVT2, EVT3, EVT4, EVT	rw	INPUT			Front Panel Signal GPIO selector
s0	SIG_FP_GPIO_OE	SIG:FP:GPIO:OE	time_enum		,	rw				Front Panel Signal GPIO_OE selector
s0	SIG_FP_SYNC	SIG:FP:SYNC	time_enum		INPUT, SYNC0, SYNC1, SYNC2, SYNC3, SYNC4	rw	INPUT			Front Panel Signal SYNC selector
s0	SIG_FP_SYNC_OE	SIG:FP:SYNC:OE	time_enum		,	rw				Front Panel Signal SYNC_OE selector
s0	SIG_FP_TRG	SIG:FP:TRG	time_enum		INPUT, TRG0, TRG1, TRG2, TRG3, TRG4, TRG	rw	INPUT			Front Panel Signal TRG selector
s0	SIG_FP_TRGOUT	SIG:FP:TRGOUT	time_enum		INPUT, TRG0, TRG1, TRG2, TRG3, TRG4, TRG	rw	INPUT			Front Panel Signal TRGOUT selector
s0	SIG_SRC_CLK_0	SIG:SRC:CLK:0	time_enum		nc, HDMI, GPG0, COMM_A, INT01M	rw	INT01M			select signal source CLK line 0

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SIG_SRC_CLK_1	SIG:SRC:CLK:1	time_enum		MCLK, HDMI, GPG1, COMM_B	rw	MCLK			select signal source CLK line 1
s0	SIG_SRC_SYNC_0	SIG:SRC:SYNC:0	time_enum		EXT, HDMI, INT01M, WRPPS, MCLK, GPG0	rw	HDMI			select signal source SYNC line 0
s0	SIG_SRC_SYNC_1	SIG:SRC:SYNC:1	time_enum		EXT, HDMI, INT01M, WRPPS, MBCLK, GPG1	rw	EXT			select signal source SYNC line 1
s0	SIG_SRC_TRG_0	SIG:SRC:TRG:0	time_enum		EXT, HDMI, HOSTB, GPG0, DSP0, WRTT0, nc,	rw	HDMI			select signal source TRG line 0
s0	SIG_SRC_TRG_1	SIG:SRC:TRG:1	time_enum		STRIG, HOSTA, HDMI_GPIO, GPG1, DSP1, FP_	rw	STRIG			select signal source TRG line 1
s0	SIG_SYNC_BUS_IN_CLK	SIG:SYNC_BUS:IN:CLK	time_enum		,	rw				sync bus selector BUS_IN_CLK
s0	SIG_SYNC_BUS_IN_GPIO	SIG:SYNC_BUS:IN:GPIO	time_enum		,	rw				sync bus selector BUS_IN_GPIO
s0	SIG_SYNC_BUS_IN_SYNC	SIG:SYNC_BUS:IN:SYNC	time_enum		,	rw				sync bus selector BUS_IN_SYNC
s0	SIG_SYNC_BUS_IN_TRG	SIG:SYNC_BUS:IN:TRG	time_enum		,	rw				sync bus selector BUS_IN_TRG
s0	SIG_SYNC_BUS_OUT_CABLE_DET	SIG:SYNC_BUS:OUT:CABLE_DET	time_enum		nc, CONNECTED	rw	CONNECTED			sync bus selector BUS_OUT_CABLE_DET
s0	SIG_SYNC_BUS_OUT_CLK	SIG:SYNC_BUS_OUT:CLK	time_enum		,	rw				sync bus selector BUS_OUT_CLK
s0	SIG_SYNC_BUS_OUT_DRVEN	SIG:SYNC_BUS:OUT:DRVEN	time_enum		OFF, ON	rw	ON			sync bus selector BUS_OUT_DRVEN
s0	SIG_SYNC_BUS_OUT_GPIO	SIG:SYNC_BUS_OUT:GPIO	time_enum		,	rw				sync bus selector BUS_OUT_GPIO
s0	SIG_SYNC_BUS_OUT_SYNC	SIG:SYNC_BUS_OUT:SYNC	time_enum		,	rw				sync bus selector BUS_OUT_SYNC

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SIG_SYNC_BUS_OUT_TRG	SIG:SYNC_BUS_OUT:TRG	time_enum		,	rw				sync bus selector BUS_OUT_TRG
s0	SIG_SYNC_OUT_CLK	SIG:SYNC_OUT:CLK	time_enum		DO, DO, CLK, GPG	rw	CLK			sync bus selector OUT_CLK
s0	SIG_SYNC_OUT_CLK_DX	SIG:SYNC_OUT:CLK:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d1			sync bus selector OUT_CLK_DX
s0	SIG_SYNC_OUT_GPIO	SIG:SYNC_OUT:GPIO	time_enum		DO, DO, EVNT, GPG	rw	DO			sync bus selector OUT_GPIO
s0	SIG_SYNC_OUT_GPIO_DX	SIG:SYNC_OUT:GPIO:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			sync bus selector OUT_GPIO_DX
s0	SIG_SYNC_OUT_SYNC	SIG:SYNC_OUT:SYNC	time_enum		DO, DO, SYNC, GPG	rw	SYNC			sync bus selector OUT_SYNC
s0	SIG_SYNC_OUT_SYNC_DX	SIG:SYNC_OUT:SYNC:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d2			sync bus selector OUT_SYNC_DX
s0	SIG_SYNC_OUT_TRG	SIG:SYNC_OUT:TRG	time_enum		DO, DO, TRG, GPG	rw	TRG			sync bus selector OUT_TRG
s0	SIG_SYNC_OUT_TRG_DX	SIG:SYNC_OUT:TRG:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d2			sync bus selector OUT_TRG_DX
s0	SIG_SYN_EXT_ACTIVE	SIG:SYN_EXT:ACTIVE	time_double			rw	0.0	0	0	SYNC signal counter EXT value ACTIVE
s0	SIG_SYN_EXT_COUNT	SIG:SYN_EXT:COUNT	time_double			rw		0	0	SYNC signal counter EXT value COUNT
s0	SIG_SYN_EXT_FREQ	SIG:SYN_EXT:FREQ	time_double	Hz		rw	0.000	0	0	SYNC signal counter EXT value FREQ
s0	SIG_SYN_EXT_RESET	SIG:SYN_EXT:RESET	time_enum		,	rw				SYNC signal counter EXT value RESET
s0	SIG_SYN_MB_ACTIVE	SIG:SYN_MB:ACTIVE	time_double			rw	0.0	0	0	SYNC signal counter MB value ACTIVE

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SIG_SYN_MB_COUNT	SIG:SYN_MB:COUNT	time_double			rw		0	0	SYNC signal counter MB value COUNT
s0	SIG_SYN_MB_FREQ	SIG:SYN_MB:FREQ	time_double	Hz		rw	0.000	0	0	SYNC signal counter MB value FREQ
s0	SIG_SYN_MB_RESET	SIG:SYN_MB:RESET	time_enum		,	rw				SYNC signal counter MB value RESET
s0	SIG_SYN_S1_ACTIVE	SIG:SYN_S1:ACTIVE	time_double			rw	0.0	0	0	SYNC signal counter S1 value ACTIVE
s0	SIG_SYN_S1_COUNT	SIG:SYN_S1:COUNT	time_double			rw		0	0	SYNC signal counter S1 value COUNT
s0	SIG_SYN_S1_FREQ	SIG:SYN_S1:FREQ	time_double	Hz		rw	0.000	0	0	SYNC signal counter S1 value FREQ
s0	SIG_SYN_S1_RESET	SIG:SYN_S1:RESET	time_enum		,	rw				SYNC signal counter S1 value RESET
s0	SIG_SYN_S2_ACTIVE	SIG:SYN_S2:ACTIVE	time_double			rw	0.0	0	0	SYNC signal counter S2 value ACTIVE
s0	SIG_SYN_S2_COUNT	SIG:SYN_S2:COUNT	time_double			rw		0	0	SYNC signal counter S2 value COUNT
s0	SIG_SYN_S2_FREQ	SIG:SYN_S2:FREQ	time_double	Hz		rw	0.000	0	0	SYNC signal counter S2 value FREQ
s0	SIG_SYN_S2_RESET	SIG:SYN_S2:RESET	time_enum		,	rw				SYNC signal counter S2 value RESET
s0	SIG_SYN_S3_ACTIVE	SIG:SYN_S3:ACTIVE	time_double			rw	0.0	0	0	SYNC signal counter S3 value ACTIVE
s0	SIG_SYN_S3_COUNT	SIG:SYN_S3:COUNT	time_double			rw		0	0	SYNC signal counter S3 value COUNT
s0	SIG_SYN_S3_FREQ	SIG:SYN_S3:FREQ	time_double	Hz		rw	0.000	0	0	SYNC signal counter S3 value FREQ
s0	SIG_SYN_S3_RESET	SIG:SYN_S3:RESET	time_enum		,	rw				SYNC signal counter S3 value RESET
s0	SIG_SYN_S4_ACTIVE	SIG:SYN_S4:ACTIVE	time_double			rw	0.0	0	0	SYNC signal counter S4 value ACTIVE
s0	SIG_SYN_S4_COUNT	SIG:SYN_S4:COUNT	time_double			rw		0	0	SYNC signal counter S4 value COUNT
s0	SIG_SYN_S4_FREQ	SIG:SYN_S4:FREQ	time_double	Hz		rw	0.000	0	0	SYNC signal counter S4 value FREQ

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SIG_SYN_S4_RESET	SIG:SYN_S4:RESET	time_enum		,	rw				SYNC signal counter S4 value RESET
s0	SIG_SYN_S5_ACTIVE	SIG:SYN_S5:ACTIVE	time_double			rw	0.0	0	0	SYNC signal counter S5 value ACTIVE
s0	SIG_SYN_S5_COUNT	SIG:SYN_S5:COUNT	time_double			rw		0	0	SYNC signal counter S5 value COUNT
s0	SIG_SYN_S5_FREQ	SIG:SYN_S5:FREQ	time_double	Hz		rw	0.000	0	0	SYNC signal counter S5 value FREQ
s0	SIG_SYN_S5_RESET	SIG:SYN_S5:RESET	time_enum		,	rw				SYNC signal counter S5 value RESET
s0	SIG_SYN_S6_ACTIVE	SIG:SYN_S6:ACTIVE	time_double			rw	0.0	0	0	SYNC signal counter S6 value ACTIVE
s0	SIG_SYN_S6_COUNT	SIG:SYN_S6:COUNT	time_double			rw		0	0	SYNC signal counter S6 value COUNT
s0	SIG_SYN_S6_FREQ	SIG:SYN_S6:FREQ	time_double	Hz		rw	0.000	0	0	SYNC signal counter S6 value FREQ
s0	SIG_SYN_S6_RESET	SIG:SYN_S6:RESET	time_enum		,	rw				SYNC signal counter S6 value RESET
s0	SIG_TRG_EXT_ACTIVE	SIG:TRG_EXT:ACTIVE	time_double			rw	1.0	0	0	TRG signal counter EXT value ACTIVE
s0	SIG_TRG_EXT_COUNT	SIG:TRG_EXT:COUNT	time_double			rw	3e+05	0	0	TRG signal counter EXT value COUNT
s0	SIG_TRG_EXT_FREQ	SIG:TRG_EXT:FREQ	time_double	Hz		rw	1000.000	0	0	TRG signal counter EXT value FREQ
s0	SIG_TRG_EXT_RESET	SIG:TRG_EXT:RESET	time_enum		,	rw				TRG signal counter EXT value RESET
s0	SIG_TRG_MB_ACTIVE	SIG:TRG_MB:ACTIVE	time_double			rw	0.0	0	0	TRG signal counter MB value ACTIVE
s0	SIG_TRG_MB_COUNT	SIG:TRG_MB:COUNT	time_double			rw	1	0	0	TRG signal counter MB value COUNT
s0	SIG_TRG_MB_FREQ	SIG:TRG_MB:FREQ	time_double	Hz		rw	0.000	0	0	TRG signal counter MB value FREQ
s0	SIG_TRG_MB_RESET	SIG:TRG_MB:RESET	time_enum		,	rw				TRG signal counter MB value RESET
s0	SIG_TRG_S1_ACTIVE	SIG:TRG_S1:ACTIVE	time_double			rw	0.0	0	0	TRG signal counter S1 value ACTIVE

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SIG_TRG_S1_COUNT	SIG:TRG_S1:COUNT	time_double			rw		0	0	TRG signal counter S1 value COUNT
s0	SIG_TRG_S1_FREQ	SIG:TRG_S1:FREQ	time_double	Hz		rw	0.000	0	0	TRG signal counter S1 value FREQ
s0	SIG_TRG_S1_RESET	SIG:TRG_S1:RESET	time_enum		,	rw				TRG signal counter S1 value RESET
s0	SIG_TRG_S2_ACTIVE	SIG:TRG_S2:ACTIVE	time_double			rw	0.0	0	0	TRG signal counter S2 value ACTIVE
s0	SIG_TRG_S2_COUNT	SIG:TRG_S2:COUNT	time_double			rw		0	0	TRG signal counter S2 value COUNT
s0	SIG_TRG_S2_FREQ	SIG:TRG_S2:FREQ	time_double	Hz		rw	0.000	0	0	TRG signal counter S2 value FREQ
s0	SIG_TRG_S2_RESET	SIG:TRG_S2:RESET	time_enum		,	rw				TRG signal counter S2 value RESET
s0	SIG_TRG_S3_ACTIVE	SIG:TRG_S3:ACTIVE	time_double			rw	0.0	0	0	TRG signal counter S3 value ACTIVE
s0	SIG_TRG_S3_COUNT	SIG:TRG_S3:COUNT	time_double			rw		0	0	TRG signal counter S3 value COUNT
s0	SIG_TRG_S3_FREQ	SIG:TRG_S3:FREQ	time_double	Hz		rw	0.000	0	0	TRG signal counter S3 value FREQ
s0	SIG_TRG_S3_RESET	SIG:TRG_S3:RESET	time_enum		,	rw				TRG signal counter S3 value RESET
s0	SIG_TRG_S4_ACTIVE	SIG:TRG_S4:ACTIVE	time_double			rw	0.0	0	0	TRG signal counter S4 value ACTIVE
s0	SIG_TRG_S4_COUNT	SIG:TRG_S4:COUNT	time_double			rw		0	0	TRG signal counter S4 value COUNT
s0	SIG_TRG_S4_FREQ	SIG:TRG_S4:FREQ	time_double	Hz		rw	0.000	0	0	TRG signal counter S4 value FREQ
s0	SIG_TRG_S4_RESET	SIG:TRG_S4:RESET	time_enum		,	rw				TRG signal counter S4 value RESET
s0	SIG_TRG_S5_ACTIVE	SIG:TRG_S5:ACTIVE	time_double			rw	0.0	0	0	TRG signal counter S5 value ACTIVE
s0	SIG_TRG_S5_COUNT	SIG:TRG_S5:COUNT	time_double			rw		0	0	TRG signal counter S5 value COUNT
s0	SIG_TRG_S5_FREQ	SIG:TRG_S5:FREQ	time_double	Hz		rw	0.000	0	0	TRG signal counter S5 value FREQ

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SIG_TRG_S5_RESET	SIG:TRG_S5:RESET	time_enum		,	rw				TRG signal counter S5 value RESET
s0	SIG_TRG_S6_ACTIVE	SIG:TRG_S6:ACTIVE	time_double			rw	0.0	0	0	TRG signal counter S6 value ACTIVE
s0	SIG_TRG_S6_COUNT	SIG:TRG_S6:COUNT	time_double			rw		0	0	TRG signal counter S6 value COUNT
s0	SIG_TRG_S6_FREQ	SIG:TRG_S6:FREQ	time_double	Hz		rw	0.000	0	0	TRG signal counter S6 value FREQ
s0	SIG_TRG_S6_RESET	SIG:TRG_S6:RESET	time_enum		,	rw				TRG signal counter S6 value RESET
s0	SIG_ZCLK_SRC	SIG:ZCLK_SRC	time_enum		INT33M, nc, CLK.d0, CLK.d1, CLK.d2, CLK.	rw	CLK.d0			source select for ZCLK
s0	SITELIST					rw	216,1=482,2=482,3=482,4=482,5=482,6=482			Lists sites in system
s0	SL2					rw				
s0	SPAD1_US	SPAD1_US	time_enum		disable, enable	rw	disable			select SPAD1 Micro Seconds
s0		SPAD1_US:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			Spad 1 Micro Seconds Selector DX
s0		SPAD1_US:SENSE	time_enum		falling, rising	rw	falling			Spad 1 Micro Seconds Selector SENSE
s0		SPAD:DISEL	time_enum		OFF, DI4, DI32	rw	OFF			Scratch Pad mode select DISEL
s0		SPAD:LEN								Scratch Pad mode select LEN
s0		SPAD:MODE	time_enum		OFF, SPAD, FRAME	rw	OFF			Scratch Pad mode select MODE
s0	SS					rwx	ERROR site not set			indicates Sample Size in bytes

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SSB	SSB	time_long			rw	96	0	0	
s0	STACK_480	STACK_480	time_enum		none	rw	none			acq480 STACK mode select
s0		STAT:AXI64:C:ACTIVE	time_double			rw		0	0	stats AXI64:C:ACTIVE
s0		STAT:AXI64:C:COUNT	time_double			rw		0	0	stats AXI64:C:COUNT
s0		STAT:AXI64:C:FREQ	time_double	Hz		rw		0	0	stats AXI64:C:FREQ
s0		STAT:AXI64:C:RESET	time_enum		,	rw				stats AXI64:C:RESET
s0		STAT:AXI64:F:ACTIVE	time_double			rw		0	0	stats AXI64:F:ACTIVE
s0		STAT:AXI64:F:COUNT	time_double			rw		0	0	stats AXI64:F:COUNT
s0		STAT:AXI64:F:FREQ	time_double	Hz		rw		0	0	stats AXI64:F:FREQ
s0		STAT:AXI64:F:RESET	time_enum		,	rw				stats AXI64:F:RESET
s0		STAT:AXI64:W:ACTIVE	time_double			rw		0	0	stats AXI64:W:ACTIVE
s0		STAT:AXI64:W:COUNT	time_double			rw		0	0	stats AXI64:W:COUNT
s0		STAT:AXI64:W:FREQ	time_double	Hz		rw		0	0	stats AXI64:W:FREQ
s0		STAT:AXI64:W:RESET	time_enum		,	rw				stats AXI64:W:RESET
s0		STAT:BLEN	time_long			rw	4190208	0	0	Block Length
s0		STAT:GETS:ACTIVE	time_double			rw		0	0	stats GETS:ACTIVE
s0		STAT:GETS:COUNT	time_double			rw		0	0	stats GETS:COUNT
s0		STAT:GETS:FREQ	time_double	Hz		rw		0	0	stats GETS:FREQ
s0		STAT:GETS:MBPS	time_double			rw		0	0	stats GETS:MBPS
s0		STAT:GETS:MBYTES	time_double			rw		0	0	stats GETS:MBYTES
s0		STAT:GETS:RESET	time_enum		,	rw				stats GETS:RESET
s0		STAT:PUTS:ACTIVE	time_double			rw		0	0	stats PUTS:ACTIVE
s0		STAT:PUTS:COUNT	time_double			rw		0	0	stats PUTS:COUNT
s0		STAT:PUTS:FREQ	time_double	Hz		rw		0	0	stats PUTS:FREQ
s0		STAT:PUTS:RESET	time_enum		,	rw				stats PUTS:RESET
s0	SYS_CLK_BYPASS	SYS:CLK:BYPASS				rwx				CLK source select BYPASS
s0	SYS_CLK_C1B	SYS:CLK:C1B				rwx				CLK source select C1B

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	SYS_CLK_C2B	SYS:CLK:C2B				rwx				CLK source select C2B
s0	SYS_CLK_CONFIG	SYS:CLK:CONFIG				rwx	-1.00e+06--2.00e+07			CLK source select CONFIG
s0	SYS_CLK_FPMUX	SYS:CLK:FPMUX				rwx	FPCLK			CLK select Front Panel MUX
s0	SYS_CLK_LOL	SYS:CLK:LOL				rwx				CLK source select LOL
s0	SYS_CLK_OE_CLK1_ELF1	SYS:CLK:OE_CLK1_ELF1				rwx				CLK source select OE_CLK1_ELF1
s0	SYS_CLK_OE_CLK1_ELF2	SYS:CLK:OE_CLK1_ELF2				rwx				CLK source select OE_CLK1_ELF2
s0	SYS_CLK_OE_CLK1_ELF3	SYS:CLK:OE_CLK1_ELF3				rwx				CLK source select OE_CLK1_ELF3
s0	SYS_CLK_OE_CLK1_ELF4	SYS:CLK:OE_CLK1_ELF4				rwx				CLK source select OE_CLK1_ELF4
s0	SYS_CLK_OE_CLK1_ELF5	SYS:CLK:OE_CLK1_ELF5				rwx				CLK source select OE_CLK1_ELF5
s0	SYS_CLK_OE_CLK1_ELF6	SYS:CLK:OE_CLK1_ELF6				rwx				CLK source select OE_CLK1_ELF6
s0	SYS_CLK_OE_CLK1_ZYNQ	SYS:CLK:OE_CLK1_ZYNQ				rwx				CLK source select OE_CLK1_ZYNQ
s0	SYS_CLK_Si5326_PLAN	SYS:CLK:Si5326:PLAN				rwx	10M			CLK source select Si5326_PLAN
s0	SYS_CLK_Si5326_PLAN_EN	SYS:CLK:Si5326:PLAN_EN				rwx	1			CLK source select Si5326_PLAN_EN
s0	SYS_CLK_Si570_OE	SYS:CLK:Si570_OE				rwx				CLK source select Si570_OE
s0	SYS_TEMP					rw	mainboard=26.5,SITE1=41.5,SITE3=40.5,SITE5=38,SITEE=36,ZYNQ=54			
s0	SYS_VOLTS					rw	5VP=4.97,VADJ=1.77,VAN=-5.1,VAP=5.05,vccaux=1.782,vccbram=0.986,vccint=0.987			
s0	set_si5326_bypass					rwx	load.si5326 [-o file] regmap			
s0	si5326bypass					rw				
s0	si5326config					rw	-1.00e+06--2.00e+07			
s0	si5326_step_phase					rwx				
s0	si5326_step_state					rw	3,1,-5,0			
s0	spad0					rw	0x00000000			Set or Read value of SPAD reg 0

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	spad1					rw	0x00000000			Set or Read value of SPAD reg 1
s0	spad1_us_clk_src					rw				Set or Read value of SPAD reg 1
s0	spad2					rw	0x00000000			Set or Read value of SPAD reg 2
s0	spad3					rw	0x00000000			Set or Read value of SPAD reg 3
s0	spad4					rw	0xdead0005			Set or Read value of SPAD reg 4
s0	spad5					rw	0xdead0005			Set or Read value of SPAD reg 5
s0	spad6					rw	0x00009bde			Set or Read value of SPAD reg 6
s0	spad7					rw	0x00000000			Set or Read value of SPAD reg 7
s0	spadcop0					rw	0,0,0,0 0			Configure a software update for spad 0 enable,site,reg,usecs
s0	spadcop1					rw	0,0,0,0 0			Configure a software update for spad 1 enable,site,reg,usecs
s0	spadcop2					rw	0,0,0,0 0			Configure a software update for spad 2 enable,site,reg,usecs
s0	spadcop3					rw	0,0,0,0 0			Configure a software update for spad 3 enable,site,reg,usecs
s0	spadcop4					rw	1,5,70,976 112829			Configure a software update for spad 4 enable,site,reg,usecs
s0	spadcop5					rw	2,5,74,976 112870			Configure a software update for spad 5 enable,site,reg,usecs
s0	spadcop6					rw	1,5,cccc,976 112911			Configure a software update for spad 6 enable,site,reg,usecs
s0	spadcop7					rw	0,0,0,0 0			Configure a software update for spad 7 enable,site,reg,usecs
s0	spadstart					rw	96			
s0	special_sites					rw	12 13			
s0	streamtonowhered					rwx				enable streaming to nowhere
s0	sync_role					rwx	rpmaster 20000000			global sync_role. Check manual for details or sync_role help
s0	TIM_CTRL_LOCK					rw				Deprecated
s0	TRANSIENT					rwx	TRANSIENT			transient enable
s0	TRANSIENT_DELAYMS	TRANSIENT:DELAYMS				rwx	1000			transient parameter _DELAYMS
s0	TRANSIENT_OSAM	TRANSIENT:OSAM				rwx	1			transient parameter _OSAM
s0	TRANSIENT_POST	TRANSIENT:POST				rwx				transient parameter _POST
s0	TRANSIENT_PRE	TRANSIENT:PRE				rwx				transient parameter _PRE
s0	TRANSIENT_REPEAT	TRANSIENT:REPEAT				rwx				transient parameter _REPEAT

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s0	TRANSIENT_SET_ABORT	TRANSIENT:SET_ABORT				rwx				transient parameter _SET_ABORT
s0	TRANSIENT_SET_ARM	TRANSIENT:SET_ARM				rwx				transient parameter _SET_ARM
s0	TRANSIENT_SOFT_TRIGGER	TRANSIENT:SOFT_TRIGGER				rwx	1			transient parameter _SOFT_TRIGGER
s0	TRANS_ACT_FIND_EV_CUR	TRANS_ACT:FIND_EV:CUR				rx				transient actual FIND_EV_CUR status
s0	TRANS_ACT_FIND_EV_NBU	TRANS_ACT:FIND_EV:NBU				rx				transient actual FIND_EV_NBU status
s0	TRANS_ACT_FIND_EV_STA	TRANS_ACT:FIND_EV:STA				rx	IDLE			transient actual FIND_EV_STA status
s0	TRANS_ACT_POST	TRANS_ACT:POST				rx				transient actual POST status
s0	TRANS_ACT_POST_MDSPUTCH	TRANS_ACT:POST:MDSPUTCH				rx	-1			transient actual POST_MDSPUTCH status
s0	TRANS_ACT_PRE	TRANS_ACT:PRE				rx				transient actual PRE status
s0	TRANS_ACT_STATE	TRANS_ACT:STATE				rx	IDLE			transient actual STATE status
s0	TRANS_ACT_STATE_NOT_IDLE	TRANS_ACT:STATE_NOT_IDLE				rx				transient actual STATE_NOT_IDLE status
s0	TRANS_ACT_TOTSAM	TRANS_ACT:TOTSAM				rx				transient actual TOTSAM status
s0	xdt					rw				

Site **s1**
EpicsPV Prefix **acq2106_340:1:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	ACC					rw	2,0,173			
s1		ACQ480:HAS_50R	time_enum		0, 1	rw	1			50 ohm option _is_ available on this module
s1		ACQ480:JC:LOL	time_enum		0, 1	rw	1			Jitter Cleaner Loss Of Lock failure status
s1		ACQ480:JC:LOS	time_enum		0, 1	rw				Jitter Cleaner Los Of Signal failure status
s1		ACQ480:JC:PRESENT	time_enum		0, 1	rw	1			Jitter Cleaner is Present status
s1		ACQ480:LINK:LOTI	time_enum		0, 1	rw	1			Link Loss Of Training Indicator
s1		ACQ480:TRAIN	time_enum		ACQ480_RESET, ACQ480_START, ACQ480_DESKE	rw	ACQ480_RESET			Indicator shows ACQ480 Training state
s1		ACQ480:TWO_LANE	time_enum		0, 1	rw				ADC has two lanes per bit enabled for 80MSPS operation
s1	ACQ480_FIR_01	ACQ480:FIR:01	time_enum		DISABLE, LP_ODD_D2, HP_ODD_D2, LP_EVEN_D	rw	DISABLE			Select ADC FIR Filter. 01 masters 02..08
s1	ACQ480_FIR_DECIM	ACQ480:FIR:DECIM	time_double			rw	1	0	0	Shows Decimation due to ADC FIR selection
s1	ACQ480_FPGA_DECIM	ACQ480:FPGA:DECIM	time_long			rw	10	0	0	Show decimation due to FPGA 1, 4, 10 ..selected on FPGA personality load.
s1	ACQ480_GAIN_01	ACQ480:GAIN:01	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH01
s1	ACQ480_GAIN_02	ACQ480:GAIN:02	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH02
s1	ACQ480_GAIN_03	ACQ480:GAIN:03	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH03
s1	ACQ480_GAIN_04	ACQ480:GAIN:04	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH04
s1	ACQ480_GAIN_05	ACQ480:GAIN:05	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH05
s1	ACQ480_GAIN_06	ACQ480:GAIN:06	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH06

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	ACQ480_GAIN_07	ACQ480:GAIN:07	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH07
s1	ACQ480_GAIN_08	ACQ480:GAIN:08	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH08
s1	ACQ480_GAIN_ALL	ACQ480:GAIN:ALL	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			
s1	ACQ480_HPF_01	ACQ480:HPF:01	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH01 See ADS1278 manual for details
s1	ACQ480_HPF_02	ACQ480:HPF:02	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH02 See ADS1278 manual for details
s1	ACQ480_HPF_03	ACQ480:HPF:03	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH03 See ADS1278 manual for details
s1	ACQ480_HPF_04	ACQ480:HPF:04	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH04 See ADS1278 manual for details
s1	ACQ480_HPF_05	ACQ480:HPF:05	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH05 See ADS1278 manual for details

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	ACQ480_HPF_06	ACQ480:HPF:06	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH06 See ADS1278 manual for details
s1	ACQ480_HPF_07	ACQ480:HPF:07	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH07 See ADS1278 manual for details
s1	ACQ480_HPF_08	ACQ480:HPF:08	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH08 See ADS1278 manual for details
s1	ACQ480_INVERT_01	ACQ480:INVERT:01	time_enum		REG, INV	rw	REG			Invert Input CH01 Could be useful for debug, or create a differential input from two channels
s1	ACQ480_INVERT_02	ACQ480:INVERT:02	time_enum		REG, INV	rw	REG			Invert Input CH02 Could be useful for debug, or create a differential input from two channels
s1	ACQ480_INVERT_03	ACQ480:INVERT:03	time_enum		REG, INV	rw	REG			Invert Input CH03 Could be useful for debug, or create a differential input from two channels
s1	ACQ480_INVERT_04	ACQ480:INVERT:04	time_enum		REG, INV	rw	REG			Invert Input CH04 Could be useful for debug, or create a differential input from two channels
s1	ACQ480_INVERT_05	ACQ480:INVERT:05	time_enum		REG, INV	rw	REG			Invert Input CH05 Could be useful for debug, or create a differential input from two channels
s1	ACQ480_INVERT_06	ACQ480:INVERT:06	time_enum		REG, INV	rw	REG			Invert Input CH06 Could be useful for debug, or create a differential input from two channels
s1	ACQ480_INVERT_07	ACQ480:INVERT:07	time_enum		REG, INV	rw	REG			Invert Input CH07 Could be useful for debug, or create a differential input from two channels
s1	ACQ480_INVERT_08	ACQ480:INVERT:08	time_enum		REG, INV	rw	REG			Invert Input CH08 Could be useful for debug, or create a differential input from two channels
s1	ACQ480_LFNS_01	ACQ480:LFNS:01	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH01 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s1	ACQ480_LFNS_02	ACQ480:LFNS:02	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH02 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s1	ACQ480_LFNS_03	ACQ480:LFNS:03	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH03 See ADS1278 manual for details. Only effective with ADC_FIR enabled

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	ACQ480_LFNS_04	ACQ480:LFNS:04	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH04 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s1	ACQ480_LFNS_05	ACQ480:LFNS:05	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH05 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s1	ACQ480_LFNS_06	ACQ480:LFNS:06	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH06 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s1	ACQ480_LFNS_07	ACQ480:LFNS:07	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH07 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s1	ACQ480_LFNS_08	ACQ480:LFNS:08	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH08 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s1	ACQ480_LFNS_ALL	ACQ480:LFNS:ALL	time_enum		OFF, ON	rw	OFF			
s1	ACQ480_MR_10DEC	ACQ480:MR:10DEC	time_enum		dec4, dec8, dec16, dec32	rw	dec4			
s1	ACQ480_MR_EN	ACQ480:MR:EN	time_enum		,	rw				ACQ480 Enable Multi Rate
s1	ACQ480_MR_EVSEL_0	ACQ480:MR:EVSEL:0	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			ACQ480 Multirate Signal Line Select 0
s1	ACQ480_MR_EVSEL_1	ACQ480:MR:EVSEL:1	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			ACQ480 Multirate Signal Line Select 1
s1	ACQ480_OSR	ACQ480:OSR	time_double			rw	2e+06	0	0	Shows ADC Output Sample Rate OSR = FS / ADC_DECIM / FPGA_DECIM
s1	ACQ480_T50R	ACQ480:T50R	time_enum		0, 1	rw				Enable 50 Ohm input impedance, all channels Initial Value: 0 PVType: time_enum
s1	ACQ480_T50R_01	ACQ480:T50R:01	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH01
s1	ACQ480_T50R_02	ACQ480:T50R:02	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH02
s1	ACQ480_T50R_03	ACQ480:T50R:03	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH03
s1	ACQ480_T50R_04	ACQ480:T50R:04	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH04
s1	ACQ480_T50R_05	ACQ480:T50R:05	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH05

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	ACQ480_T50R_06	ACQ480:T50R:06	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH06
s1	ACQ480_T50R_07	ACQ480:T50R:07	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH07
s1	ACQ480_T50R_08	ACQ480:T50R:08	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH08
s1	ADC_CAL	ADC_CAL	time_enum		default, caldef, calibrated	rw	calibrated			Initial Value: calibrated PVType: time_enum
s1	ADC_MODE	ADC_MODE	time_long			rw	-1	0	0	Indicates ADC is calibrated Initial Value: -1
s1	AGIX					rw				
s1		AI:TW:01								Raw Transient Waveform CH01
s1		AI:TW:01:V	time_long			rw		0	0	Cooked Transient Waveform CH01 Volts
s1		AI:TW:02								Raw Transient Waveform CH02
s1		AI:TW:02:V	time_long			rw		0	0	Cooked Transient Waveform CH02 Volts
s1		AI:TW:03								Raw Transient Waveform CH03
s1		AI:TW:03:V	time_long			rw		0	0	Cooked Transient Waveform CH03 Volts
s1		AI:TW:04								Raw Transient Waveform CH04
s1		AI:TW:04:V	time_long			rw		0	0	Cooked Transient Waveform CH04 Volts
s1		AI:TW:05								Raw Transient Waveform CH05
s1		AI:TW:05:V	time_long			rw		0	0	Cooked Transient Waveform CH05 Volts
s1		AI:TW:06								Raw Transient Waveform CH06
s1		AI:TW:06:V	time_long			rw		0	0	Cooked Transient Waveform CH06 Volts
s1		AI:TW:07								Raw Transient Waveform CH07
s1		AI:TW:07:V	time_long			rw		0	0	Cooked Transient Waveform CH07 Volts
s1		AI:TW:08								Raw Transient Waveform CH08
s1		AI:TW:08:V	time_long			rw		0	0	Cooked Transient Waveform CH08 Volts
s1		AI:TW:TB	time_long			rw		0	0	Transient Waveform Timebase record
s1		AI:WF:01	time_short			rw	array([], dtype=int16)	0	0	Analog input 01 waveform
s1		AI:WF:01:ACTIVE	time_double			rw		0	0	Live Waveform Status CH01 Function ACTIVE
s1		AI:WF:01:OVER	time_enum	,		rw				Live Waveform Status CH01 Function OVER
s1		AI:WF:01:TH	time_long			rw	512	0	0	Live Waveform Status CH01 Function TH
s1		AI:WF:01:UPDATES	time_double			rw		0	0	Live Waveform Status CH01 Function UPDATES

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1		AI:WF:01:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH01 Volts
s1		AI:WF:02	time_short			rw	array([], dtype=int16)	0	0	Analog input 02 waveform
s1		AI:WF:02:ACTIVE	time_double			rw		0	0	Live Waveform Status CH02 Function ACTIVE
s1		AI:WF:02:OVER	time_enum		,	rw				Live Waveform Status CH02 Function OVER
s1		AI:WF:02:TH	time_long			rw	512	0	0	Live Waveform Status CH02 Function TH
s1		AI:WF:02:UPDATES	time_double			rw		0	0	Live Waveform Status CH02 Function UPDATES
s1		AI:WF:02:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH02 Volts
s1		AI:WF:03	time_short			rw	array([], dtype=int16)	0	0	Analog input 03 waveform
s1		AI:WF:03:ACTIVE	time_double			rw		0	0	Live Waveform Status CH03 Function ACTIVE
s1		AI:WF:03:OVER	time_enum		,	rw				Live Waveform Status CH03 Function OVER
s1		AI:WF:03:TH	time_long			rw	512	0	0	Live Waveform Status CH03 Function TH
s1		AI:WF:03:UPDATES	time_double			rw		0	0	Live Waveform Status CH03 Function UPDATES
s1		AI:WF:03:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH03 Volts
s1		AI:WF:04	time_short			rw	array([], dtype=int16)	0	0	Analog input 04 waveform
s1		AI:WF:04:ACTIVE	time_double			rw		0	0	Live Waveform Status CH04 Function ACTIVE
s1		AI:WF:04:OVER	time_enum		,	rw				Live Waveform Status CH04 Function OVER
s1		AI:WF:04:TH	time_long			rw	512	0	0	Live Waveform Status CH04 Function TH
s1		AI:WF:04:UPDATES	time_double			rw		0	0	Live Waveform Status CH04 Function UPDATES
s1		AI:WF:04:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH04 Volts
s1		AI:WF:05	time_short			rw	array([], dtype=int16)	0	0	Analog input 05 waveform
s1		AI:WF:05:ACTIVE	time_double			rw		0	0	Live Waveform Status CH05 Function ACTIVE
s1		AI:WF:05:OVER	time_enum		,	rw				Live Waveform Status CH05 Function OVER
s1		AI:WF:05:TH	time_long			rw	512	0	0	Live Waveform Status CH05 Function TH
s1		AI:WF:05:UPDATES	time_double			rw		0	0	Live Waveform Status CH05 Function UPDATES
s1		AI:WF:05:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH05 Volts
s1		AI:WF:06	time_short			rw	array([], dtype=int16)	0	0	Analog input 06 waveform
s1		AI:WF:06:ACTIVE	time_double			rw		0	0	Live Waveform Status CH06 Function ACTIVE
s1		AI:WF:06:OVER	time_enum		,	rw				Live Waveform Status CH06 Function OVER
s1		AI:WF:06:TH	time_long			rw	512	0	0	Live Waveform Status CH06 Function TH
s1		AI:WF:06:UPDATES	time_double			rw		0	0	Live Waveform Status CH06 Function UPDATES

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1		AI:WF:06:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH06 Volts
s1		AI:WF:07	time_short			rw	array([], dtype=int16)	0	0	Analog input 07 waveform
s1		AI:WF:07:ACTIVE	time_double			rw		0	0	Live Waveform Status CH07 Function ACTIVE
s1		AI:WF:07:OVER	time_enum		,	rw				Live Waveform Status CH07 Function OVER
s1		AI:WF:07:TH	time_long			rw	512	0	0	Live Waveform Status CH07 Function TH
s1		AI:WF:07:UPDATES	time_double			rw		0	0	Live Waveform Status CH07 Function UPDATES
s1		AI:WF:07:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH07 Volts
s1		AI:WF:08	time_short			rw	array([], dtype=int16)	0	0	Analog input 08 waveform
s1		AI:WF:08:ACTIVE	time_double			rw		0	0	Live Waveform Status CH08 Function ACTIVE
s1		AI:WF:08:OVER	time_enum		,	rw				Live Waveform Status CH08 Function OVER
s1		AI:WF:08:TH	time_long			rw	512	0	0	Live Waveform Status CH08 Function TH
s1		AI:WF:08:UPDATES	time_double			rw		0	0	Live Waveform Status CH08 Function UPDATES
s1		AI:WF:08:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH08 Volts
s1		AI:WF:TB	time_long			rw		0	0	Live Waveform Timebase
s1		AI:WF:W1	time_long			rw		0	0	
s1		AI:WF:W2	time_long			rw		0	0	
s1	AI_CAL_EOFF	AI:CAL:EOFF	time_float			rw		0	0	AI Offset Calibration values, index from 0
s1	AI_CAL_ESLO	AI:CAL:ESLO	time_float			rw		0	0	AI Calibration Slope, index from 0
s1	acq480_dump					rwx	ffff 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 10:0000 0000 0000 0000 0000 0000 0000 0000 0000 00			acq480 low level control dump
s1	acq480_flush					rwx				acq480 low level control flush
s1	acq480_fpga_decim					r	10			acq480 low level control fpga_decim
s1	acq480_getGain					rwx				acq480 low level control getGain
s1	acq480_help					rwx	PLL [FSMSPS DECIM] dump flush getGain			acq480 low level control help
s1	acq480_loti					r	1			acq480 low level control loti
s1	acq480_makeLinks					rwx	ln -s /usr/local/bin/acq480_knobs acq480_PLL ln -s /usr/local/bin/acq480_knobs acq480_dump ln -s /usr/local/bin/acq480_k			acq480 low level control makeLinks

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	acq480_map					rwx	MAP_CH1234_TO_OUT1A 1 MAP_CH1234_TO_OUT1B 1 2 MAP_CH1234_TO_OUT2A 2 MAP_CH1234_TO_OUT2B 2 2 MAP_CH1234_TO_OUT3A 3 MAP_CH			acq480 low level control map
s1	acq480_PLL					rwx	getPLL() d1 = 0000			acq480 low level control PLL
s1	acq480_readall					rwx				acq480 low level control readall
s1	acq480_reg					rwx				acq480 low level control reg
s1	acq480_reset					rwx				acq480 low level control reset
s1	acq480_setAverageSelect					rwx				acq480 low level control setAverageSelect
s1	acq480_setClkHardSync					rwx				acq480 low level control setClkHardSync
s1	acq480_setDataPattern					rwx	-EN_2WIRE BTC_MODE -MSB_FIRST -EN_SDR -FALL_SDR			acq480 low level control setDataPattern
s1	acq480_setDataRate					rwx				acq480 low level control setDataRate
s1	acq480_setDecimationFilter					rwx				acq480 low level control setDecimationFilter
s1	acq480_setFilterCoefficients					rwx				acq480 low level control setFilterCoefficients
s1	acq480_setGain					rwx				acq480 low level control setGain
s1	acq480_setHiPassFilter					rwx				acq480 low level control setHiPassFilter
s1	acq480_setInvert					rwx				acq480 low level control setInvert
s1	acq480_setLFNS					rwx				acq480 low level control setLFNS
s1	acq480_setLvdsTestPatDeskew					rwx				acq480 low level control setLvdsTestPatDeskew
s1	acq480_setLvdsTestPatRamp					rwx				acq480 low level control setLvdsTestPatRamp
s1	acq480_setPatDeskew					rwx				acq480 low level control setPatDeskew
s1	acq480_setPatSync					rwx				acq480 low level control setPatSync
s1	acq480_setTwoWireMode					rwx				acq480 low level control setTwoWireMode
s1	acq480_train_ctrl					rw	0x00000000			acq480 low level control train_ctrl
s1	acq480_train_hi_val					r	0x00000000 0			acq480 low level control train_hi_val
s1	acq480_train_lo_val					r	0x00000000 4			acq480 low level control train_lo_val

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	acq480_two_lane_mode					rw				acq480 low level control two_lane_mode
s1	acq482_cmap					rw	1			acq480 channel mapping 4|8
s1	BURST	BURST	time_enum		OFF, nc, RGM, RTM, nc	rw	OFF			BURST Mode enable: RGM, RTM Initial Value: RGM PVType: time_enum
s1		BURST:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			BURST module signal line number Initial Value: d0 PVType: time_enum
s1		BURST:SENSE	time_enum		falling, rising	rw	falling			BURST mode signal edge select Initial Value: rising PVType: time_enum
s1	burst					rw	burst=0,0,0 disable			
s1	CLK	CLK	time_enum		internal, external	rw	external			Select CLK type
s1	CLKDIV	CLKDIV	time_double			rw	1			Clock divider value
s1	CLK_DX	CLK:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d1			CLK source select DX
s1	CLK_IS_EXT_D0	CLK_IS_EXT_D0	time_double			rw	CLK_IS_EXT_D0 0			Clock is E
s1	CLK_IS_EXT_D1	CLK_IS_EXT_D1	time_double			rw	1			Clock is E
s1	CLK_IS_EXT_D2	CLK_IS_EXT_D2	time_double			rw	CLK_IS_EXT_D2 0			Clock is E
s1	CLK_IS_INTERNAL	CLK_IS_INTERNAL	time_double			rw	CLK_IS_INTERNAL 0			Clock is I
s1	CLK_SENSE	CLK:SENSE	time_enum		falling, rising	rw	rising			CLK source select SENSE
s1	COMPLETED_SHOT	COMPLETED_SHOT	time_long			rw		0	0	shot number of last completed shot
s1	DEC					rw	2,0,173			
s1	DT	DT	time_double			rw	DT 0			Delta Time nsec
s1	data32					rw				data is 32 bit (not 16 bit)
s1	EVENT0	EVENT0	time_enum		disable, enable	rw	disable			EVENT 0 Enable
s1	EVENT0_DX	EVENT0:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			EVENT 0 selector DX

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	EVENT0_SENSE	EVENT0:SENSE	time_enum		falling, rising	rw	falling			EVENT 0 selector SENSE
s1	EVENT1	EVENT1	time_enum		disable, enable	rw	disable			EVENT 1 Enable
s1	EVENT1_DX	EVENT1:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			EVENT 1 selector DX
s1	EVENT1_SENSE	EVENT1:SENSE	time_enum		falling, rising	rw	falling			EVENT 1 selector SENSE
s1		EVT:INDX:ACTIVE	time_double			rw		0	0	EVT Event INDX Counter Value ACTIVE
s1		EVT:INDX:COUNT	time_double			rw		0	0	EVT Event INDX Counter Value COUNT
s1		EVT:INDX:FREQ	time_double	Hz		rw		0	0	EVT Event INDX Counter Value FREQ
s1		EVT:INDX:RESET	time_enum		,	rw				EVT Event INDX Counter Value RESET
s1		EVT:SMPL:ACTIVE	time_double			rw		0	0	EVT Event SMPL Counter Value ACTIVE
s1		EVT:SMPL:COUNT	time_double			rw		0	0	EVT Event SMPL Counter Value COUNT
s1		EVT:SMPL:FREQ	time_double	Hz		rw		0	0	EVT Event SMPL Counter Value FREQ
s1		EVT:SMPL:RESET	time_enum		,	rw				EVT Event SMPL Counter Value RESET
s1	event_time					rw				
s1	evt_sc_latch					r				
s1	ffir_coeff					rw	13 "Permission denied" failed to open "ffir_coeff"			
s1	ffir_counter					r				
s1	ffir_reset					rw	13 "Permission denied" failed to open "ffir_reset"			
s1	GAIN_01	GAIN:01	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_01

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	GAIN_02	GAIN:02	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_02
s1	GAIN_03	GAIN:03	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_03
s1	GAIN_04	GAIN:04	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_04
s1	GAIN_05	GAIN:05	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_05
s1	GAIN_06	GAIN:06	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_06
s1	GAIN_07	GAIN:07	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_07
s1	GAIN_08	GAIN:08	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_08
s1	GAIN_CHANGES	GAIN_CHANGES	time_double			rw	GAIN_CHANGES 0			indicates GAIN has changed
s1	HAS_CH_GAINS	HAS_CH_GAINS	time_enum		,	rw	HAS_CH_GAINS			Module has per channel GAIN control
s1	HAS_RGM	HAS_RGM	time_enum		0, 1	rw	1			Module supports Repeating Gate (Burst) Mode
s1	INTCLK_HZ	INTCLK_HZ	time_long	Hz		rw	50000000	80000000	10000	Internal Clock frequency [integer Hz]
s1	IS_PRESENT	IS_PRESENT	time_enum		EMPTY, PRESENT	rw	PRESENT			module IS PRESENT

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	is_adc					r	1 sar			
s1	JC_LOL					rw	1			JC Jitter Cleaner State LOL
s1	JC_LOS					rw				JC Jitter Cleaner State LOS
s1	MANUFACTURER					r	D-TACQ Solutions			Show module Manufacturer
s1	MAX_KHZ					rw	80000			
s1	MIN_KHZ					rw	10000			
s1	MODEL	MODEL	time_string			rw	ACQ482ELF			Module MODEL
s1	MTYPE	MTYPE	time_string			rw	8			Module Type Code
s1	module_variant					r	2			
s1	mr_10dec					rw				
s1	mr_en					rw				
s1	mr_sel0					rw				
s1	mr_sel1					rw				
s1	NCHAN	NCHAN	time_long			rw	8			Module number of channels
s1		OS:COUNT	time_double			rw		0	0	oversampling COUNT
s1		OS:FREQ	time_double	Hz		rw		0	0	oversampling FREQ
s1	PART_NUM	PART_NUM	time_string			rw	ACQ482ELF-16-2V5-H			Module full part number
s1	prompt						None			prompt {0|1} changes command prompt
s1	RGM	RGM	time_enum		OFF, nc, RGM, RTM, nc	rw	OFF			Repeating Gate (Burst) Mode mode selector
s1	RGM_DX	RGM:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			Repeating Gate (Burst) Mode source select
s1	RGM_SENSE	RGM:SENSE	time_enum		falling, rising	rw	falling			Repeating Gate (Burst) Mode source select
s1	ROLE	ROLE	time_string			rw	MASTER			
s1	RTM_TRANSLEN	RTM_TRANSLEN	time_long			rw	RTM_TRANSLEN 0	16777215	1	Repeating Transient Mode TRANSLEN control
s1	RTM_TRANSLEN_ONCHANGE	RTM_TRANSLEN:ONCHANGE	time_double			rw		16777216	0	Repeating Transient Mode TRANSLEN_ONCHANGE control

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	SERIAL	SERIAL	time_string			rw	E48210105			Module Serial Number
s1	SHOT	SHOT	time_long			rw		0	0	Shot Number
s1	SHOT_IN_PROGRESS	SHOT_IN_PROGRESS	time_double			rw		0	0	shot is in progress.
s1	SIG_CLK_COUNT_ACTIVE	SIG:CLK_COUNT:ACTIVE	time_double			rw	1.0	0	0	CLK counter signal COUNT value ACTIVE
s1	SIG_CLK_COUNT_COUNT	SIG:CLK_COUNT:COUNT	time_double			rw	6e+09	0	0	CLK counter signal COUNT value COUNT
s1	SIG_CLK_COUNT_FREQ	SIG:CLK_COUNT:FREQ	time_double	Hz		rw	2e+07	0	0	CLK counter signal COUNT value FREQ
s1	SIG_CLK_COUNT_RESET	SIG:CLK_COUNT:RESET	time_enum		,	rw				CLK counter signal COUNT value RESET
s1	SIG_CLK_TRAIN_BSY	SIG:CLK:TRAIN_BSY	time_enum		0, 1	rw				Clock Training is BUSY
s1	SIG_clk_count_ACTIVE	SIG:clk_count:ACTIVE	time_double			rw	1.0	0	0	Signal counter clk value ACTIVE
s1	SIG_clk_count_COUNT	SIG:clk_count:COUNT	time_double			rw	6e+09	0	0	Signal counter clk value COUNT
s1	SIG_clk_count_FREQ	SIG:clk_count:FREQ	time_double	Hz		rw	2e+07	0	0	Signal counter clk value FREQ
s1	SIG_clk_count_RESET	SIG:clk_count:RESET	time_enum		,	rw				Signal counter clk value RESET
s1	SIG_SAMPLE_COUNT_ACTIVE	SIG:SAMPLE_COUNT:ACTIVE	time_double			rw	0.0	0	0	Signal Sample Count Value ACTIVE
s1	SIG_SAMPLE_COUNT_COUNT	SIG:SAMPLE_COUNT:COUNT	time_double			rw		0	0	Signal Sample Count Value COUNT
s1	SIG_SAMPLE_COUNT_FREQ	SIG:SAMPLE_COUNT:FREQ	time_double	Hz		rw	0.000	0	0	Signal Sample Count Value FREQ
s1	SIG_SAMPLE_COUNT_RESET	SIG:SAMPLE_COUNT:RESET	time_enum		,	rw				Signal Sample Count Value RESET
s1	SIG_SAMPLE_COUNT_RUNTIME	SIG:SAMPLE_COUNT:RUNTIME	time_double	s		rw		9999999	0	Signal Sample Count Value RUNTIME
s1	SIG_sample_count_ACTIVE	SIG:sample_count:ACTIVE	time_double			rw	0.0	0	0	Signal counter sample value ACTIVE
s1	SIG_sample_count_COUNT	SIG:sample_count:COUNT	time_double			rw		0	0	Signal counter sample value COUNT
s1	SIG_sample_count_FREQ	SIG:sample_count:FREQ	time_double	Hz		rw	0.000	0	0	Signal counter sample value FREQ
s1	SIG_sample_count_FREQ_ONCHANGE	SIG:sample_count:FREQ:ONCHANGE	time_long			rw		0	0	Signal counter sample value FREQ_ONCHANGE
s1	SIG_sample_count_RESET	SIG:sample_count:RESET	time_enum		,	rw				Signal counter sample value RESET
s1	SYNC	SYNC	time_enum		internal, external	rw	internal			SYNC enable
s1	SYNC_DX	SYNC:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			SYNC DX selector
s1	SYNC_SENSE	SYNC:SENSE	time_enum		falling, rising	rw	falling			SYNC SENSE selector
s1	sod					rw				Sample On Demand: single sample mode (where available)
s1	T50R						None			Set 50 Ohm Impedance all channels.

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s1	T50R_1					rw				Select 50 Ohm impedance CH1
s1	T50R_2					rw				Select 50 Ohm impedance CH2
s1	T50R_3					rw				Select 50 Ohm impedance CH3
s1	T50R_4					rw				Select 50 Ohm impedance CH4
s1	T50R_5					rw				Select 50 Ohm impedance CH5
s1	T50R_6					rw				Select 50 Ohm impedance CH6
s1	T50R_7					rw				Select 50 Ohm impedance CH7
s1	T50R_8					rw				Select 50 Ohm impedance CH8
s1	TRG	TRG	time_enum		none, enable	rw	enable			TRG enable
s1	TRG_DX	TRG:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			trigger source DX select
s1	TRG_SENSE	TRG:SENSE	time_enum		falling, rising	rw	rising			trigger source SENSE select
s1	TRIGGERED	TRIGGERED	time_enum		NO, YES	rw	NO			
s1	task_active					r				
s1	XDT	XDT	time_long	nsec		rw	XDT 0			eXternal clock Delta Time nsec

Site **s2**
EpicsPV Prefix **acq2106_340:2:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	ACC					rw	3,0,173			
s2		ACQ480:HAS_50R	time_enum		0, 1	rw	1			50 ohm option _is_ available on this module
s2		ACQ480:JC:LOL	time_enum		0, 1	rw				Jitter Cleaner Loss Of Lock failure status
s2		ACQ480:JC:LOS	time_enum		0, 1	rw				Jitter Cleaner Los Of Signal failure status
s2		ACQ480:JC:PRESENT	time_enum		0, 1	rw				Jitter Cleaner is Present status
s2		ACQ480:LINK:LOTI	time_enum		0, 1	rw	1			Link Loss Of Training Indicator
s2		ACQ480:TRAIN	time_enum		ACQ480_RESET, ACQ480_START, ACQ480_DESKE	rw	ACQ480_RESET			Indicator shows ACQ480 Training state
s2		ACQ480:TWO_LANE	time_enum		0, 1	rw				ADC has two lanes per bit enabled for 80MSPS operation
s2	ACQ480_FIR_01	ACQ480:FIR:01	time_enum		DISABLE, LP_ODD_D2, HP_ODD_D2, LP_EVEN_D	rw	DISABLE			Select ADC FIR Filter. 01 masters 02..08
s2	ACQ480_FIR_DECIM	ACQ480:FIR:DECIM	time_double			rw	1	0	0	Shows Decimation due to ADC FIR selection
s2	ACQ480_FPGA_DECIM	ACQ480:FPGA:DECIM	time_long			rw	10	0	0	Show decimation due to FPGA 1, 4, 10 ..selected on FPGA personality load.
s2	ACQ480_GAIN_01	ACQ480:GAIN:01	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH01
s2	ACQ480_GAIN_02	ACQ480:GAIN:02	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH02
s2	ACQ480_GAIN_03	ACQ480:GAIN:03	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH03
s2	ACQ480_GAIN_04	ACQ480:GAIN:04	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH04
s2	ACQ480_GAIN_05	ACQ480:GAIN:05	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH05
s2	ACQ480_GAIN_06	ACQ480:GAIN:06	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH06

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	ACQ480_GAIN_07	ACQ480:GAIN:07	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH07
s2	ACQ480_GAIN_08	ACQ480:GAIN:08	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH08
s2	ACQ480_GAIN_ALL	ACQ480:GAIN:ALL	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			
s2	ACQ480_HPF_01	ACQ480:HPF:01	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH01 See ADS1278 manual for details
s2	ACQ480_HPF_02	ACQ480:HPF:02	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH02 See ADS1278 manual for details
s2	ACQ480_HPF_03	ACQ480:HPF:03	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH03 See ADS1278 manual for details
s2	ACQ480_HPF_04	ACQ480:HPF:04	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH04 See ADS1278 manual for details
s2	ACQ480_HPF_05	ACQ480:HPF:05	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH05 See ADS1278 manual for details

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	ACQ480_HPF_06	ACQ480:HPF:06	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH06 See ADS1278 manual for details
s2	ACQ480_HPF_07	ACQ480:HPF:07	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH07 See ADS1278 manual for details
s2	ACQ480_HPF_08	ACQ480:HPF:08	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH08 See ADS1278 manual for details
s2	ACQ480_INVERT_01	ACQ480:INVERT:01	time_enum		REG, INV	rw	REG			Invert Input CH01 Could be useful for debug, or create a differential input from two channels
s2	ACQ480_INVERT_02	ACQ480:INVERT:02	time_enum		REG, INV	rw	REG			Invert Input CH02 Could be useful for debug, or create a differential input from two channels
s2	ACQ480_INVERT_03	ACQ480:INVERT:03	time_enum		REG, INV	rw	REG			Invert Input CH03 Could be useful for debug, or create a differential input from two channels
s2	ACQ480_INVERT_04	ACQ480:INVERT:04	time_enum		REG, INV	rw	REG			Invert Input CH04 Could be useful for debug, or create a differential input from two channels
s2	ACQ480_INVERT_05	ACQ480:INVERT:05	time_enum		REG, INV	rw	REG			Invert Input CH05 Could be useful for debug, or create a differential input from two channels
s2	ACQ480_INVERT_06	ACQ480:INVERT:06	time_enum		REG, INV	rw	REG			Invert Input CH06 Could be useful for debug, or create a differential input from two channels
s2	ACQ480_INVERT_07	ACQ480:INVERT:07	time_enum		REG, INV	rw	REG			Invert Input CH07 Could be useful for debug, or create a differential input from two channels
s2	ACQ480_INVERT_08	ACQ480:INVERT:08	time_enum		REG, INV	rw	REG			Invert Input CH08 Could be useful for debug, or create a differential input from two channels
s2	ACQ480_LFNS_01	ACQ480:LFNS:01	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH01 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s2	ACQ480_LFNS_02	ACQ480:LFNS:02	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH02 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s2	ACQ480_LFNS_03	ACQ480:LFNS:03	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH03 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s2	ACQ480_LFNS_04	ACQ480:LFNS:04	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH04 See ADS1278 manual for details. Only effective with ADC_FIR enabled

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	ACQ480_LFNS_05	ACQ480:LFNS:05	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH05 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s2	ACQ480_LFNS_06	ACQ480:LFNS:06	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH06 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s2	ACQ480_LFNS_07	ACQ480:LFNS:07	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH07 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s2	ACQ480_LFNS_08	ACQ480:LFNS:08	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH08 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s2	ACQ480_LFNS_ALL	ACQ480:LFNS:ALL	time_enum		OFF, ON	rw	OFF			
s2	ACQ480_MR_10DEC	ACQ480:MR:10DEC	time_enum		dec4, dec8, dec16, dec32	rw	dec4			
s2	ACQ480_MR_EN	ACQ480:MR:EN	time_enum		,	rw				ACQ480 Enable Multi Rate
s2	ACQ480_MR_EVSEL_0	ACQ480:MR:EVSEL:0	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			ACQ480 Multirate Signal Line Select 0
s2	ACQ480_MR_EVSEL_1	ACQ480:MR:EVSEL:1	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			ACQ480 Multirate Signal Line Select 1
s2	ACQ480_OSR	ACQ480:OSR	time_double			rw	2e+06	0	0	Shows ADC Output Sample Rate OSR = FS / ADC_DECIM / FPGA_DECIM
s2	ACQ480_T50R	ACQ480:T50R	time_enum		0, 1	rw				Enable 50 Ohm input impedance, all channels Initial Value: 0 PVType: time_enum
s2	ACQ480_T50R_01	ACQ480:T50R:01	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH01
s2	ACQ480_T50R_02	ACQ480:T50R:02	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH02
s2	ACQ480_T50R_03	ACQ480:T50R:03	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH03
s2	ACQ480_T50R_04	ACQ480:T50R:04	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH04
s2	ACQ480_T50R_05	ACQ480:T50R:05	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH05
s2	ACQ480_T50R_06	ACQ480:T50R:06	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH06
s2	ACQ480_T50R_07	ACQ480:T50R:07	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH07
s2	ACQ480_T50R_08	ACQ480:T50R:08	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH08
s2	ADC_CAL	ADC_CAL	time_enum		default, caldef, calibrated	rw	calibrated			Initial Value: calibrated PVType: time_enum

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	ADC_MODE	ADC_MODE	time_long			rw	-1	0	0	Indicates ADC is calibrated Initial Value: -1
s2	AGIX					rw	8			
s2		AI:TW:01								Raw Transient Waveform CH01
s2		AI:TW:01:V	time_long			rw		0	0	Cooked Transient Waveform CH01 Volts
s2		AI:TW:02								Raw Transient Waveform CH02
s2		AI:TW:02:V	time_long			rw		0	0	Cooked Transient Waveform CH02 Volts
s2		AI:TW:03								Raw Transient Waveform CH03
s2		AI:TW:03:V	time_long			rw		0	0	Cooked Transient Waveform CH03 Volts
s2		AI:TW:04								Raw Transient Waveform CH04
s2		AI:TW:04:V	time_long			rw		0	0	Cooked Transient Waveform CH04 Volts
s2		AI:TW:05								Raw Transient Waveform CH05
s2		AI:TW:05:V	time_long			rw		0	0	Cooked Transient Waveform CH05 Volts
s2		AI:TW:06								Raw Transient Waveform CH06
s2		AI:TW:06:V	time_long			rw		0	0	Cooked Transient Waveform CH06 Volts
s2		AI:TW:07								Raw Transient Waveform CH07
s2		AI:TW:07:V	time_long			rw		0	0	Cooked Transient Waveform CH07 Volts
s2		AI:TW:08								Raw Transient Waveform CH08
s2		AI:TW:08:V	time_long			rw		0	0	Cooked Transient Waveform CH08 Volts
s2		AI:WF:01	time_short			rw	array([], dtype=int16)	0	0	Analog input 01 waveform
s2		AI:WF:01:ACTIVE	time_double			rw		0	0	Live Waveform Status CH01 Function ACTIVE
s2		AI:WF:01:OVER	time_enum		,	rw				Live Waveform Status CH01 Function OVER
s2		AI:WF:01:TH	time_long			rw	512	0	0	Live Waveform Status CH01 Function TH
s2		AI:WF:01:UPDATES	time_double			rw		0	0	Live Waveform Status CH01 Function UPDATES
s2		AI:WF:01:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH01 Volts
s2		AI:WF:02	time_short			rw	array([], dtype=int16)	0	0	Analog input 02 waveform
s2		AI:WF:02:ACTIVE	time_double			rw		0	0	Live Waveform Status CH02 Function ACTIVE
s2		AI:WF:02:OVER	time_enum		,	rw				Live Waveform Status CH02 Function OVER
s2		AI:WF:02:TH	time_long			rw	512	0	0	Live Waveform Status CH02 Function TH
s2		AI:WF:02:UPDATES	time_double			rw		0	0	Live Waveform Status CH02 Function UPDATES
s2		AI:WF:02:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH02 Volts

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2		AI:WF:03	time_short			rw	array([], dtype=int16)	0	0	Analog input 03 waveform
s2		AI:WF:03:ACTIVE	time_double			rw		0	0	Live Waveform Status CH03 Function ACTIVE
s2		AI:WF:03:OVER	time_enum		,	rw				Live Waveform Status CH03 Function OVER
s2		AI:WF:03:TH	time_long			rw	512	0	0	Live Waveform Status CH03 Function TH
s2		AI:WF:03:UPDATES	time_double			rw		0	0	Live Waveform Status CH03 Function UPDATES
s2		AI:WF:03:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH03 Volts
s2		AI:WF:04	time_short			rw	array([], dtype=int16)	0	0	Analog input 04 waveform
s2		AI:WF:04:ACTIVE	time_double			rw		0	0	Live Waveform Status CH04 Function ACTIVE
s2		AI:WF:04:OVER	time_enum		,	rw				Live Waveform Status CH04 Function OVER
s2		AI:WF:04:TH	time_long			rw	512	0	0	Live Waveform Status CH04 Function TH
s2		AI:WF:04:UPDATES	time_double			rw		0	0	Live Waveform Status CH04 Function UPDATES
s2		AI:WF:04:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH04 Volts
s2		AI:WF:05	time_short			rw	array([], dtype=int16)	0	0	Analog input 05 waveform
s2		AI:WF:05:ACTIVE	time_double			rw		0	0	Live Waveform Status CH05 Function ACTIVE
s2		AI:WF:05:OVER	time_enum		,	rw				Live Waveform Status CH05 Function OVER
s2		AI:WF:05:TH	time_long			rw	512	0	0	Live Waveform Status CH05 Function TH
s2		AI:WF:05:UPDATES	time_double			rw		0	0	Live Waveform Status CH05 Function UPDATES
s2		AI:WF:05:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH05 Volts
s2		AI:WF:06	time_short			rw	array([], dtype=int16)	0	0	Analog input 06 waveform
s2		AI:WF:06:ACTIVE	time_double			rw		0	0	Live Waveform Status CH06 Function ACTIVE
s2		AI:WF:06:OVER	time_enum		,	rw				Live Waveform Status CH06 Function OVER
s2		AI:WF:06:TH	time_long			rw	512	0	0	Live Waveform Status CH06 Function TH
s2		AI:WF:06:UPDATES	time_double			rw		0	0	Live Waveform Status CH06 Function UPDATES
s2		AI:WF:06:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH06 Volts
s2		AI:WF:07	time_short			rw	array([], dtype=int16)	0	0	Analog input 07 waveform
s2		AI:WF:07:ACTIVE	time_double			rw		0	0	Live Waveform Status CH07 Function ACTIVE
s2		AI:WF:07:OVER	time_enum		,	rw				Live Waveform Status CH07 Function OVER
s2		AI:WF:07:TH	time_long			rw	512	0	0	Live Waveform Status CH07 Function TH
s2		AI:WF:07:UPDATES	time_double			rw		0	0	Live Waveform Status CH07 Function UPDATES
s2		AI:WF:07:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH07 Volts

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2		AI:WF:08	time_short			rw	array([], dtype=int16)	0	0	Analog input 08 waveform
s2		AI:WF:08:ACTIVE	time_double			rw		0	0	Live Waveform Status CH08 Function ACTIVE
s2		AI:WF:08:OVER	time_enum		,	rw				Live Waveform Status CH08 Function OVER
s2		AI:WF:08:TH	time_long			rw	512	0	0	Live Waveform Status CH08 Function TH
s2		AI:WF:08:UPDATES	time_double			rw		0	0	Live Waveform Status CH08 Function UPDATES
s2		AI:WF:08:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH08 Volts
s2		AI:WF:W1	time_long			rw		0	0	
s2		AI:WF:W2	time_long			rw		0	0	
s2	AI_CAL_EOFF	AI:CAL:EOFF	time_float			rw		0	0	AI Offset Calibration values, index from 0
s2	AI_CAL_ESLO	AI:CAL:ESLO	time_float			rw		0	0	AI Calibration Slope, index from 0
s2	acq480_dump					rwx	ffff 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 10:0000 0000 0000 0000 0000 0000 0000 0000 0000 00			acq480 low level control dump
s2	acq480_flush					rwx				acq480 low level control flush
s2	acq480_fpga_decim					r	10			acq480 low level control fpga_decim
s2	acq480_getGain					rwx				acq480 low level control getGain
s2	acq480_help					rwx	PLL [FSMSPS DECIM] dump flush getGain			acq480 low level control help
s2	acq480_loti					r	1			acq480 low level control loti
s2	acq480_makeLinks					rwx	ln -s /usr/local/bin/acq480_knobs acq480_PLL ln -s /usr/local/bin/acq480_knobs acq480_dump ln -s /usr/local/bin/acq480_k			acq480 low level control makeLinks
s2	acq480_map					rwx	MAP_CH1234_TO_OUT1A 1 MAP_CH1234_TO_OUT1B 1 2 MAP_CH1234_TO_OUT2A 2 MAP_CH1234_TO_OUT2B 2 2 MAP_CH1234_TO_OUT3A 3 MAP_CH			acq480 low level control map

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	acq480_PLL					rwx	getPLL() d1 = 0000			acq480 low level control PLL
s2	acq480_readall					rwx				acq480 low level control readall
s2	acq480_reg					rwx				acq480 low level control reg
s2	acq480_reset					rwx				acq480 low level control reset
s2	acq480_setAverageSelect					rwx				acq480 low level control setAverageSelect
s2	acq480_setClkHardSync					rwx				acq480 low level control setClkHardSync
s2	acq480_setDataPattern					rwx	-EN_2WIRE BTC_MODE -MSB_FIRST -EN_SDR -FALL_SDR			acq480 low level control setDataPattern
s2	acq480_setDataRate					rwx				acq480 low level control setDataRate
s2	acq480_setDecimationFilter					rwx				acq480 low level control setDecimationFilter
s2	acq480_setFilterCoefficients					rwx				acq480 low level control setFilterCoefficients
s2	acq480_setGain					rwx				acq480 low level control setGain
s2	acq480_setHiPassFilter					rwx				acq480 low level control setHiPassFilter
s2	acq480_setInvert					rwx				acq480 low level control setInvert
s2	acq480_setLFNS					rwx				acq480 low level control setLFNS
s2	acq480_setLvdsTestPatDeskew					rwx				acq480 low level control setLvdsTestPatDeskew
s2	acq480_setLvdsTestPatRamp					rwx				acq480 low level control setLvdsTestPatRamp
s2	acq480_setPatDeskew					rwx				acq480 low level control setPatDeskew
s2	acq480_setPatSync					rwx				acq480 low level control setPatSync
s2	acq480_setTwoWireMode					rwx				acq480 low level control setTwoWireMode
s2	acq480_train_ctrl					rw	0x00000000			acq480 low level control train_ctrl
s2	acq480_train_hi_val					r	0x00000000 0			acq480 low level control train_hi_val
s2	acq480_train_lo_val					r	0x00000000 4			acq480 low level control train_lo_val
s2	acq480_two_lane_mode					rw				acq480 low level control two_lane_mode
s2	acq482_cmap					rw	1			acq480 channel mapping 4|8
s2	burst					rw	burst=0,0,0 disable			

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	CLK	CLK	time_enum		internal, external	rw	internal			Select CLK type
s2	CLKDIV	CLKDIV	time_double			rw	1			Clock divider value
s2	CLK_DX	CLK:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			CLK source select DX
s2	CLK_SENSE	CLK:SENSE	time_enum		falling, rising	rw	falling			CLK source select SENSE
s2	COMPLETED_SHOT	COMPLETED_SHOT	time_long			rw		0	0	shot number of last completed shot
s2	DEC					rw	3,0,173			
s2	data32					rw				data is 32 bit (not 16 bit)
s2	EVENT0	EVENT0	time_enum		disable, enable	rw	disable			EVENT 0 Enable
s2	EVENT0_DX	EVENT0:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			EVENT 0 selector DX
s2	EVENT0_SENSE	EVENT0:SENSE	time_enum		falling, rising	rw	falling			EVENT 0 selector SENSE
s2	EVENT1	EVENT1	time_enum		disable, enable	rw	disable			EVENT 1 Enable
s2	EVENT1_DX	EVENT1:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			EVENT 1 selector DX
s2	EVENT1_SENSE	EVENT1:SENSE	time_enum		falling, rising	rw	falling			EVENT 1 selector SENSE
s2	evt_sc_latch					r				
s2	ffir_coeff					rw	13 "Permission denied" failed to open "ffir_coeff"			
s2	ffir_counter					r				
s2	ffir_reset					rw	13 "Permission denied" failed to open "ffir_reset"			

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	GAIN_01	GAIN:01	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_01
s2	GAIN_02	GAIN:02	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_02
s2	GAIN_03	GAIN:03	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_03
s2	GAIN_04	GAIN:04	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_04
s2	GAIN_05	GAIN:05	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_05
s2	GAIN_06	GAIN:06	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_06
s2	GAIN_07	GAIN:07	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_07
s2	GAIN_08	GAIN:08	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_08

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	GAIN_CHANGES	GAIN_CHANGES	time_double			rw	GAIN_CHANGES 0			indicates GAIN has changed
s2	HAS_CH_GAINS	HAS_CH_GAINS	time_enum		,	rw	HAS_CH_GAINS			Module has per channel GAIN control
s2	INTCLK_HZ	INTCLK_HZ	time_long	Hz		rw	500000	80000000	10000	Internal Clock frequency [integer Hz]
s2	IS_PRESENT	IS_PRESENT	time_enum		EMPTY, PRESENT	rw	PRESENT			module IS PRESENT
s2	is_adc					r	1 sar			
s2	MANUFACTURER					r	D-TACQ Solutions			Show module Manufacturer
s2	MAX_KHZ					rw	80000			
s2	MIN_KHZ					rw	10000			
s2	MODEL	MODEL	time_string			rw	ACQ482ELF			Module MODEL
s2	MTYPE	MTYPE	time_string			rw	8			Module Type Code
s2	module_variant					r	82			
s2	mr_10dec					rw				
s2	mr_en					rw				
s2	mr_sel0					rw				
s2	mr_sel1					rw				
s2	NCHAN	NCHAN	time_long			rw	8			Module number of channels
s2		OS:COUNT	time_double			rw		0	0	oversampling COUNT
s2		OS:FREQ	time_double	Hz		rw		0	0	oversampling FREQ
s2	PART_NUM	PART_NUM	time_string			rw	ACQ482ELF-16-2V5-H			Module full part number
s2	prompt						None			prompt {0|1} changes command prompt
s2	ROLE	ROLE	time_string			rw	SLAVE			
s2	SERIAL	SERIAL	time_string			rw	E48211105			Module Serial Number
s2	SHOT	SHOT	time_long			rw		0	0	Shot Number
s2	SHOT_IN_PROGRESS	SHOT_IN_PROGRESS	time_double			rw		0	0	shot is in progress.
s2	SIG_CLK_COUNT_ACTIVE	SIG:CLK_COUNT:ACTIVE	time_double			rw	1.0	0	0	CLK counter signal COUNT value ACTIVE
s2	SIG_CLK_COUNT_COUNT	SIG:CLK_COUNT:COUNT	time_double			rw	6e+09	0	0	CLK counter signal COUNT value COUNT
s2	SIG_CLK_COUNT_FREQ	SIG:CLK_COUNT:FREQ	time_double	Hz		rw	2e+07	0	0	CLK counter signal COUNT value FREQ

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	SIG_CLK_COUNT_RESET	SIG:CLK_COUNT:RESET	time_enum		,	rw				CLK counter signal COUNT value RESET
s2	SIG_CLK_TRAIN_BSY	SIG:CLK:TRAIN_BSY	time_enum		0, 1	rw				Clock Training is BUSY
s2	SIG_clk_count_ACTIVE	SIG:clk_count:ACTIVE	time_double			rw	1.0	0	0	Signal counter clk value ACTIVE
s2	SIG_clk_count_COUNT	SIG:clk_count:COUNT	time_double			rw	6e+09	0	0	Signal counter clk value COUNT
s2	SIG_clk_count_FREQ	SIG:clk_count:FREQ	time_double	Hz		rw	2e+07	0	0	Signal counter clk value FREQ
s2	SIG_clk_count_RESET	SIG:clk_count:RESET	time_enum		,	rw				Signal counter clk value RESET
s2	SIG_SAMPLE_COUNT_ACTIVE	SIG:SAMPLE_COUNT:ACTIVE	time_double			rw	0.0	0	0	Signal Sample Count Value ACTIVE
s2	SIG_SAMPLE_COUNT_COUNT	SIG:SAMPLE_COUNT:COUNT	time_double			rw		0	0	Signal Sample Count Value COUNT
s2	SIG_SAMPLE_COUNT_FREQ	SIG:SAMPLE_COUNT:FREQ	time_double	Hz		rw	0.000	0	0	Signal Sample Count Value FREQ
s2	SIG_SAMPLE_COUNT_RESET	SIG:SAMPLE_COUNT:RESET	time_enum		,	rw				Signal Sample Count Value RESET
s2	SIG_SAMPLE_COUNT_RUNTIME	SIG:SAMPLE_COUNT:RUNTIME	time_double	s		rw		9999999	0	Signal Sample Count Value RUNTIME
s2	SIG_sample_count_ACTIVE	SIG:sample_count:ACTIVE	time_double			rw	0.0	0	0	Signal counter sample value ACTIVE
s2	SIG_sample_count_COUNT	SIG:sample_count:COUNT	time_double			rw		0	0	Signal counter sample value COUNT
s2	SIG_sample_count_FREQ	SIG:sample_count:FREQ	time_double	Hz		rw	0.000	0	0	Signal counter sample value FREQ
s2	SIG_sample_count_RESET	SIG:sample_count:RESET	time_enum		,	rw				Signal counter sample value RESET
s2	SYNC	SYNC	time_enum		internal, external	rw	internal			SYNC enable
s2	SYNC_DX	SYNC:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			SYNC DX selector
s2	SYNC_SENSE	SYNC:SENSE	time_enum		falling, rising	rw	falling			SYNC SENSE selector
s2	sod					rw				Sample On Demand: single sample mode (where available)
s2	T50R						None			Set 50 Ohm Impedance all channels.
s2	T50R_1					rw				Select 50 Ohm impedance CH1
s2	T50R_2					rw				Select 50 Ohm impedance CH2
s2	T50R_3					rw				Select 50 Ohm impedance CH3
s2	T50R_4					rw				Select 50 Ohm impedance CH4
s2	T50R_5					rw				Select 50 Ohm impedance CH5
s2	T50R_6					rw				Select 50 Ohm impedance CH6
s2	T50R_7					rw				Select 50 Ohm impedance CH7

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s2	T50R_8					rw				Select 50 Ohm impedance CH8
s2	TRG	TRG	time_enum		none, enable	rw	enable			TRG enable
s2	TRG_DX	TRG:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			trigger source DX select
s2	TRG_SENSE	TRG:SENSE	time_enum		falling, rising	rw	rising			trigger source SENSE select
s2	TRIGGERED	TRIGGERED	time_enum		NO, YES	rw	NO			
s2	task_active					r				

Site **s3**
EpicsPV Prefix **acq2106_340:3:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	ACC					rw	4,0,173			
s3		ACQ480:HAS_50R	time_enum		0, 1	rw	1			50 ohm option _is_ available on this module
s3		ACQ480:JC:LOL	time_enum		0, 1	rw	1			Jitter Cleaner Loss Of Lock failure status
s3		ACQ480:JC:LOS	time_enum		0, 1	rw				Jitter Cleaner Los Of Signal failure status
s3		ACQ480:JC:PRESENT	time_enum		0, 1	rw	1			Jitter Cleaner is Present status
s3		ACQ480:LINK:LOTI	time_enum		0, 1	rw	1			Link Loss Of Training Indicator
s3		ACQ480:TRAIN	time_enum		ACQ480_RESET, ACQ480_START, ACQ480_DESKE	rw	ACQ480_RESET			Indicator shows ACQ480 Training state
s3		ACQ480:TWO_LANE	time_enum		0, 1	rw				ADC has two lanes per bit enabled for 80MSPS operation
s3	ACQ480_FIR_01	ACQ480:FIR:01	time_enum		DISABLE, LP_ODD_D2, HP_ODD_D2, LP_EVEN_D	rw	DISABLE			Select ADC FIR Filter. 01 masters 02..08
s3	ACQ480_FIR_DECIM	ACQ480:FIR:DECIM	time_double			rw	1	0	0	Shows Decimation due to ADC FIR selection
s3	ACQ480_FPGA_DECIM	ACQ480:FPGA:DECIM	time_long			rw	10	0	0	Show decimation due to FPGA 1, 4, 10 ..selected on FPGA personality load.
s3	ACQ480_GAIN_01	ACQ480:GAIN:01	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH01
s3	ACQ480_GAIN_02	ACQ480:GAIN:02	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH02
s3	ACQ480_GAIN_03	ACQ480:GAIN:03	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH03
s3	ACQ480_GAIN_04	ACQ480:GAIN:04	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH04
s3	ACQ480_GAIN_05	ACQ480:GAIN:05	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH05
s3	ACQ480_GAIN_06	ACQ480:GAIN:06	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH06

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	ACQ480_GAIN_07	ACQ480:GAIN:07	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH07
s3	ACQ480_GAIN_08	ACQ480:GAIN:08	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH08
s3	ACQ480_GAIN_ALL	ACQ480:GAIN:ALL	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			
s3	ACQ480_HPF_01	ACQ480:HPF:01	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH01 See ADS1278 manual for details
s3	ACQ480_HPF_02	ACQ480:HPF:02	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH02 See ADS1278 manual for details
s3	ACQ480_HPF_03	ACQ480:HPF:03	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH03 See ADS1278 manual for details
s3	ACQ480_HPF_04	ACQ480:HPF:04	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH04 See ADS1278 manual for details
s3	ACQ480_HPF_05	ACQ480:HPF:05	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH05 See ADS1278 manual for details

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	ACQ480_HPF_06	ACQ480:HPF:06	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH06 See ADS1278 manual for details
s3	ACQ480_HPF_07	ACQ480:HPF:07	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH07 See ADS1278 manual for details
s3	ACQ480_HPF_08	ACQ480:HPF:08	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH08 See ADS1278 manual for details
s3	ACQ480_INVERT_01	ACQ480:INVERT:01	time_enum		REG, INV	rw	REG			Invert Input CH01 Could be useful for debug, or create a differential input from two channels
s3	ACQ480_INVERT_02	ACQ480:INVERT:02	time_enum		REG, INV	rw	REG			Invert Input CH02 Could be useful for debug, or create a differential input from two channels
s3	ACQ480_INVERT_03	ACQ480:INVERT:03	time_enum		REG, INV	rw	REG			Invert Input CH03 Could be useful for debug, or create a differential input from two channels
s3	ACQ480_INVERT_04	ACQ480:INVERT:04	time_enum		REG, INV	rw	REG			Invert Input CH04 Could be useful for debug, or create a differential input from two channels
s3	ACQ480_INVERT_05	ACQ480:INVERT:05	time_enum		REG, INV	rw	REG			Invert Input CH05 Could be useful for debug, or create a differential input from two channels
s3	ACQ480_INVERT_06	ACQ480:INVERT:06	time_enum		REG, INV	rw	REG			Invert Input CH06 Could be useful for debug, or create a differential input from two channels
s3	ACQ480_INVERT_07	ACQ480:INVERT:07	time_enum		REG, INV	rw	REG			Invert Input CH07 Could be useful for debug, or create a differential input from two channels
s3	ACQ480_INVERT_08	ACQ480:INVERT:08	time_enum		REG, INV	rw	REG			Invert Input CH08 Could be useful for debug, or create a differential input from two channels
s3	ACQ480_LFNS_01	ACQ480:LFNS:01	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH01 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s3	ACQ480_LFNS_02	ACQ480:LFNS:02	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH02 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s3	ACQ480_LFNS_03	ACQ480:LFNS:03	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH03 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s3	ACQ480_LFNS_04	ACQ480:LFNS:04	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH04 See ADS1278 manual for details. Only effective with ADC_FIR enabled

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	ACQ480_LFNS_05	ACQ480:LFNS:05	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH05 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s3	ACQ480_LFNS_06	ACQ480:LFNS:06	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH06 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s3	ACQ480_LFNS_07	ACQ480:LFNS:07	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH07 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s3	ACQ480_LFNS_08	ACQ480:LFNS:08	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH08 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s3	ACQ480_LFNS_ALL	ACQ480:LFNS:ALL	time_enum		OFF, ON	rw	OFF			
s3	ACQ480_MR_10DEC	ACQ480:MR:10DEC	time_enum		dec4, dec8, dec16, dec32	rw	dec4			
s3	ACQ480_MR_EN	ACQ480:MR:EN	time_enum		,	rw				ACQ480 Enable Multi Rate
s3	ACQ480_MR_EVSEL_0	ACQ480:MR:EVSEL:0	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			ACQ480 Multirate Signal Line Select 0
s3	ACQ480_MR_EVSEL_1	ACQ480:MR:EVSEL:1	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			ACQ480 Multirate Signal Line Select 1
s3	ACQ480_OSR	ACQ480:OSR	time_double			rw	2e+06	0	0	Shows ADC Output Sample Rate OSR = FS / ADC_DECIM / FPGA_DECIM
s3	ACQ480_T50R	ACQ480:T50R	time_enum		0, 1	rw				Enable 50 Ohm input impedance, all channels Initial Value: 0 PVType: time_enum
s3	ACQ480_T50R_01	ACQ480:T50R:01	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH01
s3	ACQ480_T50R_02	ACQ480:T50R:02	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH02
s3	ACQ480_T50R_03	ACQ480:T50R:03	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH03
s3	ACQ480_T50R_04	ACQ480:T50R:04	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH04
s3	ACQ480_T50R_05	ACQ480:T50R:05	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH05
s3	ACQ480_T50R_06	ACQ480:T50R:06	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH06
s3	ACQ480_T50R_07	ACQ480:T50R:07	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH07
s3	ACQ480_T50R_08	ACQ480:T50R:08	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH08
s3	ADC_CAL	ADC_CAL	time_enum		default, caldef, calibrated	rw	calibrated			Initial Value: calibrated PVType: time_enum

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	ADC_MODE	ADC_MODE	time_long			rw	-1	0	0	Indicates ADC is calibrated Initial Value: -1
s3	AGIX					rw	16			
s3		AI:TW:01								Raw Transient Waveform CH01
s3		AI:TW:01:V	time_long			rw		0	0	Cooked Transient Waveform CH01 Volts
s3		AI:TW:02								Raw Transient Waveform CH02
s3		AI:TW:02:V	time_long			rw		0	0	Cooked Transient Waveform CH02 Volts
s3		AI:TW:03								Raw Transient Waveform CH03
s3		AI:TW:03:V	time_long			rw		0	0	Cooked Transient Waveform CH03 Volts
s3		AI:TW:04								Raw Transient Waveform CH04
s3		AI:TW:04:V	time_long			rw		0	0	Cooked Transient Waveform CH04 Volts
s3		AI:TW:05								Raw Transient Waveform CH05
s3		AI:TW:05:V	time_long			rw		0	0	Cooked Transient Waveform CH05 Volts
s3		AI:TW:06								Raw Transient Waveform CH06
s3		AI:TW:06:V	time_long			rw		0	0	Cooked Transient Waveform CH06 Volts
s3		AI:TW:07								Raw Transient Waveform CH07
s3		AI:TW:07:V	time_long			rw		0	0	Cooked Transient Waveform CH07 Volts
s3		AI:TW:08								Raw Transient Waveform CH08
s3		AI:TW:08:V	time_long			rw		0	0	Cooked Transient Waveform CH08 Volts
s3		AI:WF:01	time_short			rw	array([], dtype=int16)	0	0	Analog input 01 waveform
s3		AI:WF:01:ACTIVE	time_double			rw		0	0	Live Waveform Status CH01 Function ACTIVE
s3		AI:WF:01:OVER	time_enum		,	rw				Live Waveform Status CH01 Function OVER
s3		AI:WF:01:TH	time_long			rw	512	0	0	Live Waveform Status CH01 Function TH
s3		AI:WF:01:UPDATES	time_double			rw		0	0	Live Waveform Status CH01 Function UPDATES
s3		AI:WF:01:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH01 Volts
s3		AI:WF:02	time_short			rw	array([], dtype=int16)	0	0	Analog input 02 waveform
s3		AI:WF:02:ACTIVE	time_double			rw		0	0	Live Waveform Status CH02 Function ACTIVE
s3		AI:WF:02:OVER	time_enum		,	rw				Live Waveform Status CH02 Function OVER
s3		AI:WF:02:TH	time_long			rw	512	0	0	Live Waveform Status CH02 Function TH
s3		AI:WF:02:UPDATES	time_double			rw		0	0	Live Waveform Status CH02 Function UPDATES
s3		AI:WF:02:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH02 Volts

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3		AI:WF:03	time_short			rw	array([], dtype=int16)	0	0	Analog input 03 waveform
s3		AI:WF:03:ACTIVE	time_double			rw		0	0	Live Waveform Status CH03 Function ACTIVE
s3		AI:WF:03:OVER	time_enum		,	rw				Live Waveform Status CH03 Function OVER
s3		AI:WF:03:TH	time_long			rw	512	0	0	Live Waveform Status CH03 Function TH
s3		AI:WF:03:UPDATES	time_double			rw		0	0	Live Waveform Status CH03 Function UPDATES
s3		AI:WF:03:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH03 Volts
s3		AI:WF:04	time_short			rw	array([], dtype=int16)	0	0	Analog input 04 waveform
s3		AI:WF:04:ACTIVE	time_double			rw		0	0	Live Waveform Status CH04 Function ACTIVE
s3		AI:WF:04:OVER	time_enum		,	rw				Live Waveform Status CH04 Function OVER
s3		AI:WF:04:TH	time_long			rw	512	0	0	Live Waveform Status CH04 Function TH
s3		AI:WF:04:UPDATES	time_double			rw		0	0	Live Waveform Status CH04 Function UPDATES
s3		AI:WF:04:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH04 Volts
s3		AI:WF:05	time_short			rw	array([], dtype=int16)	0	0	Analog input 05 waveform
s3		AI:WF:05:ACTIVE	time_double			rw		0	0	Live Waveform Status CH05 Function ACTIVE
s3		AI:WF:05:OVER	time_enum		,	rw				Live Waveform Status CH05 Function OVER
s3		AI:WF:05:TH	time_long			rw	512	0	0	Live Waveform Status CH05 Function TH
s3		AI:WF:05:UPDATES	time_double			rw		0	0	Live Waveform Status CH05 Function UPDATES
s3		AI:WF:05:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH05 Volts
s3		AI:WF:06	time_short			rw	array([], dtype=int16)	0	0	Analog input 06 waveform
s3		AI:WF:06:ACTIVE	time_double			rw		0	0	Live Waveform Status CH06 Function ACTIVE
s3		AI:WF:06:OVER	time_enum		,	rw				Live Waveform Status CH06 Function OVER
s3		AI:WF:06:TH	time_long			rw	512	0	0	Live Waveform Status CH06 Function TH
s3		AI:WF:06:UPDATES	time_double			rw		0	0	Live Waveform Status CH06 Function UPDATES
s3		AI:WF:06:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH06 Volts
s3		AI:WF:07	time_short			rw	array([], dtype=int16)	0	0	Analog input 07 waveform
s3		AI:WF:07:ACTIVE	time_double			rw		0	0	Live Waveform Status CH07 Function ACTIVE
s3		AI:WF:07:OVER	time_enum		,	rw				Live Waveform Status CH07 Function OVER
s3		AI:WF:07:TH	time_long			rw	512	0	0	Live Waveform Status CH07 Function TH
s3		AI:WF:07:UPDATES	time_double			rw		0	0	Live Waveform Status CH07 Function UPDATES
s3		AI:WF:07:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH07 Volts

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3		AI:WF:08	time_short			rw	array([], dtype=int16)	0	0	Analog input 08 waveform
s3		AI:WF:08:ACTIVE	time_double			rw		0	0	Live Waveform Status CH08 Function ACTIVE
s3		AI:WF:08:OVER	time_enum		,	rw				Live Waveform Status CH08 Function OVER
s3		AI:WF:08:TH	time_long			rw	512	0	0	Live Waveform Status CH08 Function TH
s3		AI:WF:08:UPDATES	time_double			rw		0	0	Live Waveform Status CH08 Function UPDATES
s3		AI:WF:08:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH08 Volts
s3		AI:WF:W1	time_long			rw		0	0	
s3		AI:WF:W2	time_long			rw		0	0	
s3	AI_CAL_EOFF	AI:CAL:EOFF	time_float			rw		0	0	AI Offset Calibration values, index from 0
s3	AI_CAL_ESLO	AI:CAL:ESLO	time_float			rw		0	0	AI Calibration Slope, index from 0
s3	acq480_dump					rwx	ffff 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 10:0000 0000 0000 0000 0000 0000 0000 0000 0000 00			acq480 low level control dump
s3	acq480_flush					rwx				acq480 low level control flush
s3	acq480_fpga_decim					r	10			acq480 low level control fpga_decim
s3	acq480_getGain					rwx				acq480 low level control getGain
s3	acq480_help					rwx	PLL [FSMSPS DECIM] dump flush getGain			acq480 low level control help
s3	acq480_loti					r	1			acq480 low level control loti
s3	acq480_makeLinks					rwx	ln -s /usr/local/bin/acq480_knobs acq480_PLL ln -s /usr/local/bin/acq480_knobs acq480_dump ln -s /usr/local/bin/acq480_k			acq480 low level control makeLinks
s3	acq480_map					rwx	MAP_CH1234_TO_OUT1A 1 MAP_CH1234_TO_OUT1B 1 2 MAP_CH1234_TO_OUT2A 2 MAP_CH1234_TO_OUT2B 2 2 MAP_CH1234_TO_OUT3A 3 MAP_CH			acq480 low level control map

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	acq480_PLL					rwx	getPLL() d1 = 0000			acq480 low level control PLL
s3	acq480_readall					rwx				acq480 low level control readall
s3	acq480_reg					rwx				acq480 low level control reg
s3	acq480_reset					rwx				acq480 low level control reset
s3	acq480_setAverageSelect					rwx				acq480 low level control setAverageSelect
s3	acq480_setClkHardSync					rwx				acq480 low level control setClkHardSync
s3	acq480_setDataPattern					rwx	-EN_2WIRE BTC_MODE -MSB_FIRST -EN_SDR -FALL_SDR			acq480 low level control setDataPattern
s3	acq480_setDataRate					rwx				acq480 low level control setDataRate
s3	acq480_setDecimationFilter					rwx				acq480 low level control setDecimationFilter
s3	acq480_setFilterCoefficients					rwx				acq480 low level control setFilterCoefficients
s3	acq480_setGain					rwx				acq480 low level control setGain
s3	acq480_setHiPassFilter					rwx				acq480 low level control setHiPassFilter
s3	acq480_setInvert					rwx				acq480 low level control setInvert
s3	acq480_setLFNS					rwx				acq480 low level control setLFNS
s3	acq480_setLvdsTestPatDeskew					rwx				acq480 low level control setLvdsTestPatDeskew
s3	acq480_setLvdsTestPatRamp					rwx				acq480 low level control setLvdsTestPatRamp
s3	acq480_setPatDeskew					rwx				acq480 low level control setPatDeskew
s3	acq480_setPatSync					rwx				acq480 low level control setPatSync
s3	acq480_setTwoWireMode					rwx				acq480 low level control setTwoWireMode
s3	acq480_train_ctrl					rw	0x00000000			acq480 low level control train_ctrl
s3	acq480_train_hi_val					r	0x00000000 0			acq480 low level control train_hi_val
s3	acq480_train_lo_val					r	0x00000000 4			acq480 low level control train_lo_val
s3	acq480_two_lane_mode					rw				acq480 low level control two_lane_mode
s3	acq482_cmap					rw	1			acq480 channel mapping 4|8
s3	burst					rw	burst=0,0,0 disable			

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	CLK	CLK	time_enum		internal, external	rw	internal			Select CLK type
s3	CLKDIV	CLKDIV	time_double			rw	1			Clock divider value
s3	CLK_DX	CLK:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			CLK source select DX
s3	CLK_SENSE	CLK:SENSE	time_enum		falling, rising	rw	falling			CLK source select SENSE
s3	COMPLETED_SHOT	COMPLETED_SHOT	time_long			rw		0	0	shot number of last completed shot
s3	DEC					rw	4,0,173			
s3	data32					rw				data is 32 bit (not 16 bit)
s3	EVENT0	EVENT0	time_enum		disable, enable	rw	disable			EVENT 0 Enable
s3	EVENT0_DX	EVENT0:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			EVENT 0 selector DX
s3	EVENT0_SENSE	EVENT0:SENSE	time_enum		falling, rising	rw	falling			EVENT 0 selector SENSE
s3	EVENT1	EVENT1	time_enum		disable, enable	rw	disable			EVENT 1 Enable
s3	EVENT1_DX	EVENT1:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			EVENT 1 selector DX
s3	EVENT1_SENSE	EVENT1:SENSE	time_enum		falling, rising	rw	falling			EVENT 1 selector SENSE
s3	evt_sc_latch					r				
s3	ffir_coeff					rw	13 "Permission denied" failed to open "ffir_coeff"			
s3	ffir_counter					r				
s3	ffir_reset					rw	13 "Permission denied" failed to open "ffir_reset"			

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	GAIN_01	GAIN:01	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_01
s3	GAIN_02	GAIN:02	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_02
s3	GAIN_03	GAIN:03	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_03
s3	GAIN_04	GAIN:04	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_04
s3	GAIN_05	GAIN:05	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_05
s3	GAIN_06	GAIN:06	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_06
s3	GAIN_07	GAIN:07	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_07
s3	GAIN_08	GAIN:08	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_08

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	GAIN_CHANGES	GAIN_CHANGES	time_double			rw	GAIN_CHANGES 0			indicates GAIN has changed
s3	HAS_CH_GAINS	HAS_CH_GAINS	time_enum		,	rw	HAS_CH_GAINS			Module has per channel GAIN control
s3	INTCLK_HZ	INTCLK_HZ	time_long	Hz		rw	500000	80000000	10000	Internal Clock frequency [integer Hz]
s3	IS_PRESENT	IS_PRESENT	time_enum		EMPTY, PRESENT	rw	PRESENT			module IS PRESENT
s3	is_adc					r	1 sar			
s3	JC_LOL					rw	1			JC Jitter Cleaner State LOL
s3	JC_LOS					rw				JC Jitter Cleaner State LOS
s3	MANUFACTURER					r	D-TACQ Solutions			Show module Manufacturer
s3	MAX_KHZ					rw	80000			
s3	MIN_KHZ					rw	10000			
s3	MODEL	MODEL	time_string			rw	ACQ482ELF			Module MODEL
s3	MTYPE	MTYPE	time_string			rw	8			Module Type Code
s3	module_variant					r	82			
s3	mr_10dec					rw				
s3	mr_en					rw				
s3	mr_sel0					rw				
s3	mr_sel1					rw				
s3	NCHAN	NCHAN	time_long			rw	8			Module number of channels
s3		OS:COUNT	time_double			rw		0	0	oversampling COUNT
s3		OS:FREQ	time_double	Hz		rw		0	0	oversampling FREQ
s3	PART_NUM	PART_NUM	time_string			rw	ACQ482ELF-16-2V5-H			Module full part number
s3	prompt						None			prompt {0|1} changes command prompt
s3	ROLE	ROLE	time_string			rw	SLAVE			
s3	SERIAL	SERIAL	time_string			rw	E48210106			Module Serial Number
s3	SHOT	SHOT	time_long			rw		0	0	Shot Number
s3	SHOT_IN_PROGRESS	SHOT_IN_PROGRESS	time_double			rw		0	0	shot is in progress.
s3	SIG_CLK_COUNT_ACTIVE	SIG:CLK_COUNT:ACTIVE	time_double			rw	1.0	0	0	CLK counter signal COUNT value ACTIVE

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	SIG_CLK_COUNT_COUNT	SIG:CLK_COUNT:COUNT	time_double			rw	6e+09	0	0	CLK counter signal COUNT value COUNT
s3	SIG_CLK_COUNT_FREQ	SIG:CLK_COUNT:FREQ	time_double	Hz		rw	2e+07	0	0	CLK counter signal COUNT value FREQ
s3	SIG_CLK_COUNT_RESET	SIG:CLK_COUNT:RESET	time_enum		,	rw				CLK counter signal COUNT value RESET
s3	SIG_CLK_TRAIN_BSY	SIG:CLK:TRAIN_BSY	time_enum		0, 1	rw				Clock Training is BUSY
s3	SIG_clk_count_ACTIVE	SIG:clk_count:ACTIVE	time_double			rw	1.0	0	0	Signal counter clk value ACTIVE
s3	SIG_clk_count_COUNT	SIG:clk_count:COUNT	time_double			rw	6e+09	0	0	Signal counter clk value COUNT
s3	SIG_clk_count_FREQ	SIG:clk_count:FREQ	time_double	Hz		rw	2e+07	0	0	Signal counter clk value FREQ
s3	SIG_clk_count_RESET	SIG:clk_count:RESET	time_enum		,	rw				Signal counter clk value RESET
s3	SIG_SAMPLE_COUNT_ACTIVE	SIG:SAMPLE_COUNT:ACTIVE	time_double			rw	0.0	0	0	Signal Sample Count Value ACTIVE
s3	SIG_SAMPLE_COUNT_COUNT	SIG:SAMPLE_COUNT:COUNT	time_double			rw		0	0	Signal Sample Count Value COUNT
s3	SIG_SAMPLE_COUNT_FREQ	SIG:SAMPLE_COUNT:FREQ	time_double	Hz		rw	0.000	0	0	Signal Sample Count Value FREQ
s3	SIG_SAMPLE_COUNT_RESET	SIG:SAMPLE_COUNT:RESET	time_enum		,	rw				Signal Sample Count Value RESET
s3	SIG_SAMPLE_COUNT_RUNTIME	SIG:SAMPLE_COUNT:RUNTIME	time_double	s		rw		9999999	0	Signal Sample Count Value RUNTIME
s3	SIG_sample_count_ACTIVE	SIG:sample_count:ACTIVE	time_double			rw	0.0	0	0	Signal counter sample value ACTIVE
s3	SIG_sample_count_COUNT	SIG:sample_count:COUNT	time_double			rw		0	0	Signal counter sample value COUNT
s3	SIG_sample_count_FREQ	SIG:sample_count:FREQ	time_double	Hz		rw	0.000	0	0	Signal counter sample value FREQ
s3	SIG_sample_count_RESET	SIG:sample_count:RESET	time_enum		,	rw				Signal counter sample value RESET
s3	SYNC	SYNC	time_enum		internal, external	rw	internal			SYNC enable
s3	SYNC_DX	SYNC:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			SYNC DX selector
s3	SYNC_SENSE	SYNC:SENSE	time_enum		falling, rising	rw	falling			SYNC SENSE selector
s3	sod					rw				Sample On Demand: single sample mode (where available)
s3	T50R						None			Set 50 Ohm Impedance all channels.
s3	T50R_1					rw				Select 50 Ohm impedance CH1
s3	T50R_2					rw				Select 50 Ohm impedance CH2
s3	T50R_3					rw				Select 50 Ohm impedance CH3
s3	T50R_4					rw				Select 50 Ohm impedance CH4
s3	T50R_5					rw				Select 50 Ohm impedance CH5

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s3	T50R_6					rw				Select 50 Ohm impedance CH6
s3	T50R_7					rw				Select 50 Ohm impedance CH7
s3	T50R_8					rw				Select 50 Ohm impedance CH8
s3	TRG	TRG	time_enum		none, enable	rw	enable			TRG enable
s3	TRG_DX	TRG:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			trigger source DX select
s3	TRG_SENSE	TRG:SENSE	time_enum		falling, rising	rw	rising			trigger source SENSE select
s3	TRIGGERED	TRIGGERED	time_enum		NO, YES	rw	NO			
s3	task_active					r				

Site **s4**
EpicsPV Prefix **acq2106_340:4:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	ACC					rw	5,0,173			
s4		ACQ480:HAS_50R	time_enum		0, 1	rw	1			50 ohm option _is_ available on this module
s4		ACQ480:JC:LOL	time_enum		0, 1	rw				Jitter Cleaner Loss Of Lock failure status
s4		ACQ480:JC:LOS	time_enum		0, 1	rw				Jitter Cleaner Los Of Signal failure status
s4		ACQ480:JC:PRESENT	time_enum		0, 1	rw				Jitter Cleaner is Present status
s4		ACQ480:LINK:LOTI	time_enum		0, 1	rw	1			Link Loss Of Training Indicator
s4		ACQ480:TRAIN	time_enum		ACQ480_RESET, ACQ480_START, ACQ480_DESKE	rw	ACQ480_RESET			Indicator shows ACQ480 Training state
s4		ACQ480:TWO_LANE	time_enum		0, 1	rw				ADC has two lanes per bit enabled for 80MSPS operation
s4	ACQ480_FIR_01	ACQ480:FIR:01	time_enum		DISABLE, LP_ODD_D2, HP_ODD_D2, LP_EVEN_D	rw	DISABLE			Select ADC FIR Filter. 01 masters 02..08
s4	ACQ480_FIR_DECIM	ACQ480:FIR:DECIM	time_double			rw	1	0	0	Shows Decimation due to ADC FIR selection
s4	ACQ480_FPGA_DECIM	ACQ480:FPGA:DECIM	time_long			rw	10	0	0	Show decimation due to FPGA 1, 4, 10 ..selected on FPGA personality load.
s4	ACQ480_GAIN_01	ACQ480:GAIN:01	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH01
s4	ACQ480_GAIN_02	ACQ480:GAIN:02	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH02
s4	ACQ480_GAIN_03	ACQ480:GAIN:03	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH03
s4	ACQ480_GAIN_04	ACQ480:GAIN:04	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH04
s4	ACQ480_GAIN_05	ACQ480:GAIN:05	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH05
s4	ACQ480_GAIN_06	ACQ480:GAIN:06	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH06

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	ACQ480_GAIN_07	ACQ480:GAIN:07	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH07
s4	ACQ480_GAIN_08	ACQ480:GAIN:08	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH08
s4	ACQ480_GAIN_ALL	ACQ480:GAIN:ALL	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			
s4	ACQ480_HPF_01	ACQ480:HPF:01	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH01 See ADS1278 manual for details
s4	ACQ480_HPF_02	ACQ480:HPF:02	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH02 See ADS1278 manual for details
s4	ACQ480_HPF_03	ACQ480:HPF:03	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH03 See ADS1278 manual for details
s4	ACQ480_HPF_04	ACQ480:HPF:04	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH04 See ADS1278 manual for details
s4	ACQ480_HPF_05	ACQ480:HPF:05	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH05 See ADS1278 manual for details

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	ACQ480_HPF_06	ACQ480:HPF:06	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH06 See ADS1278 manual for details
s4	ACQ480_HPF_07	ACQ480:HPF:07	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH07 See ADS1278 manual for details
s4	ACQ480_HPF_08	ACQ480:HPF:08	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH08 See ADS1278 manual for details
s4	ACQ480_INVERT_01	ACQ480:INVERT:01	time_enum		REG, INV	rw	REG			Invert Input CH01 Could be useful for debug, or create a differential input from two channels
s4	ACQ480_INVERT_02	ACQ480:INVERT:02	time_enum		REG, INV	rw	REG			Invert Input CH02 Could be useful for debug, or create a differential input from two channels
s4	ACQ480_INVERT_03	ACQ480:INVERT:03	time_enum		REG, INV	rw	REG			Invert Input CH03 Could be useful for debug, or create a differential input from two channels
s4	ACQ480_INVERT_04	ACQ480:INVERT:04	time_enum		REG, INV	rw	REG			Invert Input CH04 Could be useful for debug, or create a differential input from two channels
s4	ACQ480_INVERT_05	ACQ480:INVERT:05	time_enum		REG, INV	rw	REG			Invert Input CH05 Could be useful for debug, or create a differential input from two channels
s4	ACQ480_INVERT_06	ACQ480:INVERT:06	time_enum		REG, INV	rw	REG			Invert Input CH06 Could be useful for debug, or create a differential input from two channels
s4	ACQ480_INVERT_07	ACQ480:INVERT:07	time_enum		REG, INV	rw	REG			Invert Input CH07 Could be useful for debug, or create a differential input from two channels
s4	ACQ480_INVERT_08	ACQ480:INVERT:08	time_enum		REG, INV	rw	REG			Invert Input CH08 Could be useful for debug, or create a differential input from two channels
s4	ACQ480_LFNS_01	ACQ480:LFNS:01	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH01 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s4	ACQ480_LFNS_02	ACQ480:LFNS:02	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH02 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s4	ACQ480_LFNS_03	ACQ480:LFNS:03	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH03 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s4	ACQ480_LFNS_04	ACQ480:LFNS:04	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH04 See ADS1278 manual for details. Only effective with ADC_FIR enabled

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	ACQ480_LFNS_05	ACQ480:LFNS:05	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH05 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s4	ACQ480_LFNS_06	ACQ480:LFNS:06	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH06 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s4	ACQ480_LFNS_07	ACQ480:LFNS:07	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH07 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s4	ACQ480_LFNS_08	ACQ480:LFNS:08	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH08 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s4	ACQ480_LFNS_ALL	ACQ480:LFNS:ALL	time_enum		OFF, ON	rw	OFF			
s4	ACQ480_MR_10DEC	ACQ480:MR:10DEC	time_enum		dec4, dec8, dec16, dec32	rw	dec4			
s4	ACQ480_MR_EN	ACQ480:MR:EN	time_enum		,	rw				ACQ480 Enable Multi Rate
s4	ACQ480_MR_EVSEL_0	ACQ480:MR:EVSEL:0	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			ACQ480 Multirate Signal Line Select 0
s4	ACQ480_MR_EVSEL_1	ACQ480:MR:EVSEL:1	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			ACQ480 Multirate Signal Line Select 1
s4	ACQ480_OSR	ACQ480:OSR	time_double			rw	2e+06	0	0	Shows ADC Output Sample Rate OSR = FS / ADC_DECIM / FPGA_DECIM
s4	ACQ480_T50R	ACQ480:T50R	time_enum		0, 1	rw				Enable 50 Ohm input impedance, all channels Initial Value: 0 PVType: time_enum
s4	ACQ480_T50R_01	ACQ480:T50R:01	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH01
s4	ACQ480_T50R_02	ACQ480:T50R:02	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH02
s4	ACQ480_T50R_03	ACQ480:T50R:03	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH03
s4	ACQ480_T50R_04	ACQ480:T50R:04	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH04
s4	ACQ480_T50R_05	ACQ480:T50R:05	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH05
s4	ACQ480_T50R_06	ACQ480:T50R:06	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH06
s4	ACQ480_T50R_07	ACQ480:T50R:07	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH07
s4	ACQ480_T50R_08	ACQ480:T50R:08	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH08
s4	ADC_CAL	ADC_CAL	time_enum		default, caldef, calibrated	rw	calibrated			Initial Value: calibrated PVType: time_enum

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	ADC_MODE	ADC_MODE	time_long			rw	-1	0	0	Indicates ADC is calibrated Initial Value: -1
s4	AGIX					rw	24			
s4		AI:TW:01								Raw Transient Waveform CH01
s4		AI:TW:01:V	time_long			rw		0	0	Cooked Transient Waveform CH01 Volts
s4		AI:TW:02								Raw Transient Waveform CH02
s4		AI:TW:02:V	time_long			rw		0	0	Cooked Transient Waveform CH02 Volts
s4		AI:TW:03								Raw Transient Waveform CH03
s4		AI:TW:03:V	time_long			rw		0	0	Cooked Transient Waveform CH03 Volts
s4		AI:TW:04								Raw Transient Waveform CH04
s4		AI:TW:04:V	time_long			rw		0	0	Cooked Transient Waveform CH04 Volts
s4		AI:TW:05								Raw Transient Waveform CH05
s4		AI:TW:05:V	time_long			rw		0	0	Cooked Transient Waveform CH05 Volts
s4		AI:TW:06								Raw Transient Waveform CH06
s4		AI:TW:06:V	time_long			rw		0	0	Cooked Transient Waveform CH06 Volts
s4		AI:TW:07								Raw Transient Waveform CH07
s4		AI:TW:07:V	time_long			rw		0	0	Cooked Transient Waveform CH07 Volts
s4		AI:TW:08								Raw Transient Waveform CH08
s4		AI:TW:08:V	time_long			rw		0	0	Cooked Transient Waveform CH08 Volts
s4		AI:WF:01	time_short			rw	array([], dtype=int16)	0	0	Analog input 01 waveform
s4		AI:WF:01:ACTIVE	time_double			rw		0	0	Live Waveform Status CH01 Function ACTIVE
s4		AI:WF:01:OVER	time_enum		,	rw				Live Waveform Status CH01 Function OVER
s4		AI:WF:01:TH	time_long			rw	512	0	0	Live Waveform Status CH01 Function TH
s4		AI:WF:01:UPDATES	time_double			rw		0	0	Live Waveform Status CH01 Function UPDATES
s4		AI:WF:01:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH01 Volts
s4		AI:WF:02	time_short			rw	array([], dtype=int16)	0	0	Analog input 02 waveform
s4		AI:WF:02:ACTIVE	time_double			rw		0	0	Live Waveform Status CH02 Function ACTIVE
s4		AI:WF:02:OVER	time_enum		,	rw				Live Waveform Status CH02 Function OVER
s4		AI:WF:02:TH	time_long			rw	512	0	0	Live Waveform Status CH02 Function TH
s4		AI:WF:02:UPDATES	time_double			rw		0	0	Live Waveform Status CH02 Function UPDATES
s4		AI:WF:02:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH02 Volts

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4		AI:WF:03	time_short			rw	array([], dtype=int16)	0	0	Analog input 03 waveform
s4		AI:WF:03:ACTIVE	time_double			rw		0	0	Live Waveform Status CH03 Function ACTIVE
s4		AI:WF:03:OVER	time_enum		,	rw				Live Waveform Status CH03 Function OVER
s4		AI:WF:03:TH	time_long			rw	512	0	0	Live Waveform Status CH03 Function TH
s4		AI:WF:03:UPDATES	time_double			rw		0	0	Live Waveform Status CH03 Function UPDATES
s4		AI:WF:03:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH03 Volts
s4		AI:WF:04	time_short			rw	array([], dtype=int16)	0	0	Analog input 04 waveform
s4		AI:WF:04:ACTIVE	time_double			rw		0	0	Live Waveform Status CH04 Function ACTIVE
s4		AI:WF:04:OVER	time_enum		,	rw				Live Waveform Status CH04 Function OVER
s4		AI:WF:04:TH	time_long			rw	512	0	0	Live Waveform Status CH04 Function TH
s4		AI:WF:04:UPDATES	time_double			rw		0	0	Live Waveform Status CH04 Function UPDATES
s4		AI:WF:04:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH04 Volts
s4		AI:WF:05	time_short			rw	array([], dtype=int16)	0	0	Analog input 05 waveform
s4		AI:WF:05:ACTIVE	time_double			rw		0	0	Live Waveform Status CH05 Function ACTIVE
s4		AI:WF:05:OVER	time_enum		,	rw				Live Waveform Status CH05 Function OVER
s4		AI:WF:05:TH	time_long			rw	512	0	0	Live Waveform Status CH05 Function TH
s4		AI:WF:05:UPDATES	time_double			rw		0	0	Live Waveform Status CH05 Function UPDATES
s4		AI:WF:05:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH05 Volts
s4		AI:WF:06	time_short			rw	array([], dtype=int16)	0	0	Analog input 06 waveform
s4		AI:WF:06:ACTIVE	time_double			rw		0	0	Live Waveform Status CH06 Function ACTIVE
s4		AI:WF:06:OVER	time_enum		,	rw				Live Waveform Status CH06 Function OVER
s4		AI:WF:06:TH	time_long			rw	512	0	0	Live Waveform Status CH06 Function TH
s4		AI:WF:06:UPDATES	time_double			rw		0	0	Live Waveform Status CH06 Function UPDATES
s4		AI:WF:06:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH06 Volts
s4		AI:WF:07	time_short			rw	array([], dtype=int16)	0	0	Analog input 07 waveform
s4		AI:WF:07:ACTIVE	time_double			rw		0	0	Live Waveform Status CH07 Function ACTIVE
s4		AI:WF:07:OVER	time_enum		,	rw				Live Waveform Status CH07 Function OVER
s4		AI:WF:07:TH	time_long			rw	512	0	0	Live Waveform Status CH07 Function TH
s4		AI:WF:07:UPDATES	time_double			rw		0	0	Live Waveform Status CH07 Function UPDATES
s4		AI:WF:07:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH07 Volts

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4		AI:WF:08	time_short			rw	array([], dtype=int16)	0	0	Analog input 08 waveform
s4		AI:WF:08:ACTIVE	time_double			rw		0	0	Live Waveform Status CH08 Function ACTIVE
s4		AI:WF:08:OVER	time_enum		,	rw				Live Waveform Status CH08 Function OVER
s4		AI:WF:08:TH	time_long			rw	512	0	0	Live Waveform Status CH08 Function TH
s4		AI:WF:08:UPDATES	time_double			rw		0	0	Live Waveform Status CH08 Function UPDATES
s4		AI:WF:08:V	time_long			rw		0	0	Live Cooked Analog Input Waveform CH08 Volts
s4		AI:WF:W1	time_long			rw		0	0	
s4		AI:WF:W2	time_long			rw		0	0	
s4	AI_CAL_EOFF	AI:CAL:EOFF	time_float			rw		0	0	AI Offset Calibration values, index from 0
s4	AI_CAL_ESLO	AI:CAL:ESLO	time_float			rw		0	0	AI Calibration Slope, index from 0
s4	acq480_dump					rwx	ffff 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 10:0000 0000 0000 0000 0000 0000 0000 0000 0000 00			acq480 low level control dump
s4	acq480_flush					rwx				acq480 low level control flush
s4	acq480_fpga_decim					r	10			acq480 low level control fpga_decim
s4	acq480_getGain					rwx				acq480 low level control getGain
s4	acq480_help					rwx	PLL [FSMSPS DECIM] dump flush getGain			acq480 low level control help
s4	acq480_loti					r	1			acq480 low level control loti
s4	acq480_makeLinks					rwx	ln -s /usr/local/bin/acq480_knobs acq480_PLL ln -s /usr/local/bin/acq480_knobs acq480_dump ln -s /usr/local/bin/acq480_k			acq480 low level control makeLinks
s4	acq480_map					rwx	MAP_CH1234_TO_OUT1A 1 MAP_CH1234_TO_OUT1B 1 2 MAP_CH1234_TO_OUT2A 2 MAP_CH1234_TO_OUT2B 2 2 MAP_CH1234_TO_OUT3A 3 MAP_CH			acq480 low level control map

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	acq480_PLL					rwx	getPLL() d1 = 0000			acq480 low level control PLL
s4	acq480_readall					rwx				acq480 low level control readall
s4	acq480_reg					rwx				acq480 low level control reg
s4	acq480_reset					rwx				acq480 low level control reset
s4	acq480_setAverageSelect					rwx				acq480 low level control setAverageSelect
s4	acq480_setClkHardSync					rwx				acq480 low level control setClkHardSync
s4	acq480_setDataPattern					rwx	-EN_2WIRE BTC_MODE -MSB_FIRST -EN_SDR -FALL_SDR			acq480 low level control setDataPattern
s4	acq480_setDataRate					rwx				acq480 low level control setDataRate
s4	acq480_setDecimationFilter					rwx				acq480 low level control setDecimationFilter
s4	acq480_setFilterCoefficients					rwx				acq480 low level control setFilterCoefficients
s4	acq480_setGain					rwx				acq480 low level control setGain
s4	acq480_setHiPassFilter					rwx				acq480 low level control setHiPassFilter
s4	acq480_setInvert					rwx				acq480 low level control setInvert
s4	acq480_setLFNS					rwx				acq480 low level control setLFNS
s4	acq480_setLvdsTestPatDeskew					rwx				acq480 low level control setLvdsTestPatDeskew
s4	acq480_setLvdsTestPatRamp					rwx				acq480 low level control setLvdsTestPatRamp
s4	acq480_setPatDeskew					rwx				acq480 low level control setPatDeskew
s4	acq480_setPatSync					rwx				acq480 low level control setPatSync
s4	acq480_setTwoWireMode					rwx				acq480 low level control setTwoWireMode
s4	acq480_train_ctrl					rw	0x00000000			acq480 low level control train_ctrl
s4	acq480_train_hi_val					r	0x00000000 0			acq480 low level control train_hi_val
s4	acq480_train_lo_val					r	0x00000000 4			acq480 low level control train_lo_val
s4	acq480_two_lane_mode					rw				acq480 low level control two_lane_mode
s4	acq482_cmap					rw	1			acq480 channel mapping 4|8
s4	burst					rw	burst=2,0,1 enable d0 RISING			

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	CLK	CLK	time_enum		internal, external	rw	internal			Select CLK type
s4	CLKDIV	CLKDIV	time_double			rw	1			Clock divider value
s4	CLK_DX	CLK:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			CLK source select DX
s4	CLK_SENSE	CLK:SENSE	time_enum		falling, rising	rw	falling			CLK source select SENSE
s4	COMPLETED_SHOT	COMPLETED_SHOT	time_long			rw		0	0	shot number of last completed shot
s4	DEC					rw	5,0,173			
s4	data32					rw				data is 32 bit (not 16 bit)
s4	EVENT0	EVENT0	time_enum		disable, enable	rw	disable			EVENT 0 Enable
s4	EVENT0_DX	EVENT0:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			EVENT 0 selector DX
s4	EVENT0_SENSE	EVENT0:SENSE	time_enum		falling, rising	rw	falling			EVENT 0 selector SENSE
s4	EVENT1	EVENT1	time_enum		disable, enable	rw	disable			EVENT 1 Enable
s4	EVENT1_DX	EVENT1:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			EVENT 1 selector DX
s4	EVENT1_SENSE	EVENT1:SENSE	time_enum		falling, rising	rw	falling			EVENT 1 selector SENSE
s4	evt_sc_latch					r				
s4	ffir_coeff					rw	13 "Permission denied" failed to open "ffir_coeff"			
s4	ffir_counter					r				
s4	ffir_reset					rw	13 "Permission denied" failed to open "ffir_reset"			

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	GAIN_01	GAIN:01	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_01
s4	GAIN_02	GAIN:02	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_02
s4	GAIN_03	GAIN:03	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_03
s4	GAIN_04	GAIN:04	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_04
s4	GAIN_05	GAIN:05	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_05
s4	GAIN_06	GAIN:06	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_06
s4	GAIN_07	GAIN:07	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_07
s4	GAIN_08	GAIN:08	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Sets GAIN for CH_08

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	GAIN_CHANGES	GAIN_CHANGES	time_double			rw	GAIN_CHANGES 0			indicates GAIN has changed
s4	HAS_CH_GAINS	HAS_CH_GAINS	time_enum		,	rw	HAS_CH_GAINS			Module has per channel GAIN control
s4	INTCLK_HZ	INTCLK_HZ	time_long	Hz		rw	500000	80000000	10000	Internal Clock frequency [integer Hz]
s4	IS_PRESENT	IS_PRESENT	time_enum		EMPTY, PRESENT	rw	PRESENT			module IS PRESENT
s4	is_adc					r	1 sar			
s4	MANUFACTURER					r	D-TACQ Solutions			Show module Manufacturer
s4	MAX_KHZ					rw	80000			
s4	MIN_KHZ					rw	10000			
s4	MODEL	MODEL	time_string			rw	ACQ482ELF			Module MODEL
s4	MTYPE	MTYPE	time_string			rw	8			Module Type Code
s4	module_variant					r	82			
s4	mr_10dec					rw				
s4	mr_en					rw				
s4	mr_sel0					rw				
s4	mr_sel1					rw				
s4	NCHAN	NCHAN	time_long			rw	8			Module number of channels
s4		OS:COUNT	time_double			rw		0	0	oversampling COUNT
s4		OS:FREQ	time_double	Hz		rw		0	0	oversampling FREQ
s4	PART_NUM	PART_NUM	time_string			rw	ACQ482ELF-16-2V5-H			Module full part number
s4	prompt						None			prompt {0|1} changes command prompt
s4	ROLE	ROLE	time_string			rw	SLAVE			
s4	SERIAL	SERIAL	time_string			rw	E48211106			Module Serial Number
s4	SHOT	SHOT	time_long			rw		0	0	Shot Number
s4	SHOT_IN_PROGRESS	SHOT_IN_PROGRESS	time_double			rw		0	0	shot is in progress.
s4	SIG_CLK_COUNT_ACTIVE	SIG:CLK_COUNT:ACTIVE	time_double			rw	1.0	0	0	CLK counter signal COUNT value ACTIVE
s4	SIG_CLK_COUNT_COUNT	SIG:CLK_COUNT:COUNT	time_double			rw	6e+09	0	0	CLK counter signal COUNT value COUNT
s4	SIG_CLK_COUNT_FREQ	SIG:CLK_COUNT:FREQ	time_double	Hz		rw	2e+07	0	0	CLK counter signal COUNT value FREQ

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	SIG_CLK_COUNT_RESET	SIG:CLK_COUNT:RESET	time_enum		,	rw				CLK counter signal COUNT value RESET
s4	SIG_CLK_TRAIN_BSY	SIG:CLK:TRAIN_BSY	time_enum		0, 1	rw				Clock Training is BUSY
s4	SIG_clk_count_ACTIVE	SIG:clk_count:ACTIVE	time_double			rw	1.0	0	0	Signal counter clk value ACTIVE
s4	SIG_clk_count_COUNT	SIG:clk_count:COUNT	time_double			rw	6e+09	0	0	Signal counter clk value COUNT
s4	SIG_clk_count_FREQ	SIG:clk_count:FREQ	time_double	Hz		rw	2e+07	0	0	Signal counter clk value FREQ
s4	SIG_clk_count_RESET	SIG:clk_count:RESET	time_enum		,	rw				Signal counter clk value RESET
s4	SIG_SAMPLE_COUNT_ACTIVE	SIG:SAMPLE_COUNT:ACTIVE	time_double			rw	0.0	0	0	Signal Sample Count Value ACTIVE
s4	SIG_SAMPLE_COUNT_COUNT	SIG:SAMPLE_COUNT:COUNT	time_double			rw		0	0	Signal Sample Count Value COUNT
s4	SIG_SAMPLE_COUNT_FREQ	SIG:SAMPLE_COUNT:FREQ	time_double	Hz		rw	0.000	0	0	Signal Sample Count Value FREQ
s4	SIG_SAMPLE_COUNT_RESET	SIG:SAMPLE_COUNT:RESET	time_enum		,	rw				Signal Sample Count Value RESET
s4	SIG_SAMPLE_COUNT_RUNTIME	SIG:SAMPLE_COUNT:RUNTIME	time_double	s		rw		9999999	0	Signal Sample Count Value RUNTIME
s4	SIG_sample_count_ACTIVE	SIG:sample_count:ACTIVE	time_double			rw	0.0	0	0	Signal counter sample value ACTIVE
s4	SIG_sample_count_COUNT	SIG:sample_count:COUNT	time_double			rw		0	0	Signal counter sample value COUNT
s4	SIG_sample_count_FREQ	SIG:sample_count:FREQ	time_double	Hz		rw	0.000	0	0	Signal counter sample value FREQ
s4	SIG_sample_count_RESET	SIG:sample_count:RESET	time_enum		,	rw				Signal counter sample value RESET
s4	SYNC	SYNC	time_enum		internal, external	rw	internal			SYNC enable
s4	SYNC_DX	SYNC:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			SYNC DX selector
s4	SYNC_SENSE	SYNC:SENSE	time_enum		falling, rising	rw	falling			SYNC SENSE selector
s4	sod					rw				Sample On Demand: single sample mode (where available)
s4	T50R						None			Set 50 Ohm Impedance all channels.
s4	T50R_1					rw				Select 50 Ohm impedance CH1
s4	T50R_2					rw				Select 50 Ohm impedance CH2
s4	T50R_3					rw				Select 50 Ohm impedance CH3
s4	T50R_4					rw				Select 50 Ohm impedance CH4
s4	T50R_5					rw				Select 50 Ohm impedance CH5
s4	T50R_6					rw				Select 50 Ohm impedance CH6
s4	T50R_7					rw				Select 50 Ohm impedance CH7

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s4	T50R_8					rw				Select 50 Ohm impedance CH8
s4	TRG	TRG	time_enum		none, enable	rw	enable			TRG enable
s4	TRG_DX	TRG:DX	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			trigger source DX select
s4	TRG_SENSE	TRG:SENSE	time_enum		falling, rising	rw	rising			trigger source SENSE select
s4	TRIGGERED	TRIGGERED	time_enum		NO, YES	rw	NO			
s4	task_active					r				

Site **s5**
EpicsPV Prefix **acq2106_340:5:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s5	ACC					rw	6,0,173			
s5		ACQ480:HAS_50R	time_enum		0, 1	rw	1			50 ohm option _is_ available on this module
s5		ACQ480:JC:LOL	time_enum		0, 1	rw	1			Jitter Cleaner Loss Of Lock failure status
s5		ACQ480:JC:LOS	time_enum		0, 1	rw				Jitter Cleaner Los Of Signal failure status
s5		ACQ480:JC:PRESENT	time_enum		0, 1	rw	1			Jitter Cleaner is Present status
s5		ACQ480:LINK:LOTI	time_enum		0, 1	rw	1			Link Loss Of Training Indicator
s5		ACQ480:TRAIN	time_enum		ACQ480_RESET, ACQ480_START, ACQ480_DESKE	rw	ACQ480_RESET			Indicator shows ACQ480 Training state
s5		ACQ480:TWO_LANE	time_enum		0, 1	rw				ADC has two lanes per bit enabled for 80MSPS operation
s5	ACQ480_FIR_01	ACQ480:FIR:01	time_enum		DISABLE, LP_ODD_D2, HP_ODD_D2, LP_EVEN_D	rw	DISABLE			Select ADC FIR Filter. 01 masters 02..08
s5	ACQ480_FIR_DECIM	ACQ480:FIR:DECIM	time_double			rw	1	0	0	Shows Decimation due to ADC FIR selection
s5	ACQ480_FPGA_DECIM	ACQ480:FPGA:DECIM	time_long			rw	10	0	0	Show decimation due to FPGA 1, 4, 10 ..selected on FPGA personality load.
s5	ACQ480_GAIN_01	ACQ480:GAIN:01	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH01
s5	ACQ480_GAIN_02	ACQ480:GAIN:02	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH02
s5	ACQ480_GAIN_03	ACQ480:GAIN:03	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH03
s5	ACQ480_GAIN_04	ACQ480:GAIN:04	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH04
s5	ACQ480_GAIN_05	ACQ480:GAIN:05	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH05
s5	ACQ480_GAIN_06	ACQ480:GAIN:06	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH06

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s5	ACQ480_GAIN_07	ACQ480:GAIN:07	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH07
s5	ACQ480_GAIN_08	ACQ480:GAIN:08	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH08
s5	ACQ480_GAIN_ALL	ACQ480:GAIN:ALL	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			
s5	ACQ480_HPF_01	ACQ480:HPF:01	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH01 See ADS1278 manual for details
s5	ACQ480_HPF_02	ACQ480:HPF:02	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH02 See ADS1278 manual for details
s5	ACQ480_HPF_03	ACQ480:HPF:03	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH03 See ADS1278 manual for details
s5	ACQ480_HPF_04	ACQ480:HPF:04	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH04 See ADS1278 manual for details
s5	ACQ480_HPF_05	ACQ480:HPF:05	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH05 See ADS1278 manual for details

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s5	ACQ480_HPF_06	ACQ480:HPF:06	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH06 See ADS1278 manual for details
s5	ACQ480_HPF_07	ACQ480:HPF:07	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH07 See ADS1278 manual for details
s5	ACQ480_HPF_08	ACQ480:HPF:08	time_enum		OFF, do not use, K2, K3, K4, K5, K6, K7,	rw	OFF			Select High Pass Filter for CH08 See ADS1278 manual for details
s5	ACQ480_INVERT_01	ACQ480:INVERT:01	time_enum		REG, INV	rw	REG			Invert Input CH01 Could be useful for debug, or create a differential input from two channels
s5	ACQ480_INVERT_02	ACQ480:INVERT:02	time_enum		REG, INV	rw	REG			Invert Input CH02 Could be useful for debug, or create a differential input from two channels
s5	ACQ480_INVERT_03	ACQ480:INVERT:03	time_enum		REG, INV	rw	REG			Invert Input CH03 Could be useful for debug, or create a differential input from two channels
s5	ACQ480_INVERT_04	ACQ480:INVERT:04	time_enum		REG, INV	rw	REG			Invert Input CH04 Could be useful for debug, or create a differential input from two channels
s5	ACQ480_INVERT_05	ACQ480:INVERT:05	time_enum		REG, INV	rw	REG			Invert Input CH05 Could be useful for debug, or create a differential input from two channels
s5	ACQ480_INVERT_06	ACQ480:INVERT:06	time_enum		REG, INV	rw	REG			Invert Input CH06 Could be useful for debug, or create a differential input from two channels
s5	ACQ480_INVERT_07	ACQ480:INVERT:07	time_enum		REG, INV	rw	REG			Invert Input CH07 Could be useful for debug, or create a differential input from two channels
s5	ACQ480_INVERT_08	ACQ480:INVERT:08	time_enum		REG, INV	rw	REG			Invert Input CH08 Could be useful for debug, or create a differential input from two channels
s5	ACQ480_LFNS_01	ACQ480:LFNS:01	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH01 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s5	ACQ480_LFNS_02	ACQ480:LFNS:02	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH02 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s5	ACQ480_LFNS_03	ACQ480:LFNS:03	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH03 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s5	ACQ480_LFNS_04	ACQ480:LFNS:04	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH04 See ADS1278 manual for details. Only effective with ADC_FIR enabled

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s5	ACQ480_LFNS_05	ACQ480:LFNS:05	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH05 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s5	ACQ480_LFNS_06	ACQ480:LFNS:06	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH06 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s5	ACQ480_LFNS_07	ACQ480:LFNS:07	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH07 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s5	ACQ480_LFNS_08	ACQ480:LFNS:08	time_enum		OFF, ON	rw	OFF			Enable Low Frequency Noise Suppression for CH08 See ADS1278 manual for details. Only effective with ADC_FIR enabled
s5	ACQ480_LFNS_ALL	ACQ480:LFNS:ALL	time_enum		OFF, ON	rw	OFF			
s5	ACQ480_MR_10DEC	ACQ480:MR:10DEC	time_enum		dec4, dec8, dec16, dec32	rw	dec4			
s5	ACQ480_MR_EN	ACQ480:MR:EN	time_enum		,	rw				ACQ480 Enable Multi Rate
s5	ACQ480_MR_EVSEL_0	ACQ480:MR:EVSEL:0	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			ACQ480 Multirate Signal Line Select 0
s5	ACQ480_MR_EVSEL_1	ACQ480:MR:EVSEL:1	time_enum		d0, d1, d2, d3, d4, d5, d6, d7	rw	d0			ACQ480 Multirate Signal Line Select 1
s5	ACQ480_OSR	ACQ480:OSR	time_double			rw	2e+06	0	0	Shows ADC Output Sample Rate OSR = FS / ADC_DECIM / FPGA_DECIM
s5	ACQ480_T50R	ACQ480:T50R	time_enum		0, 1	rw				Enable 50 Ohm input impedance, all channels Initial Value: 0 PVType: time_enum
s5	ACQ480_T50R_01	ACQ480:T50R:01	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH01
s5	ACQ480_T50R_02	ACQ480:T50R:02	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH02
s5	ACQ480_T50R_03	ACQ480:T50R:03	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH03
s5	ACQ480_T50R_04	ACQ480:T50R:04	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH04
s5	ACQ480_T50R_05	ACQ480:T50R:05	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH05
s5	ACQ480_T50R_06	ACQ480:T50R:06	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH06
s5	ACQ480_T50R_07	ACQ480:T50R:07	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH07
s5	ACQ480_T50R_08	ACQ480:T50R:08	time_enum		0, 1	rw				Enable 50 Ohm impedance for CH08
s5	ADC_CAL	ADC_CAL	time_enum		default, caldef, calibrated	rw	calibrated			Initial Value: calibrated PVType: time_enum

Site **s6**
EpicsPV Prefix **acq2106_340:6:**
Site Text

Site	HAPI Knob	Epics PV	Type	Units	Menu Vals	Attr	InitVal	Upper	Lower	Help
s6	ACC					rw	7,0,173			
s6		ACQ480:HAS_50R	time_enum		0, 1	rw	1			50 ohm option _is_ available on this module
s6		ACQ480:JC:LOL	time_enum		0, 1	rw				Jitter Cleaner Loss Of Lock failure status
s6		ACQ480:JC:LOS	time_enum		0, 1	rw				Jitter Cleaner Los Of Signal failure status
s6		ACQ480:JC:PRESENT	time_enum		0, 1	rw				Jitter Cleaner is Present status
s6		ACQ480:LINK:LOTI	time_enum		0, 1	rw	1			Link Loss Of Training Indicator
s6		ACQ480:TRAIN	time_enum		ACQ480_RESET, ACQ480_START, ACQ480_DESKE	rw	ACQ480_RESET			Indicator shows ACQ480 Training state
s6		ACQ480:TWO_LANE	time_enum		0, 1	rw				ADC has two lanes per bit enabled for 80MSPS operation
s6	ACQ480_FIR_01	ACQ480:FIR:01	time_enum		DISABLE, LP_ODD_D2, HP_ODD_D2, LP_EVEN_D	rw	DISABLE			Select ADC FIR Filter. 01 masters 02..08
s6	ACQ480_FIR_DECIM	ACQ480:FIR:DECIM	time_double			rw	1	0	0	Shows Decimation due to ADC FIR selection
s6	ACQ480_FPGA_DECIM	ACQ480:FPGA:DECIM	time_long			rw	10	0	0	Show decimation due to FPGA 1, 4, 10 ..selected on FPGA personality load.
s6	ACQ480_GAIN_01	ACQ480:GAIN:01	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH01
s6	ACQ480_GAIN_02	ACQ480:GAIN:02	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH02
s6	ACQ480_GAIN_03	ACQ480:GAIN:03	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH03
s6	ACQ480_GAIN_04	ACQ480:GAIN:04	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH04
s6	ACQ480_GAIN_05	ACQ480:GAIN:05	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH05
s6	ACQ480_GAIN_06	ACQ480:GAIN:06	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH06
s6	ACQ480_GAIN_07	ACQ480:GAIN:07	time_enum		0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB	rw	0 dB			Set ADC GAIN for CH07
s6	ACQ480_GAIN_08	ACQ480:GAIN:08	time_enum		0 dB, 1 dB, 2	rw	0 dB			Set ADC GAIN for CH08