

Name: Tushar Shrivastav Lab 4C  
COEN 20

Half word address = X...XX1 penalty: 100 cycles  
Full word address = X...X01 penalty: 200 cycles  
Full word address = X...X10 penalty: 100 cycles  
Full word address = X...X11 penalty: 200 cycles  
Address dependency penalty: 199 cycles  
Data dependency penalty: 200 cycles  
Maximum VDIV/VSQRT overlap: 16 cycles