

마이크로프로세서

-Execution Time -

Daejin Park

School of Electronics Engineering, KNU, KOREA

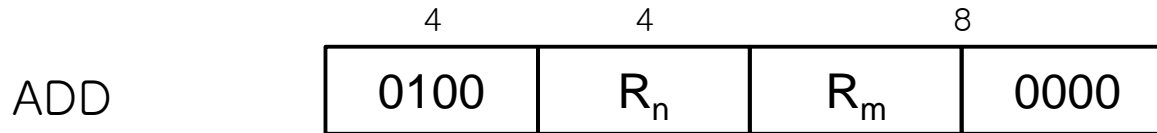
2019.05.08, 05.10



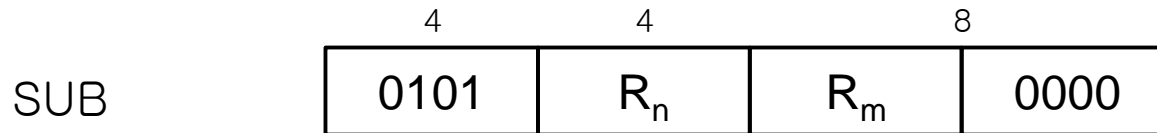
Instructions

MOV0	<table><tr><td>4</td><td>4</td><td>8</td></tr><tr><td>0000</td><td>R_n</td><td>direct</td></tr></table>	4	4	8	0000	R_n	direct	$R_n \leftarrow Mem\{direct\}$ $PC \leftarrow PC + 1$	8clks
4	4	8							
0000	R_n	direct							
MOV1	<table><tr><td>4</td><td>4</td><td>8</td></tr><tr><td>0001</td><td>R_n</td><td>direct</td></tr></table>	4	4	8	0001	R_n	direct	$Mem\{direct\} \leftarrow R_n$ $PC \leftarrow PC + 1$	8clks
4	4	8							
0001	R_n	direct							
MOV2	<table><tr><td>4</td><td>4</td><td>8</td></tr><tr><td>0010</td><td>R_n</td><td>0000 R_m</td></tr></table>	4	4	8	0010	R_n	0000 R_m	$Mem\{R_n\} \leftarrow R_m$ $PC \leftarrow PC + 1$	12clks
4	4	8							
0010	R_n	0000 R_m							
MOV3	<table><tr><td>4</td><td>4</td><td>8</td></tr><tr><td>0011</td><td>R_n</td><td>#immed</td></tr></table>	4	4	8	0011	R_n	#immed	$R_n \leftarrow \#immed$ $PC \leftarrow PC + 1$	6clks
4	4	8							
0011	R_n	#immed							

Instructions



$R_n \leftarrow R_n + (R_m \gg 4)$
 $PC \leftarrow PC + 1$ 4clks



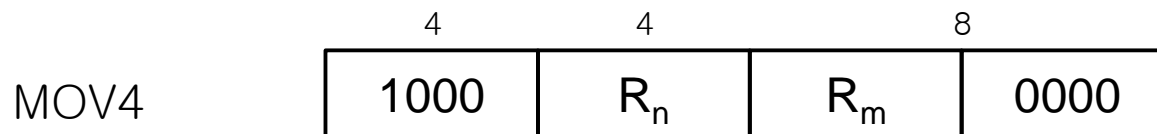
$R_n \leftarrow R_n - (R_m \gg 4)$
 $PC \leftarrow PC + 1$ 4clks



If $R_n == 0$ 12clks
 $PC \leftarrow PC + 1 + \text{relative}$



$R_n \leftarrow R_n * R_m$ 30clks
 $PC \leftarrow PC + 1$



$R_n \leftarrow \text{Mem}\{R_m\}$ 2clks
 $PC \leftarrow PC + 1$

숙제

Decode 일부수정

CDecode.h

```
enum { MOV0=0, MOV1, MOV2, MOV3, ADD, SUB, JZ, MUL, MOV4 };
```

CDecode.cpp

```
void
CT1DecodeDirectFetch::show_instruction() {
    if(m_instruction.OPCODE == MOV3) {
        cout << "MOV3 " << "R" << m_instruction.OP1 << ", #" << m_instruction.OP2 << endl;
    } else if(m_instruction.OPCODE == ADD) {
        unsigned int op2 = (m_instruction.OP2 >> 4) & 0xF;
        cout << "ADD " << "R" << m_instruction.OP1 << ", R" << op2 << endl;
    } else if(m_instruction.OPCODE == SUB) {
        unsigned int op2 = (m_instruction.OP2 >> 4) & 0xF;
        cout << "SUB " << "R" << m_instruction.OP1 << ", R" << op2 << endl;
    } else if(m_instruction.OPCODE == MOV0) {
        unsigned int op2 = m_instruction.OP2 & 0xFF;
        cout << "MOV0 " << "R" << m_instruction.OP1 << ", [" << op2 << "]" << endl;
    } else if(m_instruction.OPCODE == MOV1) {
        unsigned int op2 = m_instruction.OP2 & 0xFF;
        cout << "MOV1 " << "[" << op2 << "], R" << m_instruction.OP1 << endl;
    } else if(m_instruction.OPCODE == MUL) {
        unsigned int op2 = (m_instruction.OP2 >> 4) & 0xF;
        cout << "MUL " << "R" << m_instruction.OP1 << ", R" << op2 << endl;
    } else if(m_instruction.OPCODE == JZ) {
        cout << "JZ " << "R" << m_instruction.OP1 << ", " << m_instruction.OP2 << endl;
    }
}
```

Execution시 수행시간 측정

수정

```
class CT1ExecuteTinyUnit: public CExecute {
public:
    CT1ExecuteTinyUnit(CT1DecodeDirectFetch& decode,
                      C16RegisterFile& regs,
                      CSRAM_256W& mems)
        : m_decode_unit(decode), m_regs(regs), m_mems(mems) { }
    virtual ~CT1ExecuteTinyUnit() { }

    int do_execute();
private:
    CT1DecodeDirectFetch& m_decode_unit;
    C16RegisterFile&      m_regs;
    CSRAM_256W&           m_mems;
};
```

Execute 수정

CExecute.cpp

```
int c_lks[9] = {8, 8, 12, 6, 4, 4, 12, 30, 2};

int CT1ExecuteTinyUnit::do_execute() {
    // ex. MOV3 R0, #3
    if(m_decode_unit.get_opcode() == MOV3) {
        unsigned int reg_index = m_decode_unit.get_op1();
        int data = m_decode_unit.get_op2();

        m_regs.write_on_reg(reg_index, data);

        m_regs.set_PC(m_regs.get_PC()+1);

        return c_lks[MOV3];
    }
    // ex. ADD R0, R1 --> R0 = R0 + R1
    else if( m_decode_unit.get_opcode() == ADD) {
        unsigned int reg_n = m_decode_unit.get_op1();
        unsigned int reg_m = (m_decode_unit.get_op2() >> 4) & 0xF;

        int Rn = m_regs.read_from_reg(reg_n);
        int Rm = m_regs.read_from_reg(reg_m);

        Rn = Rn + Rm;
        m_regs.write_on_reg(reg_n, Rn);

        m_regs.set_PC(m_regs.get_PC()+1);

        return c_lks[ADD];
    }
}
```

Execute 수정

CExecute.cpp

```
// ex. SUB R0, R1 --> R0 = R0 - R1
} else if( m_decode_unit.get_opcode() == SUB) {
    unsigned int reg_n = m_decode_unit.get_op1();
    unsigned int reg_m = (m_decode_unit.get_op2() >> 4) & 0xF;

    int Rn = m_regs.read_from_reg(reg_n);
    int Rm = m_regs.read_from_reg(reg_m);

    Rn = Rn - Rm;
    m_regs.write_on_reg(reg_n, Rn);

    m_regs.set_PC(m_regs.get_PC()+1);

    return c1ks[SUB];

// ex. MOV0 R1, [3] : R1 <- M[3]
} else if( m_decode_unit.get_opcode() == MOV0 ) {
    unsigned int reg_n = m_decode_unit.get_op1();
    unsigned int mem_addr = m_decode_unit.get_op2() & 0xFF;

    int memory_data = m_mems.read_from_memory(mem_addr);

    m_regs.write_on_reg(reg_n, memory_data);

    m_regs.set_PC(m_regs.get_PC()+1);

    return c1ks[MOV0];
```

Execute 수정

CExecute.cpp

```
// ex. MOV1 [3], R1 : M[3] <- R1
} else if( m_decode_unit.get_opcode() == MOV1 ) {
    unsigned int reg_n    = m_decode_unit.get_op1();
    unsigned int mem_addr = m_decode_unit.get_op2() & 0xFF;

    int Rn = m_regs.read_from_reg(reg_n);

    m_mems.write_on_memory(mem_addr, Rn);

    m_regs.set_PC(m_regs.get_PC()+1);

    return c1ks[MOV1];

// ex. MUL R0, R1 --> R0 = R0 * R1
} else if( m_decode_unit.get_opcode() == MUL ) {
    unsigned int reg_n = m_decode_unit.get_op1();
    unsigned int reg_m = (m_decode_unit.get_op2() >> 4) & 0xF;

    int Rn = m_regs.read_from_reg(reg_n);
    int Rm = m_regs.read_from_reg(reg_m);

    Rn = Rn * Rm;
    m_regs.write_on_reg(reg_n, Rn);

    m_regs.set_PC(m_regs.get_PC()+1);

    return c1ks[MUL];

// ex. JZ R0, #3 (R0 == 0, then PC+1+3)
} else if( m_decode_unit.get_opcode() == JZ ) {

    unsigned int reg_n = m_decode_unit.get_op1();
    int offset = m_decode_unit.get_op2();

    int Rn = m_regs.read_from_reg(reg_n);
    m_regs.set_PC(m_regs.get_PC()+1);

    if( Rn == 0 ) {
        int pc = m_regs.get_PC();
        m_regs.set_PC(pc+offset);
    }

    return c1ks[JZ];
```


main 수정

main.cpp

```
int size = atoi(argv[2]);
int total_clks = 0;
// for(int i=0; i<atoi(argv[2]); i++) {
while( regs.get_PC() < size ) {
    decode.do_fetch_from(regs.get_PC());
    //decode.do_fetch_from(i);
    decode.do_decode();
    decode.show_instruction();

    total_clks += execute.do_execute();
}

cout << "After executing instruction ..." << endl;
cout << "Total clocks: " << total_clks << endl;
regs.show_regs();
```

명령어 라인수 vs. 실행시간

- 명령어 라인수는 정적인 특징
 - 라인 길이는 실제 프로그램의 수행시간이 아님.
- 실행시간 = 클럭수 * 1/freq
 - 클럭수는 마이크로프로세서가 코드를 읽어들이 해석하여 실행함으로써 프로그램 흐름에 의해 결정되는 동적인 특성임.

```
MOV0 R0, [0]
MOV0 R1, [1]
MUL R2, R4
MUL R3, R6
ADD R2, R3
MOV1 [10], R2
MOV0 R2, [2]
MOV0 R3, [3]
MUL R2, R5
MUL R3, R7
ADD R2, R3
MOV1 [11], R2
MOV0 R2, [2]
MOV0 R3, [3]
After executing instruction ...
Total clocks: 464
Register file
R0: 1
R1: 2
R2: 3
```

```
1 all:
2     g++ -o tpu CCode.cpp CDecode.cp
3
4 #   ./tpu file.bin 19
5     ./tpu matrix.bin 40
6 #   ./tpu file2.bin 20
7
```

숙제2

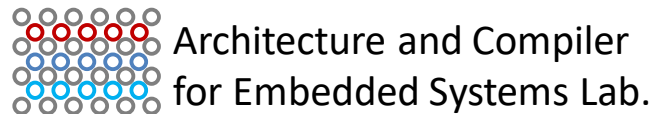
```
1 #include <iostream>
2 #include <fstream>
3 #include <cstdlib>
4
5 using namespace std;
6
7 int main() {
8     int r0 = 1;
9     int r1 = 1;
10    int r2 = 2;
11    int r5 = 10;
12
13    r5 = r5-r2;
14
15    while( r5 ) {
16        int r6 = r0 + r1;
17        r1 = r0;
18        r0 = r6;
19        r5--;
20    }
21
22    cout << "Result: " << r0 << endl;
23
24    return 0;
25 }
```

왼쪽 C코드에 대한 TPU 명령어로 표현하여 성능을 측정한다.

- MOV4를 사용하지 않은 어셈블리 코드를 작성하고 성능 측정
- MOV4를 사용할 때 성능 측정 비표

Q & A

Thank you for your attention



School of Electronics Engineering, KNU

ACES Lab (boltanut@knu.ac.kr)