1 2 3 4

## Reference

## Resistor

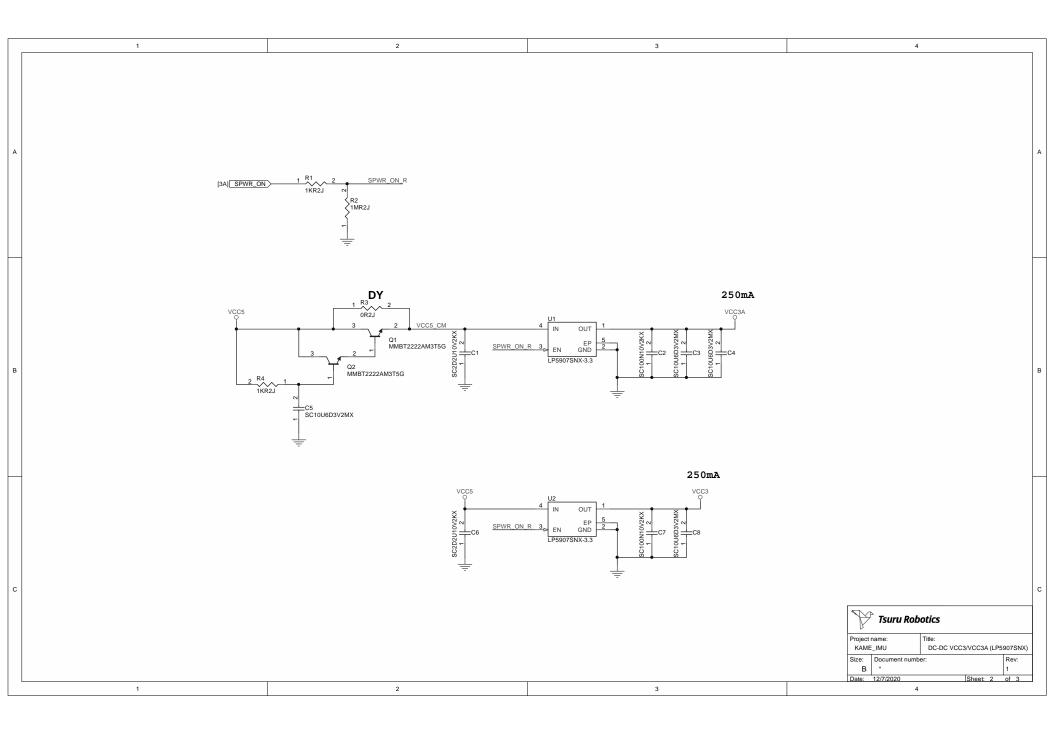
```
Naming rule 1 (single resistor):
 value + R + size + tolerance
Naming rule 2 (resistor network):
  value + R + size + tolerance + count + circuit type
Example 1:
                             Example 2:
  62D7R2F
                               220R2J4X
  62D7R => 62.7 Ohm
                              220R => 220 Ohm
  2 => 0402
                               2 => 0402
  F => ±1% tolerance
                               J => ±5% tolerance
                              4X => 4 isolated resistors
Tolerance
             Size
                        Circuit type
B: ±0.1%
             1: 0201
                      X: isolated
C: ±0.25%
             2: 0402
                       B: bussed
D: ±0.5%
             3: 0603
F: ±1%
             5: 0805
G: ±2%
             6: 1206
J: ±5%
             0: 1210
K: ±10%
M: ±20%
```

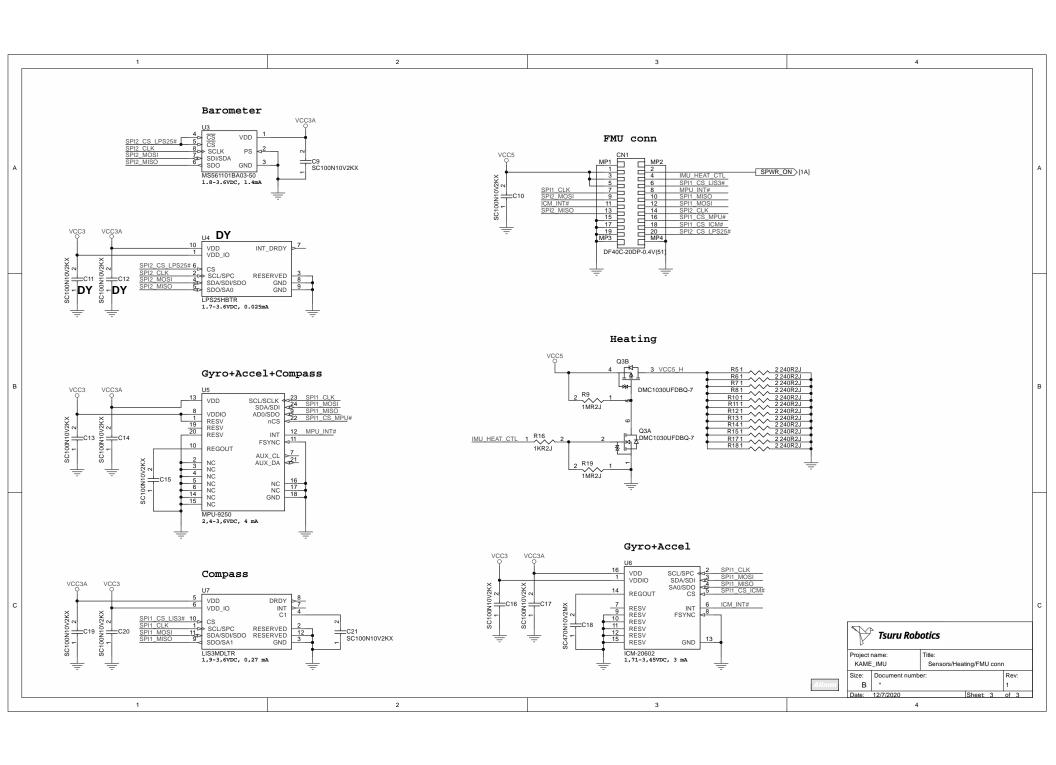
## Capacitor

```
Naming rule 1 (ceramic caps):
  type + value + voltage rating + size + tolerance + dielectric
Naming rule 2 (electrolytic caps):
  type + value + voltage rating + size + tolerance + ESR
Example 1:
                           Example 2:
 SCD1U10V2MX
                            ST330U6VD3M9
  SC => SMT Ceramic
                            ST => SMT Tantalum
 D1U => 0.1uF
                            330U => 330uF
 10V => 10V voltage rating
                            6V => 6.3V voltage rating
                            D3 => D3 package
 2 => 0402
 M => ±20% tolerance
                            M => ±20% tolerance
 X => X7R/X5R
                            9 => 9 mOhm ESR
Tolerance
            Size
                                                    Dielectric
                      Type
            1: 0201
                     SC: SMT Ceramic
                                                    X: X5R/X7R
B: ±0.1pF
C: ±0.25pF
            2: 0402
                     ST: SMT Tantalum
                                                    N: NPO/COG
D: ±0.5pF
            3: 0603
                      SA: SMT Aluminium Electrolytic
F: ±1%
            5: 0805
                      SP: SMT Aluminium Polymer
G: ±2%
            6: 1206
J: ±5%
            0: 1210
K: ±10%
M: ±20%
```

DY: No stuff

3 4





2 KAME FMU+IMU is a special-purpose microcontroller and sensor module, which is building on the work done by the following open-hardware projects: - Pixhawk FMUv1 by @pixhawk: https://github.com/pixhawk/Hardware/blob/master/README.md, licensed under cc-by-sa-3.0
- Cube by @ProffCNC: https://github.com/porficer/The-Cube/tree/master/FMU, licensed under cc-by-sa-3.0
- Pixracer by @ AUAV-OpenSource: https://github.com/AUAV-OpenSource/FMU/4-PixRacer, licensed under cc-by-sa-3.0
- KAME FMU+HMU is an open hardware design that is an evolution of the PixHawk, Pixracer, and Cube autopilot designs, licensed under the Creative Commons Autrobution-Share-Allie. 3 of Unported (CC BY-SA 3.0) license. Pull requests for fixes and relevant enhancements are welcome. Creative Commons License Disclaimer UNLESS OTHERWISE MUTUALLY AGREED TO BY THE PARTIES IN WRITING, LICENSOR OFFERS THE WORK AS-IS AND MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND CORRINING THE WORK, EXPRESS, IMPLIED, STATUTORY OR OTHERWISE, INCLUDING, WITHOUT LIMITATION, WARRANTIES OF TITLE, MERCHANTIBILITY, FITNESS FOR A SIATUTORY OR OTHERWISE, INCLUDING, WITHOUT LIMITATION, WARRANTIES OF TITLE, MERCHANTIBILITY, FITNESS FOR A PARTICULAR PURPOSE, NONINFRINGEMENT, OR THE ABSENCE OF LATENT OR OTHER DEFECTS, ACCURACY, OR THE PRESENCE OF ABSENCE OF ERRORS, WHETHER OR NOT DISCOVERABLE. SOME JURISDICTIONS DO NOT ALLOW THE EXCLUSION OF IMPLIED WARRANTIES, SO SUCH EXCLUSION ANY NOT APPLY TO YOU. EXCEPT TO THE EXTENT REQUIRED BY APPLICABLE LAW, IN NO EVENT WILL LICENSOR BE LIABLE TO YOU ON ANY LEGAL THEORY FOR ANY SPECIAL, INCIDENTAL, CONSEQUENTIAL, PUNITIVE OR EXEMPLARY DAMAGES ARISING OUT OF THIS LICENSE OR THE USE OF THE WORK, EVEN IF LICENSOR HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. http://creativecommons.org/licenses/by-sa/3.0/ Tsuru Robotics Project name: Title: KAME\_IMU Size: Document number: Rev: В Sheet: \* of \* Date: 12/7/2020 2