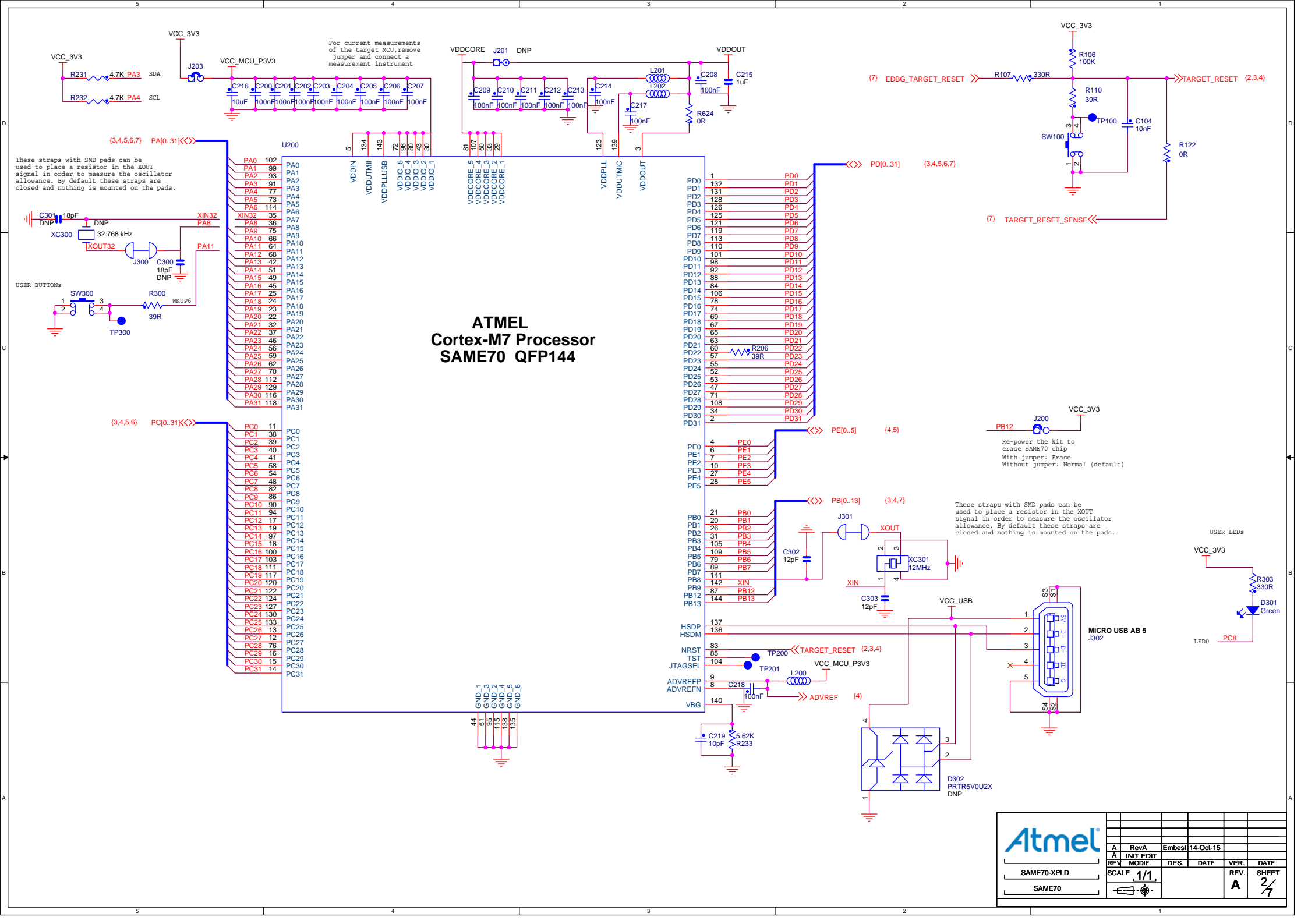
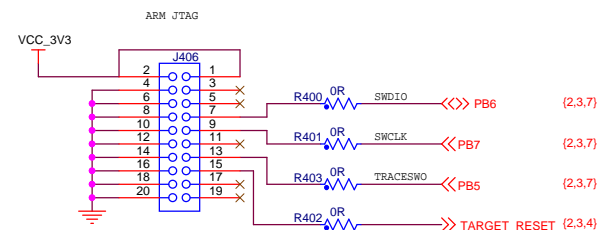
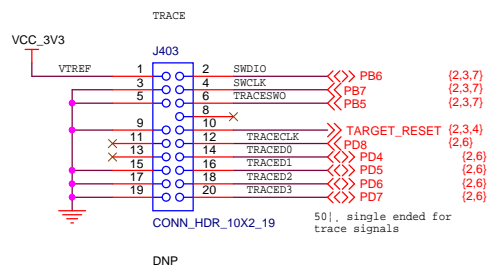
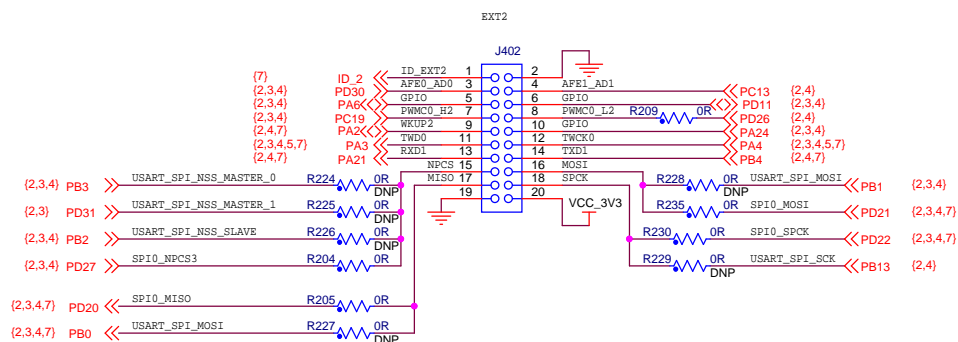
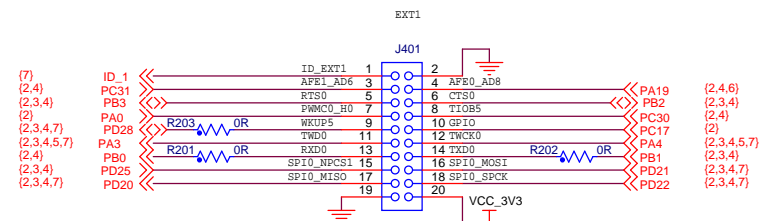
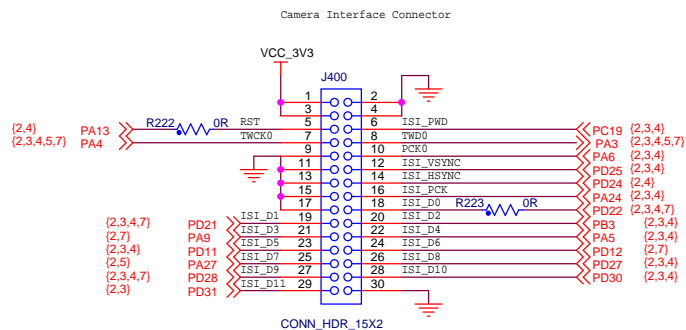
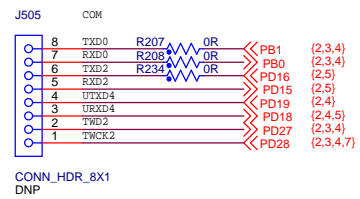
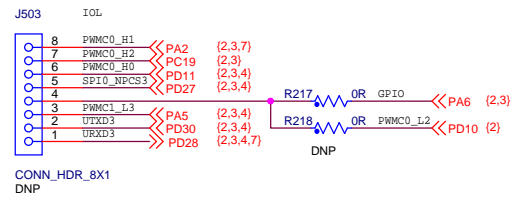
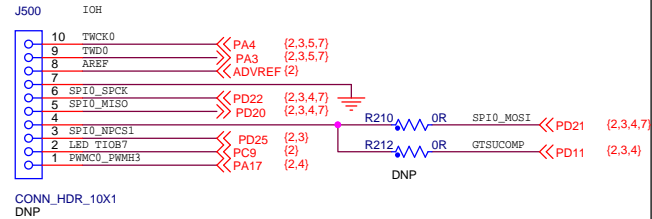
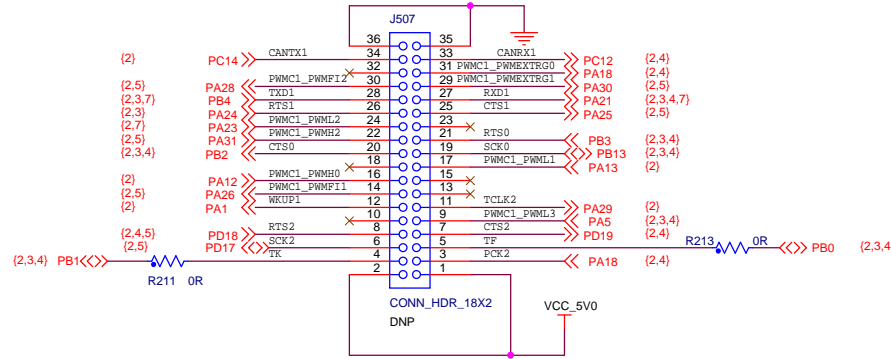
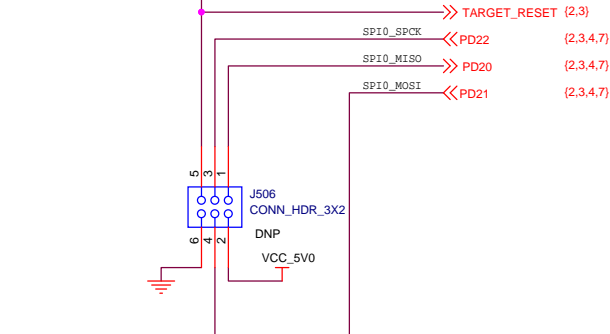
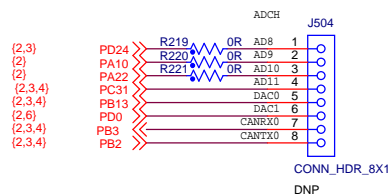
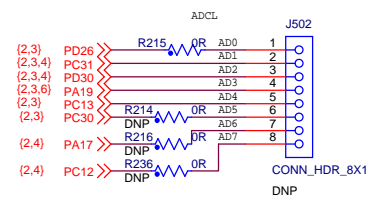
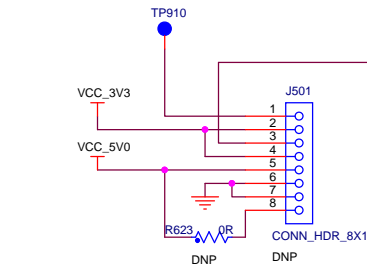


RevA	Embest	14-Oct-15			
INIT	EDIT	DES.	DATE	VER.	DATE
SCALE	1/1			REV. A	SHEET 1/1
TOP					

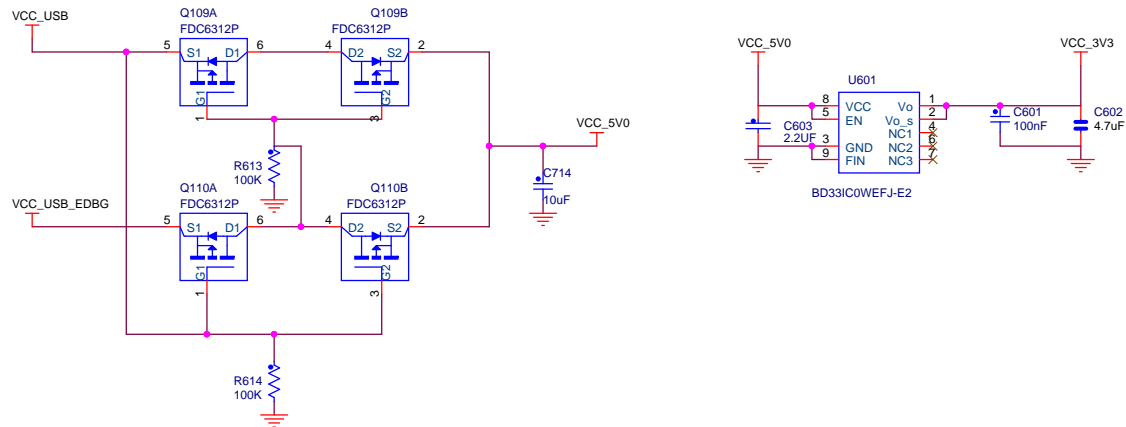
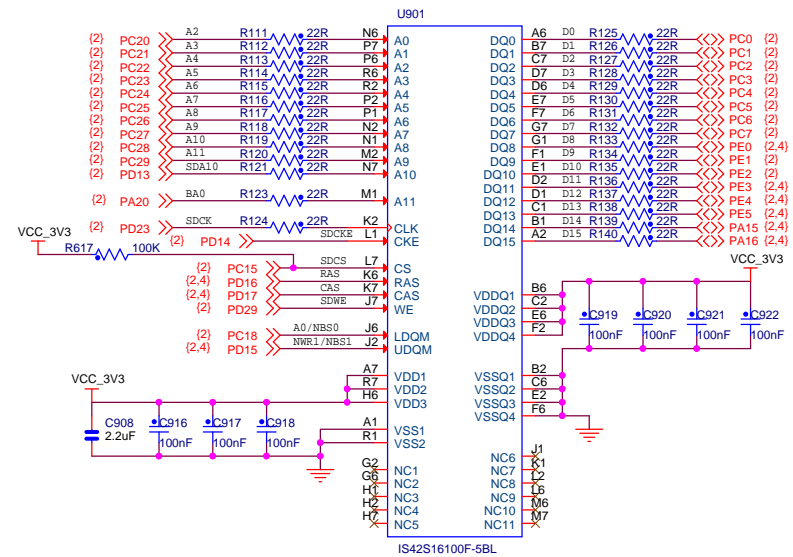
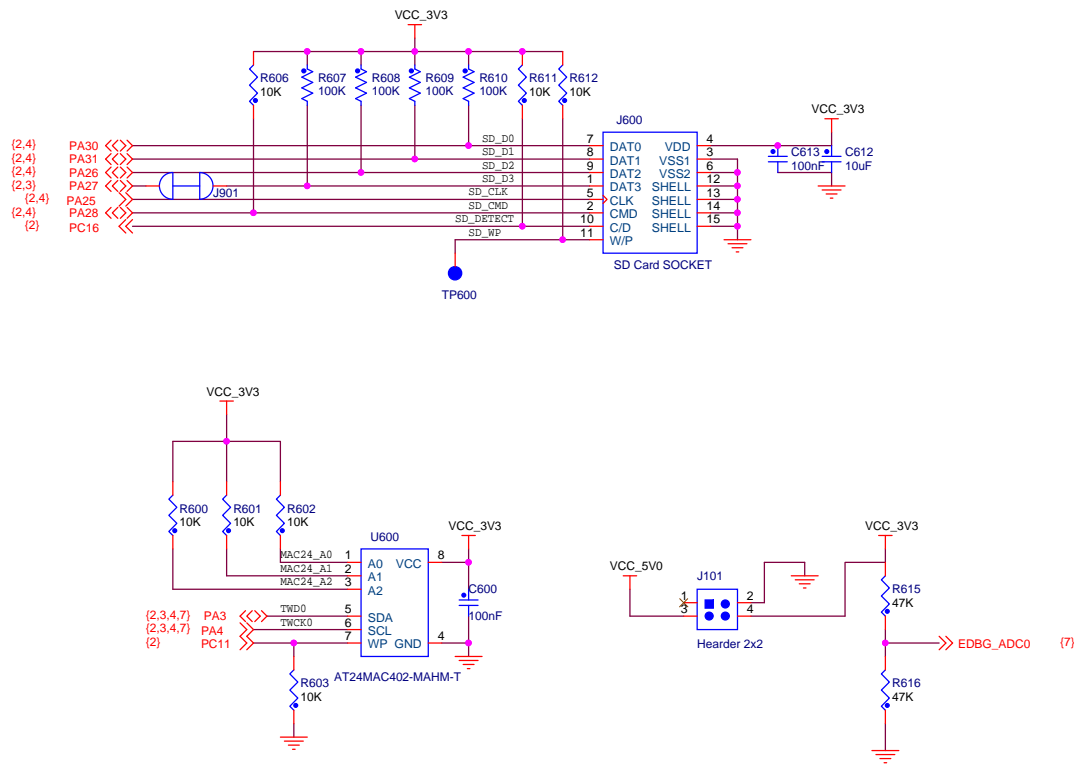




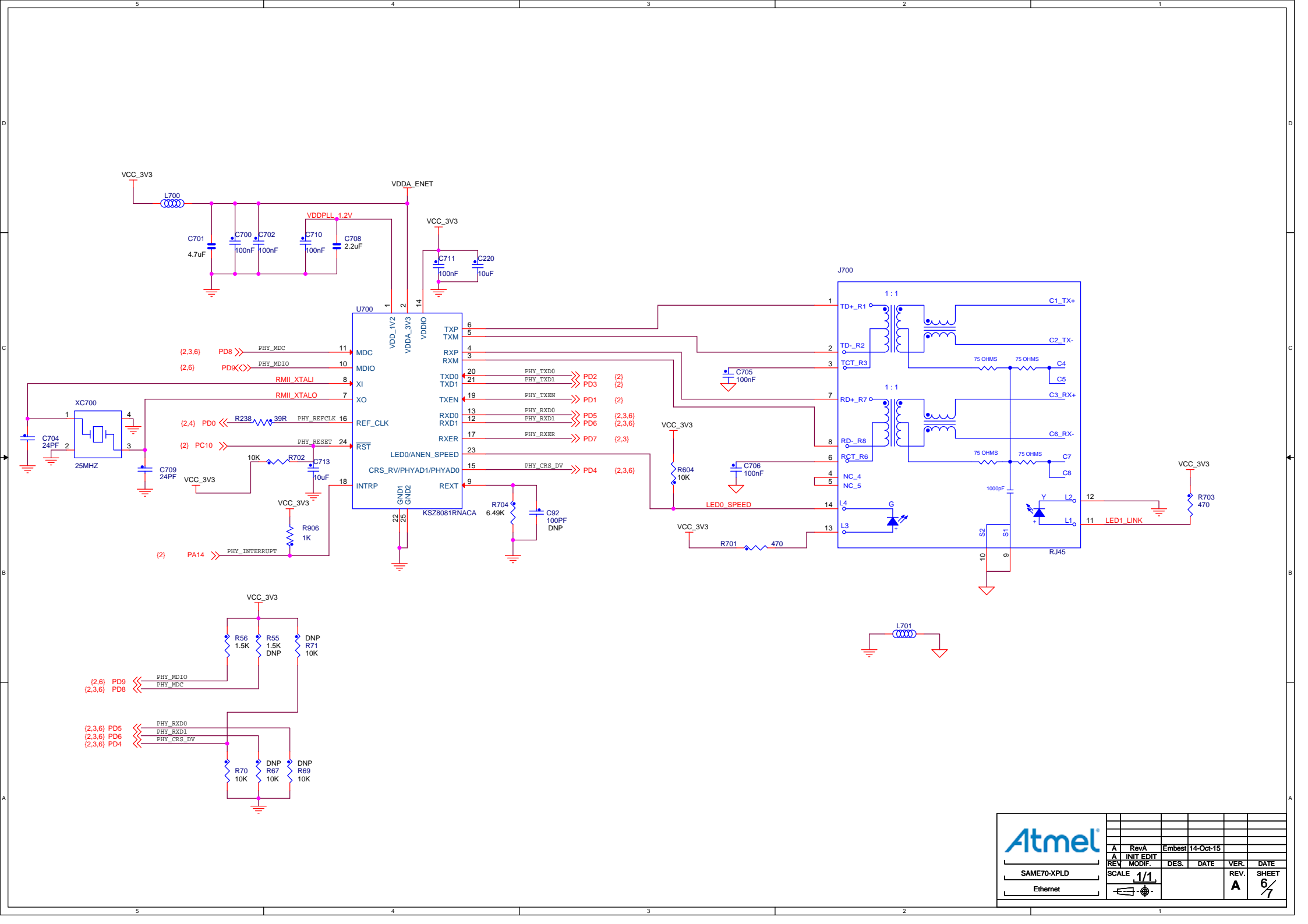


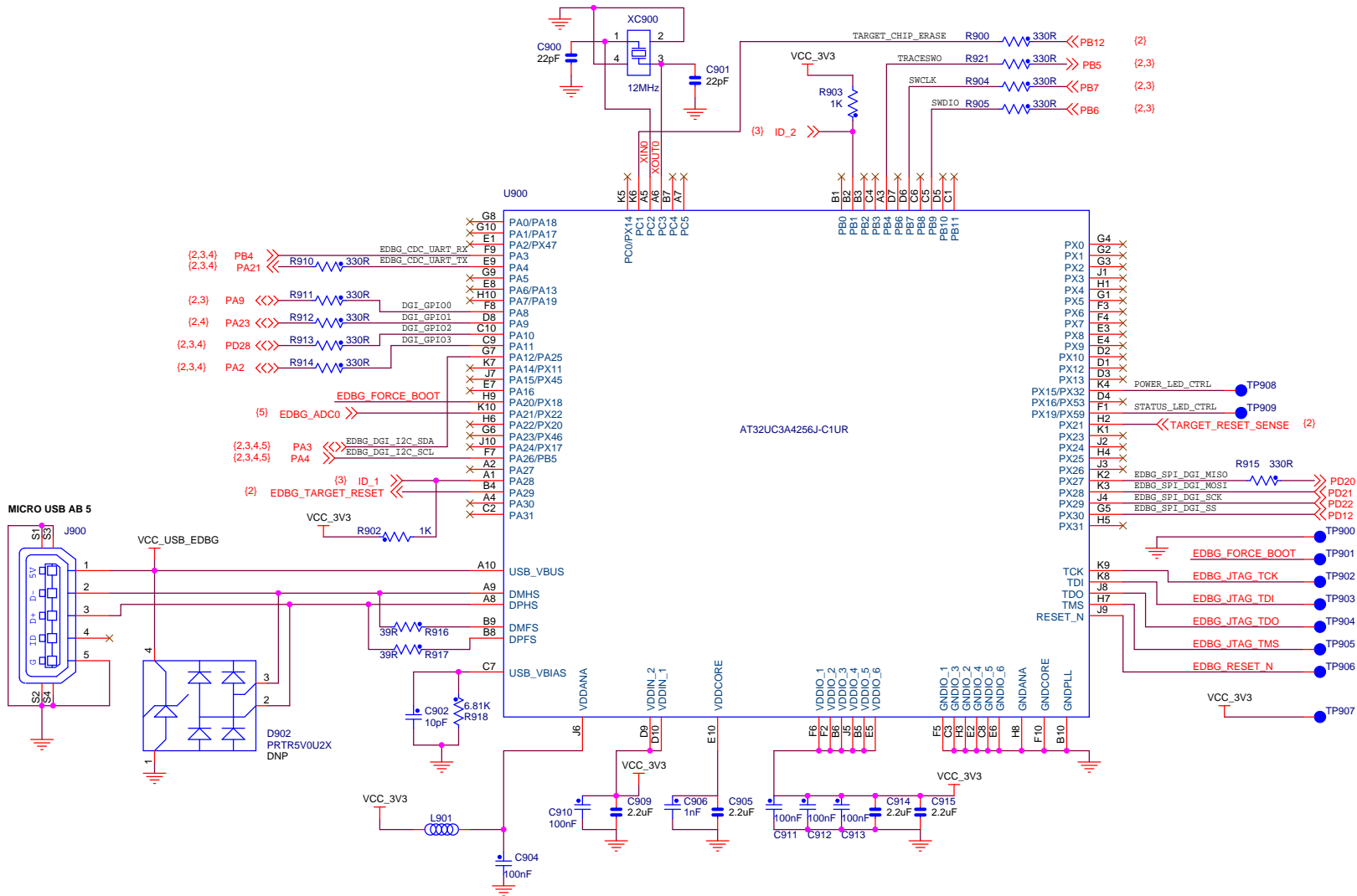
SAME70-XPLD
Shield Connectors

Rev	INIT EDIT	DES.	DATE	VER.	DATE
RevA	Embest	14-Oct-15			
SCALE	1/1			REV. A	SHEET 4/7



Atmel		RevA	Embest	14-Oct-15		
		INIT EDIT	DES.	DATE	VER.	DATE
SAME70-XPLD		SCALE 1/1			A	5/
Memory & Power						





(2,3,4) PB4 >> R910 330R EDBG_CDC_UART_RX
(2,3,4) PA21 >> R910 330R EDBG_CDC_UART_TX
(2,3) PA9 << R911 330R DGI_GPI00
(2,4) PA23 << R912 330R DGI_GPI01
(2,3,4) PD28 << R913 330R DGI_GPI02
(2,3,4) PA2 << R914 330R DGI_GPI03
(5) EDBG_FORCE_BOOT
(5) EDBG_ADC0 >>
(2,3,4,5) PA3 << EDBG_DGI_I2C_SDA
(2,3,4,5) PA4 << EDBG_DGI_I2C_SCL
(3) ID_1 >>
(2) EDBG_TARGET_RESET >>
(2,3,4) PD20 >> R915 330R EDBG_SPI_DGI_MISO
(2,3,4) PD21 >> EDBG_SPI_DGI_MOSI
(2,3,4) PD22 >> EDBG_SPI_DGI_SCK
(2,3) PD12 >> EDBG_SPI_DGI_SS

Connect TP900 to TP901 during startup of the EDBG to force it into bootloader mode