

## Thierry Tambe

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EDUCATION	<b>Harvard University</b> , Cambridge, MA Ph.D., Electrical Engineering Thesis Title: <i>Architecting High Performance Silicon Systems for Accurate and Efficient On-Chip Deep Learning.</i> <b>Texas A&amp;M University</b> , College Station, TX M.Eng., Electrical Engineering B.S., Electrical Engineering	2023   2012 2010
EXPERIENCE	<b>Stanford University</b> , Stanford, CA <i>Assistant Professor, Department of Electrical Engineering</i> <b>NVIDIA</b> , Santa Clara, CA <i>Visiting Research Scientist, Accelerators &amp; VLSI Research Group</i> <b>Harvard University</b> , Cambridge, MA <i>Graduate Researcher, Harvard Architecture, Circuits, and Compilers Group</i> <b>NVIDIA</b> , Virtual <i>Research Intern, ASIC &amp; VLSI Research Group</i> <b>Intel</b> , Hillsboro, OR <i>Senior Design Engineer, Scalable Perf. CPU Development Group</i> <b>Intel</b> , Hillsboro, OR <i>Graduate Intern, Converged Core Development Organization</i> <b>Biotronik</b> , Sugar Land, TX <i>IC Design Intern, Texas Design Center</i>	2024 - Present  2023 - 2024  2018 - 2023  2021 - 2021  2012 - 2017  2011 - 2011  2010 - 2010
PUBLICATIONS	Full list at <a href="#">lab website</a> and <a href="#">Google Scholar</a> .	
TEACHING	EE180 – Digital Systems Architecture EE271 – Introduction to VLSI Design MIT ESP Spark Educational Outreach CS248 – Advanced Design of VLSI Circuits and Systems	Winter 2025 Autumn 2024 Mar 2021 Spring 2020
AWARDS	Google ML and Systems Junior Faculty Award NVIDIA Graduate Fellowship IEEE SSCS Predoctoral Achievement Award IEEE MICRO Top Picks Honorable Mention Finalist for the Lemelson-MIT Student Prize ACM SIGDA Research Highlights Nominee Best Paper Award at ACM/IEEE Design Automation Conference (DAC)	2025 2021 - 2022 2021 - 2022 2022 2021 2021 2020
SERVICE	<b>Organizing Committee</b> <ul style="list-style-type: none"><li>MICRO Workshop and Tutorial Committee Co-Chair</li><li>DAC Session Co-Chair</li><li>The NOPE Workshop @ASPLOS</li></ul> <b>Technical Program Committee</b>	2024 2024 2022

• ISCA, ASPLOS, MICRO, ESSERC, ISLPED	2025
• ISCA, HPCA, DAC, ISLPED	2024
• ML Commons Rising Stars, YArch Workshop	2024
<b>Invited Reviewer</b>	
• IEEE Journal of Solid-State Circuits (JSSC)	2024 - 2025
• IEEE International Solid-State Circuits Conference (ISSCC)	2024
• IEEE Transactions on CAD of Integrated Circuits and Systems	2023
• Design Automation Conference (DAC)	2022
• IEEE Transactions on Neural Networks and Learning Systems	2021
<b>Stanford University</b>	
• Electrical Engineering Department Student Life Committee	2024 - 2025