

Thierry Tambe

CONTACT	Paul G. Allen Building 330 Jane Stanford Way Stanford, CA 94305	Web: https://thierrytambe.com E-mail: ttambe@stanford.edu
EDUCATION	Harvard University , Cambridge, MA Ph.D., Electrical Engineering 2023 Thesis Title: <i>Architecting High Performance Silicon Systems for Accurate and Efficient On-Chip Deep Learning.</i> Texas A&M University , College Station, TX M.Eng., Electrical Engineering 2012 B.S., Electrical Engineering 2010	
EXPERIENCE	Stanford University , Stanford, CA <i>Assistant Professor, Department of Electrical Engineering</i> 2024 - Present NVIDIA , Santa Clara, CA <i>Visiting Research Scientist, Accelerators & VLSI Research Group</i> 2023 - 2024 Harvard University , Cambridge, MA <i>Graduate Researcher, Harvard Architecture, Circuits, and Compilers Group</i> 2018 - 2023 NVIDIA , Virtual <i>Research Intern, ASIC & VLSI Research Group</i> 2021 - 2021 Intel , Hillsboro, OR <i>Senior Design Engineer, Scalable Perf. CPU Development Group</i> 2012 - 2017 Intel , Hillsboro, OR <i>Graduate Intern, Converged Core Development Organization</i> 2011 - 2011 Biotronik , Sugar Land, TX <i>IC Design Intern, Texas Design Center</i> 2010 - 2010	
PUBLICATIONS	Full list at lab website and Google Scholar .	
TEACHING	EE180 – Digital Systems Architecture Winter 2025 EE271 – Introduction to VLSI Design Autumn 2024 MIT ESP Spark Educational Outreach Mar 2021 CS248 – Advanced Design of VLSI Circuits and Systems Spring 2020	
AWARDS	NVIDIA Graduate Fellowship 2021 - 2022 IEEE SSCS Predoctoral Achievement Award 2021 - 2022 IEEE MICRO Top Picks Honorable Mention 2022 Finalist for the Lemelson-MIT Student Prize 2021 ACM SIGDA Research Highlights Nominee 2021 Best Paper Award at ACM/IEEE Design Automation Conference (DAC) 2020	
SERVICE	Organizing Committee • MICRO Workshop and Tutorial Committee Co-Chair 2024 • DAC Session Co-Chair 2024 • The NOPE Workshop @ASPLOS 2022 Technical Program Committee • ISCA, ASPLOS, ISLPED 2025 • ISCA, HPCA, DAC, ISLPED 2024	

- ML Commons Rising Stars, YArch Workshop 2024
- Invited Reviewer**
- IEEE Journal of Solid-State Circuits (JSSC) 2024
- IEEE International Solid-State Circuits Conference (ISSCC) 2024
- IEEE Transactions on CAD of Integrated Circuits and Systems 2023
- Design Automation Conference (DAC) 2022
- IEEE Transactions on Neural Networks and Learning Systems, 2021
- Stanford University**
- Electrical Engineering Department Student Life Committee 2024 - 2025