CAD Design Project 1 – Circuit Graph Construction Due: 23:55, Oct. 2, 2019

Graph is a commonly used computer data structure to handle electronic circuits. In this kickoff project, you are required to construct a circuit graph from a text-input file in BLIF format. BLIF is developed by University of California, Berkeley to describe a circuit in Boolean level. You need to read Section 1 and Section 2 of the BLIF document first. After understanding how BLIF works, you are required to write a program, which parses a BLIF file and constructs its corresponding circuit graph, on a Linux environment. The constructed circuit graph will be used in following projects. There are 3 requirements for your program:

- 1. Output the Boolean functionality of each node into a text file (file name: function.out).
- 2. Report the predecessors and successors of a node.
- 3. Upload your source code tarball (*.tgz) to moodle (including your Makefile).

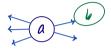
(NOTE: The uploaded file name should be the same with your student ID.)

```
Circuit-node
BLIF Example: sample01.blif
.model sample01
inputs a b c d e
.outputs h j
.names b c d f
101 1
.names b c d q
11- 1
--1 1
.names a f h
1- 1
-0 1
.names e f g i
110 1
                                                             circuit_graph
001 1
.names i j
0 1
.end
Run-time Example:
%> blif_parser sample01.blif
Please input a node: f
predecessor: b, c, d
successor: h, i
Please input a node: d
predecessor: -
                               # no predecessor
successor: f, g
Please input a node: x
node x does not exist
Please input a node: h
predecessor: a, f
successor: -
                               # no successor
                               # '0' stands for end of user inputs
Please input a node: 0
%> cat function.out
Node function:
f = b c' d
g = b c + d
h = a + f'
i = e f g' + e' f' g

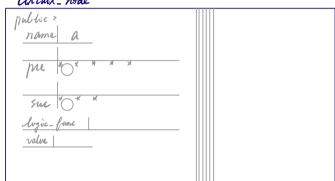
j = i'
END
```

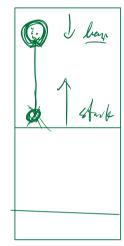
circuit ararl

public:				MSvote =
all node				'
NOW → \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$	11 Sorput rolles			
* D *D	11 outpril_nodes			
T	11-intermedt_nodes			
•				
make_input_nodes (input_node_name)				presearch (forde, brode, name)
make - output_ nodes (input_ node name)				
make_ into _ nodes (inter _ rode _ ro	me)			



circuit_ node





CC

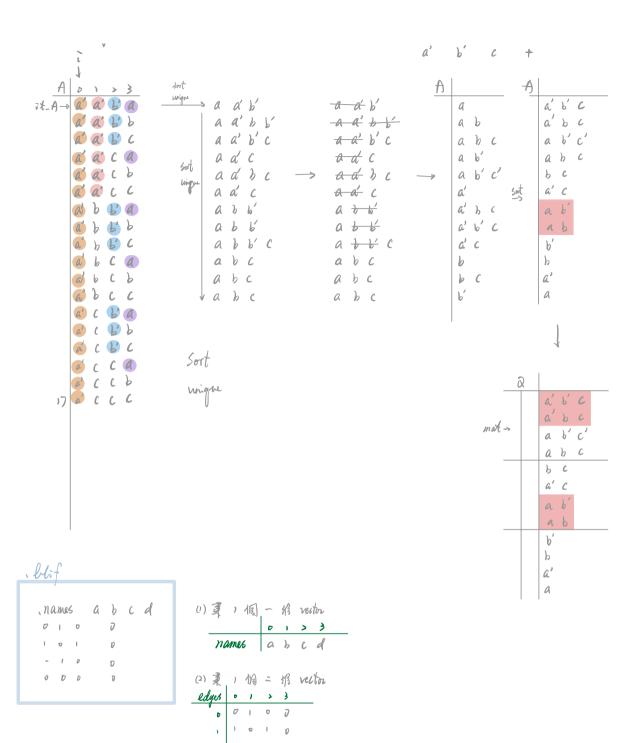
names
$$a \ b \ c \ d$$

eldge \bar{j}
 $\bar{i} = 0 \ i \ 0 \ 0$
 $\bar{j} \Rightarrow a \ a'$
 $\bar{j} \Rightarrow b \ b'$
 $\bar{c} = (a + b' + c)$
 $(a' + b + c')$
 $(a' + b + c')$
 $(a + b + c)$
 $a \ b \ c$
 $a \ b \ c$

C C'

$$d = a'bC' + ab'C + ab'C + bc' + bc' + a'b'C'$$

4	v	ſ	۵		Lit
0	á	b	C	0)
1	a	b	C	1= 3×2	143
5	b'	C			1×3×3
ક	a	b	C)	(= }=)=>



(3) 13 25 rector 2 rector 2 string > func (names, edges)

restor & restor & string > func (names, edges) }

edges						A	0	1	۷		d	=	(a'	-	6'	+	c).
0	0	1	Ø	0	蒋成 、	A	а	b	C				(a'	+	b	+	c')·
ì	١	0	1	0	>4/3, vector A)	a'	b	c'	表本、			(b'	+	()		

3 0 0 0

```
C(B, t, it-A) {
    ; f ( ; == B, size() - 1 ) }
       for (j(0); j != P[i] siu()-1; ++j)}
          copy = * > t- A >
          it.A > push back (B(i](j));
          ++ ;+_A;
          * it-A = wpy;
       it-A > push-back (B[;][B.size()-1]);
       ++ 24-A:
    else {
       for ( j(s) = j ?= B(i), size(); ++j) }
          if (j ?= 0) }
             it-Asemplace_back ((it-A-1) > begin(), (it-A-1) > (begin()+i));
            it-A > push-back(B(iJEjI);
((B, i+1, t+it-A);
          Mr }
             it-A = emplou-back (B[i][j]);
C(B, iti, ++ Tt.A);
```