

CAD Design Project 2 – Constrained Scheduling

Due: 23:55, Oct. 24, 2019

In this project, you are required to implement two versions of list scheduling algorithms for constrained scheduling problems. (1) The ML-RCS is the scheduling problem to minimize latency under resource constraints. (2) The MR-LCS is the scheduling problem to minimize resource under latency constraint. Your program would be evaluated on Linux environment according to the following requirements.

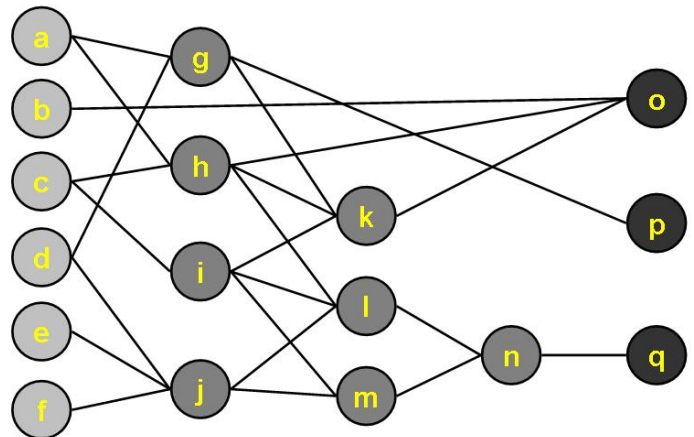
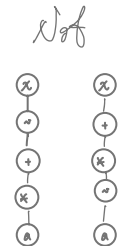
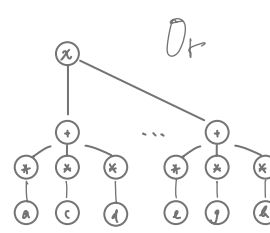
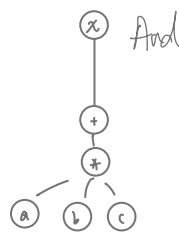
1. For simplicity, there are only 3 types of Boolean operations: *AND*, *OR*, and *NOT*.
 2. Assume that every operation takes 1-cycle latency. (The PI node is not an operation.)
 3. Read a BLIF file and the corresponding resource or latency constraints.
 4. For ML-RCS, use option "-l", output the scheduled result under resource constraints.
 5. For MR-LCS, use option "-r", output the scheduled result under latency constraint.
 6. Upload your source code tarball (*.tgz) to moodle (including your Makefile)
- (NOTE: The uploaded file name should be the same with your student ID.)
7. Generate and upload one "difficult" test case with ReadMe file.

BLIF Example: sample02.blif

```
.model sample02
.inputs a b c d e f
.outputs o p q
.names a d g
1- 1
-1 1
.names a c h
11 1
.names c i
0 1
.names d e f j
1-- 1
-1- 1
--1 1
.names g h i k
1-- 1
-1- 1
--1 1
.names h i j l
111 1
.names i j m
11 1
.names l m n
11 1
.names b h k o
111 1
.names g p
0 1
.names n q
0 1
.end
```

Logic

ded-out



SYNOPSIS for ML-RCS

```
%> list -l BLIF_FILE AND_CONSTRAINT OR_CONSTRAINT NOT_CONSTRAINT
```

Run-time Example:

```
%> list -l sample02.blif 2 1 1
Resource-constrained Scheduling
1: {h} {j} {i}
2: {l m} {g} {}
3: {n} {k} {p}
4: {o} {} {q}
#AND: 2
#OR: 1
#NOT: 1
END
```

Run-time Example:

```
%> list -l sample02.blif 1 0 1
Resource-constrained Scheduling
No feasible solution.
END
```

SYNOPSIS for MR-LCS

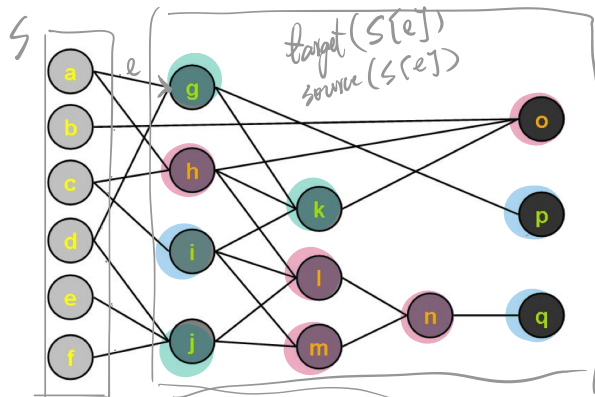
```
%> list -r BLIF_FILE LATENCY_CONSTRAINT
```

Run-time Example:

```
%> list -r sample02.blif 5
Latency-constrained Scheduling
1: {h} {j} {i}
2: {m} {} {}
3: {l} {g} {}
4: {n} {k} {p}
5: {o} {} {q}
#AND: 1
#OR: 1
#NOT: 1
END
```

Run-time Example:

```
%> list -r sample02.blif 3
Latency-constrained Scheduling
No feasible solution.
END
```



Top bottom

node				
	name			
	pre			
	suc			
ready	0	1	2	3
ready()	0	0	0	1

graph	
	all nodes

Run-time Example:

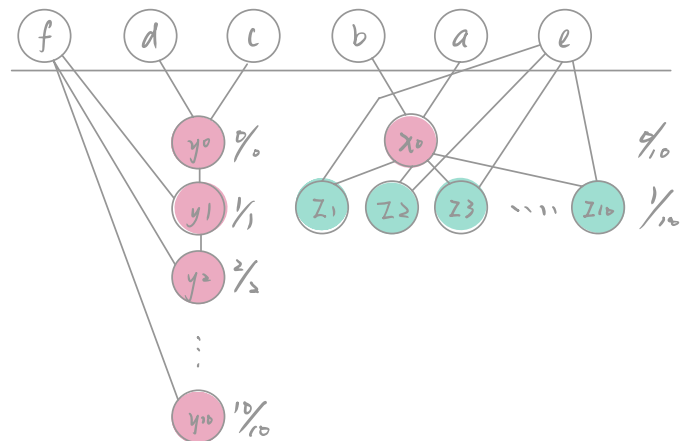
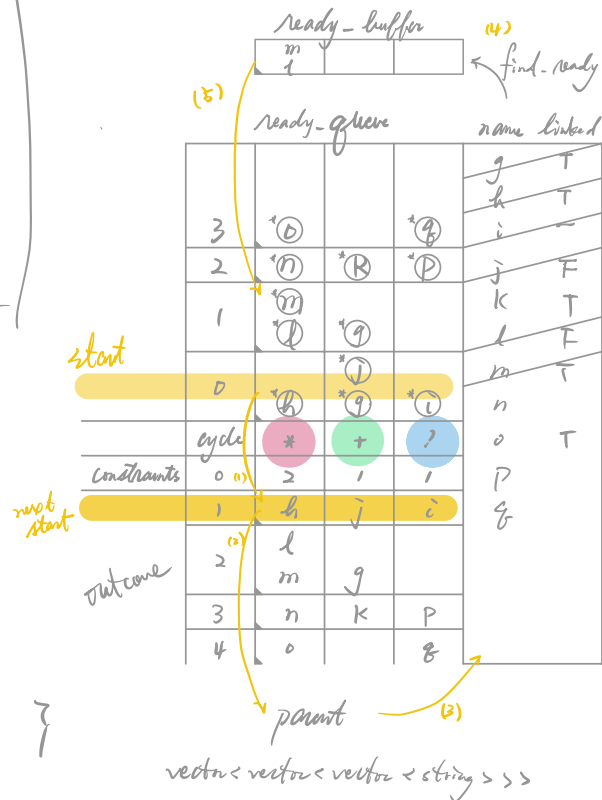
```
%> list -l sample02.blif 2 1 1
Resource-constrained Scheduling
1: {h} {j} {i}
2: {l m} {g} {}
3: {n} {k} {p}
4: {o} {} {q}
#AND: 2
#OR: 1
#NOT: 1
END
```

Run-time Example:

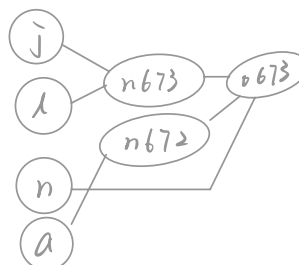
```
%> list -l sample02.blif 1 0 1
Resource-constrained Scheduling
No feasible solution.
END
```

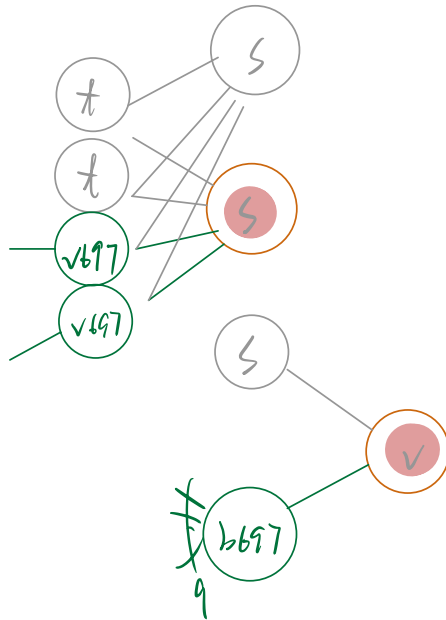
	0	1	2

RCS (graph, constraint) {



gate_cnt = 685





outputs s t v
 ————— violation
 .names t v697 s
 .names s b709 v

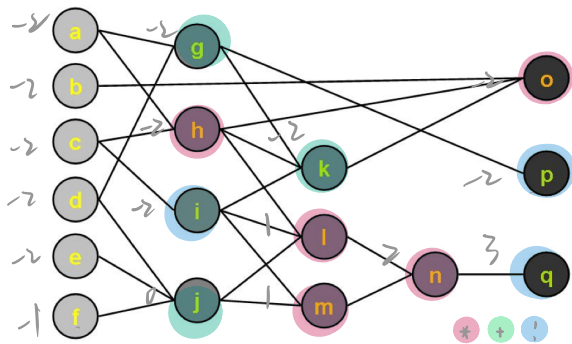
G

all_nodes

0				input
1	t	v697	s	inner
2	s	t	v	output



.names t v697 (s)



Run-time Example:

```
%> list -r sample02.blif 5
```

Latency-constrained Scheduling

1: {h} {j} {i}

2: {m} {} {}

3: {l} {g} {}

4: {n} {k} {p}

5: {o} {} {q}

#AND: 1

#OR: 1

#NOT: 1

END

Run-time Example:

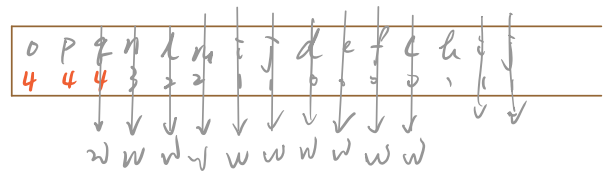
```
%> list -r sample02.blif 3
```

Latency-constrained Scheduling

No feasible solution.

END

	visited	A	R	S	D	open	logic function	pu	sum
a		∞	0			∅	∅		
b		∞	3			∅	∅		
c		∞	0			∅	∅		
d		∞	0			∅	∅		
e		∞	0			∅	∅		
f		∞	0			∅	∅		
g	T	0	2	2		●			
h	T	0	1	1		●			
i	T	0	1	1		●			
j	F	0	1	1		●			
k	F	1	3	2		●			
l	T	1	2	1		●			
m	T	1	2	1		●			
n		2	3	1		●			
o	T	2	4	2		●			
p		1	4	3		●			
q		3	4	1		●			



→ -) 0

LCS

(1) (2)

(3) check if feasible

constraints	1	1	1	time
cycle	operation			
1	h	j	i	0
2	l	g		1
3	m	k	p	2
4	n			3
5	o		q	4
				5

us

if constraints too strict,
++ constraints, clear schedule, queue, clear visit
find ready

(6)

(5)

(4)

priority queue

F

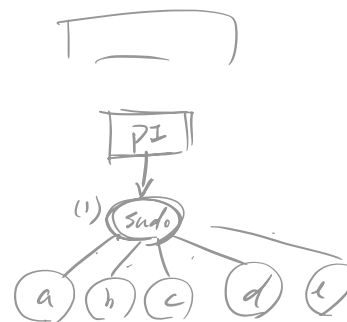
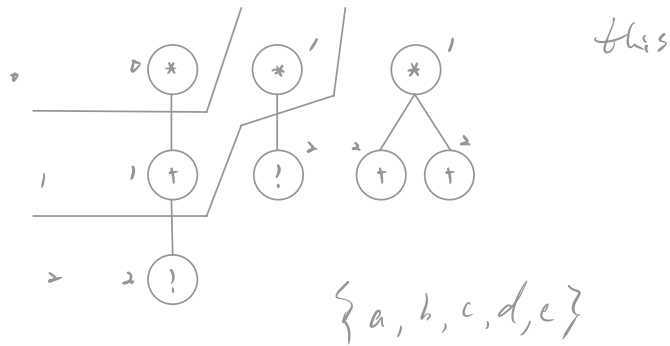
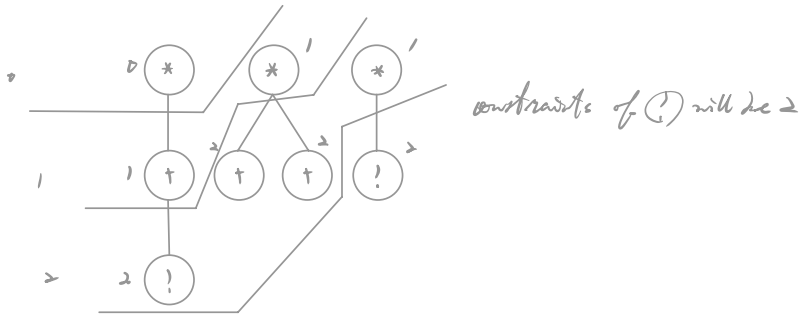
B

ready buffer

priority queue	F	B	ready buffer
h	h	h	h
g	g	g	g
i	i	i	i
j	j	j	j
k	k	k	k
l	l	l	l
m	m	m	m
n	n	n	n
o	o	o	o
p	p	p	p
q	q	q	q

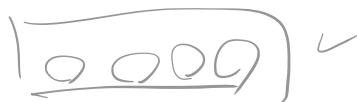
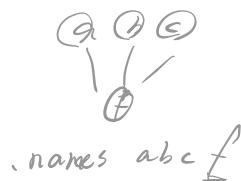
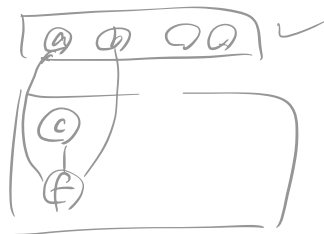
while (!all scheduled)



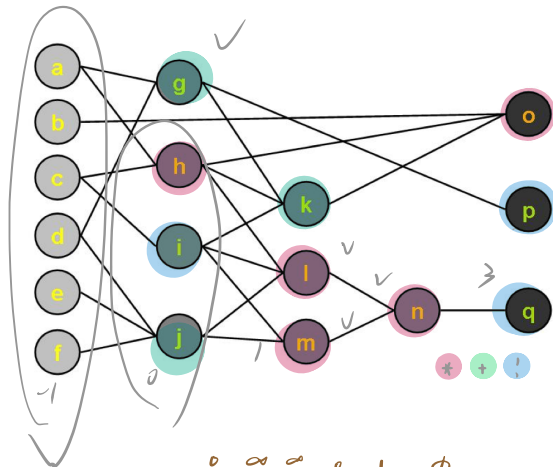


new node("a")

Sudo, suc, pushback(new node("a"))



mR-LCS



```
Run-time Example:
%> list -r sample02.blif 5
Latency-constrained Scheduling
1: {h} {j} {i}
2: {m} {} {}
3: {l} {g} {}
4: {n} {k} {p}
5: {o} {} {q}
#AND: 1
#OR: 1
#NOT: 1
END
```

```
Run-time Example:
%> list -r sample02.blif 3
Latency-constrained Scheduling
No feasible solution.
END
```

	visited	A	R	S	D	open	logic function	mu	sum
a		∞	0			\emptyset	\emptyset		
b		∞	3			\emptyset	\emptyset		
c		∞	0			\emptyset	\emptyset		
d		∞	0			\emptyset	\emptyset		
e		∞	0			\emptyset	\emptyset		
f		∞	0			\emptyset	\emptyset		
g	T	0	2	2	1	●			
h	T	0	1	1	1	●			
i	-	0	1	1	1	●			
j	F	0	1	1	1	●			
k		1	3	2	1	●			
l		1	2	1	1	●			
m		1	2	1	1	●			
n		2	3	1	1	●			
o	-	2	4	2	1	●			
p		1	4	3	1	●			
q		3	4	1	1	●			

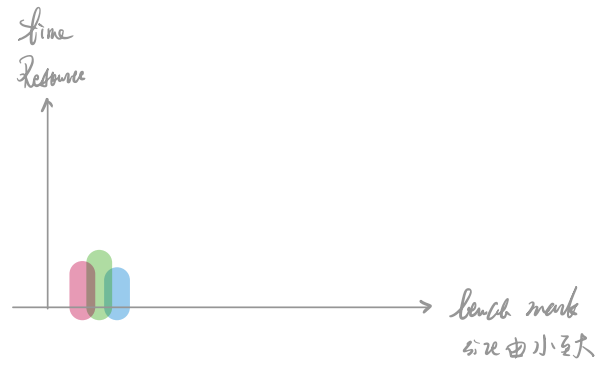
1	h	j	i	0
2	l			1
3				2
4				3

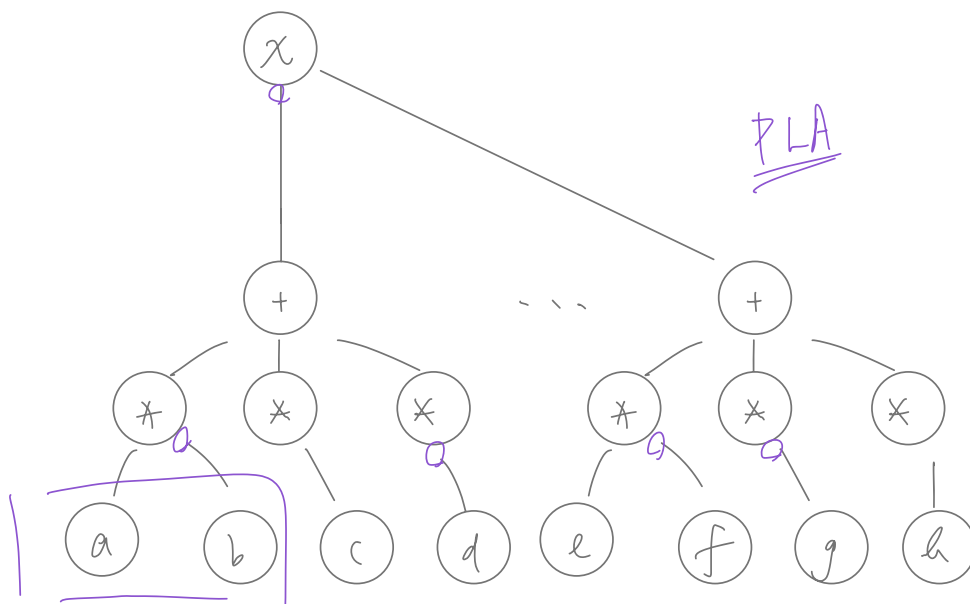
- MR-LCS
 - 滚算法证明
 - sudo code
 - time complexity 分析
 - worst case

- FD-LCS
 - 滚算法证明
 - sudo code
 - time complexity 分析

- 实验数据比较
 - bench mark
 - worst case

- 结论



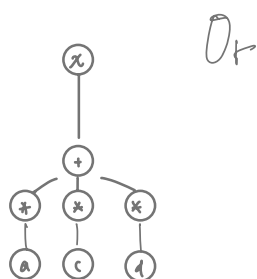
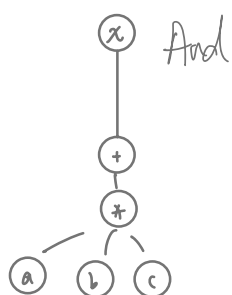


$$\begin{array}{cccccc}
 e & f & g & h & x & \\
 | & | & | & | & | & \\
 - & - & - & - & - & \\
 | & | & | & | & | & \\
 - & - & - & - & - & \\
 | & | & | & | & | & \\
 \end{array}$$

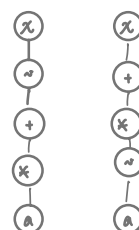
$$out = (vec<(vec>, vec>), vec<(vec>, vec>))$$

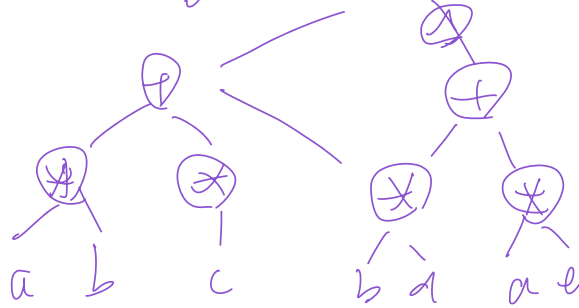
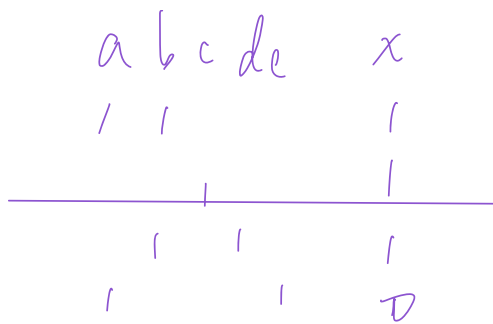
$$Or = vec<(vec>, vec>)$$

$$and = (vec>, vec>)$$



Not





$$x' = (ae)$$

$$x = v(ae)$$