Microcontroller Board

Frequency Select

Transceiver Module

EEPROM for Audio Processor

USB Type-C Receptacle (UFP)

Audio Processor

Audio Amplifier

XLR Connection

9V Battery Connection

Headphone Jack

Headphone Amplifier

WP (Audio Processor) should be ground to allow EEPROM to be written towards. EEPROM is the firmware storage.

filter high and low frequency noise and provide stable power.

Base Pin > 470k to bias the transistor and

(Optional) GDO0 used as packet recieved interrupt pin and GDO2 used as a data clock (synchronous)

If PCB traces are longer than 10cm, additional components are required t

SDA requires a pull up resistor. Typically, 10k for 100kHz and 2k for 400kHz and 1MHz.

Tied to VCC, write operations are disabled but read is unaffected. Tied to VSS, write operations are

Pull up resistor prevents floating WP pin

SelfBoot is GND to disable this mode.

ADC1 is now unused as the hot signal was directed to a single-ended output. *See

OUT+ from Boost Converter and 9 V Battery > carries the HOT end of the mic

2N3055G NPN BJT transistor. classic

This is an NPN transistor configured

Collector Pin > 10k to determine voltage

Emitter Pin

COLD end of mic is carried through

First & Second Normally Closed (PI

Normally closed - signal routes when