Athanasios (Thanassis) Tsiodras

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Education

Sept. 1995 – June 1999
PhD, *National (Metsovian) Technical University of Athens, (NTUA).*
Telecom Engineering *(ATM, IP over ATM, RSVP, QoS guarantees)*

Sept. 1990 – June 1995
SW Engineering, *National (Metsovian) Technical University of Athens, (NTUA).*
(Scored the 9th best score in all Greece in the entrance examinations, received scholarship for being in the top ten).

Experience

Professional coding

Jan. 2016 – today
Real-time Embedded Software Engineer, *European Space Agency, ESTEC, TEC-SWE/TEC-SWT.*
Over the last 6 years...

- I introduced and meticulously applied Static Analysis to the SW of our missions; gradually pushing it to become a mandatory part of our reviews. I have reviewed more than 80 missions' codebases, including but not limited to EXOMARS, GALILEO, Solar Orbiter, METOP, MTG, CHEOPS, PROBA3, Sentinel-2, Sentinel-5, EarthCARE, EDRS, SAT-AIS, EUCLID, NEOSAT, VEGA-C, and many more... In doing so, I have identified and led to the resolution of 1000s of bugs - that would have otherwise slipped through the cracks: NULL pointer dereferences, out of bounds accesses, uninitialized accesses, race conditions, and many more... *(Technologies and tools involved: Formal verification and Static Analysis, Clang Static Analyser, Infer, Polyspace Bug Finder/Code Prover, Coverity, and many more - including custom scripts I wrote that parse the code via libclang and allow for complex validations and call-graph related queries).*

- I applied DevOps automation everywhere possible (a) custom meta-distribution collecting dozens of tools (b) Docker-based Continuous Integration that constantly hunts for regressions inside it, in both CircleCI and Jenkins (c) scripted and automated build of LEON-targetting cross compilers and BSPs via a *chroot*, (d) integration of CoRA workflows in the ESA-hosted *CSDE* - integrating Bitbucket-driven and Jenkins-based CI, as well as automated cross-referencing in the corresponding JIRA tickets (e) Parsing of C source code via Clang to automatically detect dead code, and many more. I basically believe that if it can automated, it should be - humans should not be doing the work that machines can.

- I acquired hands-on knowledge in the ways of RTEMS, the OS mostly used in our missions. I have identified bugs in the RTEMS/Sparc configuration, and have provided fixes for complex issues - e.g. I debugged and traced regressions in how the linker scripts were setup, and how they were interacting (in a very complex manner) with the ordering of constructor/coverage stubs. I have also assisted in the context of missions with hands-on debugging and task accounting/schedulability issues.

- Speaking of coverage, I put the pieces together for a fully open-source and portable way of performing embedded coverage via *gcov* that automatically delivers the coverage information back to the host via *gdb*. This has been picked up by various missions - and has also been used outside the space domain, by other embedded SW practitioners.

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- I wrote a whitepaper that is used inside the Agency on how to do category A traceability from object code back to source code - using a number of open-source tools and UNIX scripting.
- I have been one of the key figures in what is currently one of the best attempts at model-driven SW engineering in the Agency; by maintaining a number of code generators that automatically integrate a multitude of tools and technologies: Simulink dataflows, SDL state machines, bespoke C and Ada code, and even VHDL-based FPGA logic.
- Speaking of FPGAs: even though I am not a HW designer by trade, my close proximity to HW designers over the years - writing low-level device drivers and programming RTOS-es next to them - has, over the years, made me intimately aware of many of the nuances of FPGA programming. For example, I recently wrote an article that gathered more than 100K views on Slashdot, Reddit and Hacker News, on how I bootstrapped a multicore Leon3 design on an abandoned Spartan6-hosting device. Another example is a real-time Mandelbrot zoomer that I wrote in VHDL, using fixed-point arithmetic - written for fun, and running on a Spartan3 device. After computing the fractal, the image is DMA-ed back to the host PC over USB.
- I am also involved in many more things in the Agency, of both a practical and an R&D nature. . . **My contributions have been recognized officially; my position was elevated to that of Senior SW Engineer in the Agency two years ago**.

Mar. 2015 – **ZenDoc**, *EverWorks*.
Dec. 2015   I undertook the responsibilities of the backend:
- created much faster implementations for our Pyramid-based endpoints - speaking to PostgreSQL over SQLAlchemy, and utilizing various service APIs during the process (MixPanel, Intercom, Segment, Recurly, Amplitude, etc). When the wrapper libraries around the HTTP APIs were not sufficient, debugging their insides was quite "interesting" (*for various definitions of interesting :-)*)
- integrated my new search-related webservices with Elastic Search
- created a standalone RabbitMQ-based queue processor that consumes async events sent from my backend (i.e. work that can be deferred so that it doesn't stall the HTTP responses, is queued in RabbitMQ and processed asynchronously. For example, this is how event notifications are sent to Segment / MixPanel / etc)
- automated many tasks using Python, cron etc - from bulk migration of users' spreadsheets to our DB, to syncing Recurly invoices and account states, etc
- migrated our servers from Rackspace to Amazon AWS. I used *Ansible* to automate large parts of the process, and afterwards I setup *monit* to monitor our services and react to downtime (the webapp is also hooked with Rollbar and NewRelic alerts).
- setup optimal (deduplicated and encrypted) hourly backups via Attic - including DB exports, rootfs, etc. I sync the results automatically to Amazon S3 buckets
- Overall: **I did both *Dev* and *DevOps***.

Sept. 2012 –   **GAIA**, *NeuroPublic*.
Mar. 2015   I applied my coding and administration skills as a full-stack engineer, in the development of
NeuroPublic's GAIA platform:

- Wrote a fully-functional AngularJS component for handling map data, utilizing OpenLayers
  3 and automatically serializing user-provided geometry information (polygons and points)
  in the DB. *(Technologies involved: AngularJS, TypeScript, OpenLayers3)*
- Used *Python/requests* to write a scenario recording proxy. Placed in between browser
  and JBoss, the proxy was used to record complete execution scenarios from our apps,
  involving many steps in many tables. The recorded scenarios were then processed by a
  code generator I wrote, that created stress-testing Python scripts. While doing so, it
  modified the POST and GET requests appropriately, so that the generated scripts could
  simulate hundreds of concurrent users, logging-in and running the scenario *(Technologies
  involved: Python/requests, HTTP)*
- In the backend, a number of clustered JBoss instances were hosted inside CentOS VMs.
  I setup the Subversion repository and automated builds through Hudson - and also coded
  custom web gateways (*Python/Flask* backend, *AngularJS* frontend) for our developers
  and sysadmins: one inspecting and reporting the current version of all the J2EE apps
  hosted in all our development, testing and production servers (being in control of the
  Hudson build scripts, I was able to inject the build revision in the backing bean *.xhtml* -
  and I could therefore query it from the deployed apps at runtime). Knowing what version
  is deployed and what version is available (from Hudson) I also provided ops personnel
  with a "single button" to deploy the latest version of any application they wanted (the
  version built by Hudson) to any of the three environments (dev/test/production), using in
  the backend the command line interface of JBoss (jboss-cli). My gateways also generated
  a number of *.csv* reports, gathering data from our PostgreSQL (via Python/psycopg2)
  and Oracle (via Python/cx_Oracle) databases, and providing them to our ops and sales
  teams. Output from some of these reports was monitored via cronjobs, and appropriate
  alerts were raised when error conditions were detected *(Technologies involved: Python,
  Flask, gunicorn, AngularJS, JBoss, cron)*
- Automated the generation of JasperReports *.jrxml* files, via a custom - 10x times less
  verbose - description. A related article where you can see an example of the language is
  here *(Technologies involved: Python/jinja2/lxml, JasperReports)*

Sept. 2012 –   **Optimizing the StrayLight algorithm**, *Semantix*.
Sept. 2013   In the context of the next Proba launch, the European Space Agency (ESA) will be processing
radiance images on the ground segment with a custom algorithm, called *Straylight*. In
task 2.2 of the "TASTE Maintenance and Evolutions" ESA project, I ported the prototype
Straylight implementation from IDL to C++, and optimized the code using multithreading,
SSE, cache-utilization optimization and CUDA. I achieved a 35x speedup. A further example
of my ability to optimize algorithms and low-level program massively parallel machines can be
found in my real-time CUDA raytracer, showcased in NVIDIA's CUDA Zone. *(Technologies
involved: C++, IDL, Python, Valgrind, Cachegrind, OpenMP, SSE, make)*

2006 – 2012 **Lead Engineer in ESA's ASSERT/TASTE project series**, *Semantix*.

When developing the ASN.1-driven code generators for the Roaming Products (see below) I gained unique experience in auto-generating complex C/C++ code, based on custom-made domain specific languages. After presenting this technology to the European Space Agency, Semantix was immediately invited to join the ASSERT consortium - and in a series of eight (8) ESA-funded projects that followed, I created over 15 custom code generators (using Python and ANTLR). These covered many needs; from automatically creating device drivers that speak to FPGAs, to generating Graphical User Interfaces for TM/TCs, to auto-grafting of interworking C "glue" code for multi-modeling-tool based development (based on a hybrid ASN.1/AADL model). Large amounts of complex - and very error-prone, if manually written - code are automatically generated, translating C data structures at runtime, between C code generated from an ASN.1 compiler (developed in-house in Semantix), and C code generated from modeling tools (SCADE/ Simulink/ ObjectGeode/ PragmaDevRTDS/ etc). *(Technologies involved: Linux, RTEMS, Leon/SPARC, Python, ANTLR, an ever increasing list of modeling tools and their code generators, SystemC, VHDL, Perl, make)*

2005 – 2006 **Lead Engineer in Semantix Roaming products**, *Semantix*.

Lead engineer in the design and development of Semantix Roaming Products. Based on the company's extensive background on processing of roaming data (TAP/BER files), a new product suite was developed that covers all the roaming-data processing of Telcos. I was responsible for the design and development of the central modules in the architecture (e.g. implementing TAP3.x validations, conversions amongst TAP3.x versions, automatic code generation, runtime meta-type system, etc) as well as the porting across non-Microsoft architectures. The custom code generators that I created, parse a small, domain-specific language that I designed, specifically for TAP3 validations; and generate hundreds of thousands of error-free C++ code (we never received a bug report, in spite of the complexity of the 1000s of validation rules and the number of clients using the programs) *(Technologies involved: Solaris, Linux, Windows, portable C++, code generation, gcc, gdb, Microsoft Visual Studio 8, Python, Perl, make)*.

2003 – 2004 **Software Engineer, Telecom projects**, *Semantix*.

Both Vodafone Greece and Telestet Greece (TIM) migrated their legacy rating and billing systems to Infranet/Integrate systems. I was actively involved in the development of a series of turn-key bridging applications: from a massively multithreaded C++ application that migrated customer data from the old system (Jupiter) to Infranet, all the way down to scripting automated procedures and real-time filtering of datasets. Optimizing and speed were key issues. *(Technologies involved: Solaris, C++, gcc, gdb, cron, Perl)*.

2002 – 2003 **Software Engineer/Device driver developer**, *Semantix*.

In the context of a signal monitoring station, I led the design and development of a distributed CORBA-based architecture of device controllers (coded in C++ for Windows 2000), which controlled a number of recording and monitoring devices. The platform monitored the devices and performed innovative "resurrection" techniques to sustain their logic regardless of software crashes and bus-specific idiosyncrasies (RS232/TCP/IP/GPIB). The devices I programmed spanned over a wide range of signal capture and processing: real-time spectrum analysers, GPS and NMEA (compasses), analog and digital recorders (tape, video, real-time grabbers implemented as PCI cards), antenna rotators and more. *(Technologies involved: Windows, C++, CORBA, communicating with HW devices over custom APIs and buses)*

2001 – 2002 **Device driver developer**, *4Plus S.A.*.

I developed 7 Windows device drivers for high speed network boards, used to implement protocol testers. I developed in C (for kernel mode), and the corresponding APIs in C++ (user mode), addressing the needs of the 7 different FPGA designs. The final products were running under Windows NT/Windows 2000, and were used to stress-test the 3G/UMTS Siemens switches, simulating traffic from thousands of users in real-time. *(Technologies involved: C, C++, Windows DDK, Windows SDK)*

Open source coding

1997 – today **Artifacts of my free time programming**.

I write articles about coding and administration on my page on the Web.
- What's running inside my Bash?, an article at the top of Hacker News (C)
- Real-time 3D software rendering, used in Phoronix benchmarks (C++)
- Solving artificial intelligence problems, using multiple languages (OCaml/F#/C#/C++)
- Incremental backups via sparse-files, hard-links, Samba, etc (Linux/Windows administration)
- Shielding files against silent corruption with Reed-Solomon
- Reverse engineering my Android tablet to install Debian on it

...and many more - my articles have been Slashdotted, gained the top spot in Reddit/programming, discussed in Hacker News, etc.

Languages

English	**Fluent**	*Cambridge certificate, grade A*
Greek	**Native speaker**	

Computer skills

Programming	C, C++, SQL (Free time: OCaml/F#, Lisp)	Web	AngularJS, TypeScript, jQuery, OpenLayers3, basic CSS
Scripting	Python, Perl, Bash (on occasion, awk/sed one-liners)	Admin	Linux/BSD, Vagrant, Docker, Jenkins, JIRA, Bitbucket, GitHub, Gitlab CI, CircleCI, TravisCI, Confluence, Mantis, Amazon EC2/S3, ownCloud, Ansible, Attic, monit, cron, etc
Systems	Linux, Solaris/BSDs, Windows	Misc	x86 ASM, SSE, OpenMP, CUDA

Interests

Puzzles/AI	Some of my posts about solving puzzles with artificial intelligence
Soldering iron	Tinkering, mostly. Here's some tangible proof - in various forms.

Publications

[1] T.Tsiodras. *Static Analysis at TEC-S; results after 4 years and more than 70 mission codebases*, Software PA and Engineering workshop, Toulouse, France. (October 2019).

[2] Thomas Laroche, Pierre Denis, Paul Parisis, Damian George, David Sanchez de la Llana, and Thanassis Tsiodras. *MicroPython Virtual Machine for On-Board Control Procedures*, DASIA. (2018).

[3] Thanassis Tsiodras. *An ESA-led toolchain using model-driven code generation to create correct-by-construction SW for safety-critical targets*, MeTRiD. (April 2018).

[4] Marcel Verhoef, Victor Bandur, Maxime Perrotin, Thanassis Tsiodras, and Peter Gorm Larsen. *Towards integration of Overture into TASTE*, 14th Overture Workshop. (2016).

[5] G.Mamais, T.Tsiodras, D.Lesens, and M.Perrotin. *An ASN.1 compiler for embedded/space systems*, Embedded Real Time Software and Systems, Toulouse, France. (February 2012).

[6] M.Perrotin, E.Conquet, J.Delange, and T.Tsiodras. *An open-source tool-chain for embedded system and software development* , ERTS, Toulouse, France. (February 2012).

[7] M.Perrotin, E.Conquet, J.Delange, T.Tsiodras, and A.Schiele. *TASTE: A Real-Time Software Engineering Tool-Chain*, SDL Forum, Toulouse, France. (July 2011).

[8] M.Perrotin, E.Conquet, P.Dissaux, T.Tsiodras, and J.Hugues. *The TASTE Toolset: turning human designed heterogeneous systems into computer built homogeneous software*, ERTS, Toulouse, France. (May 2010).

[9] Jean-Paul Blanquart, Gerard Bulsa, David Lesens, George Mamais, Maxime Perrotin, and Thanassis Tsiodras. *Data modelling technologies for dependable system-software engineering*, European Space Agency,(Special Publication) ESA SP 669. (2009).

[10] J.Hugues, M.Perrotin, and T.Tsiodras. *Using MDE for the Rapid Prototyping of Space Critical Systems*, The 19th IEEE/IFIP International Symposium on Rapid System Prototyping, Monterey, California, USA. (June 2008).

[11] M.Bordin, T.Tsiodras, and M.Perrotin. *Experience in the Integration of Heterogeneous Models in the Model-driven Engineering of High-Integrity Systems*, Proceedings of the 13th Ada-Europe international conference on Reliable Software Technologies, Venice, Italy. (June 2008).

[12] M.Markaki, T.Tsiodras, G.Mamais, and I.S.Venieris. *Performance Comparison of Alternative VCR Methods for Video-on-Demand*, The 7th IEEE Workshop on Computer-Aided Modeling, Analysis and Design of Communication Links and Networks, Sao Paolo, Brazil. (August 1998).

[13] S.L.Tombros, G.L.Lyberopoulos, G.Tselikis, T.Tsiodras, and ... *Fixed Access to UMTS Service Capabilities*, ACTS Mobile Communication Summit '97, Aalborg, Denmark. (Oct. 1997).

[14] N.E.Igoumenidis, G.L.Lyberopoulos, S.L.Tombros, T.Tsiodras, and ... *Towards Personal Mobile Multimedia Mobility in Broadband Networks Supporting Fixed and Wireless Access*, ACTS Mobile Communication Summit 98, Rhodes, Greece. (June 1998).

[15] N.Doulamis, N.Tsapatsoulis, A.Doulamis, T.Tsiodras, and S.Kollias. *Markovian models for the output of real world MPEG-1 encoders*, Proc. of the 2nd Erlangen Symposium 'Advances in Digital Communications'. (April 1997).

[16] N.Doulamis, T.Tsiodras, A.Doulamis, and S.Kollias. *Low Bit Rate Coding of Image Sequences Using Regions of Interest and Neural Networks*, Proc. of 3rd Intern.Workshop on Image and Signal Processing, Manchester, England. (November 1996).