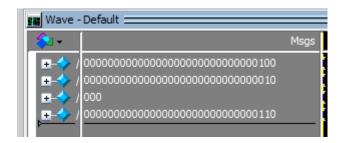
COMPUTER ORGANIZATION TUBA TOPRAK-16144116 HOMEWORK 3 REPORT

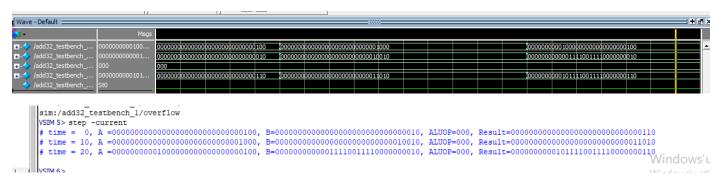
PART2:

All operations works except mult. Testbench written for add,nor,xor,or,sub,and,slt
The results are visible on the monitor and the wave.

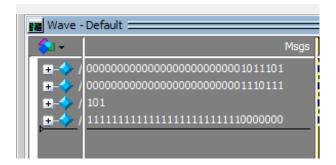
Add:



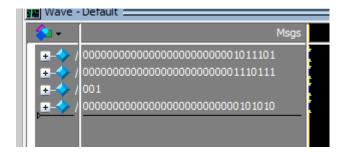
experiment with other numbers



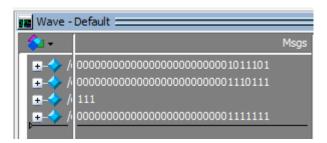
Nor:

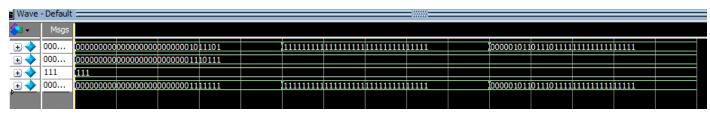


Xor:

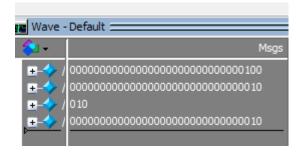


Or:



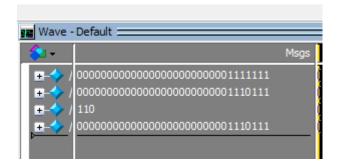


Sub:



Msgs														
o	000000000000000000000000000000000000000	0000000000	0100		000000000	0000000000	0000000000	1000		000000000	0000000000	0000001000	0000	
o	000000000000000000000000000000000000000	0000000000	0010											
0	010													
o	000000000000000000000000000000000000000			000000000000000000000000000000000000000					000000000000000000000000000000000000000					

And:



Slt: Tried 3 times for slt and monitor also showed.

```
■ ◆ A
                                               Pack... Internal
000000000000000000000000000000000110
                                               Pack... Internal
                                               Pack... Internal
                                               Net Internal
                                                                                                                      生 M ×
Transcript
🛊 ** Warning: (vsim-3015) C:/altera/13.1/hw3_tuba/sub32.v(48): [PCDPC] - Port size (32 or 32) does not match connection size (1) for port 'inl'. Th 🛕
 port definition is at: C:/altera/13.1/hw3 tuba/or 32.v(1)
        Region: /slt32 testbench 1/f3/subs/orl
  ** Warning: (vsim-3015) C:/altera/13.1/hw3_tuba/sub32.v(48): [PCDPC] - Port size (32 or 32) does not match connection size (1) for port 'in2'. Th
  port definition is at: C:/altera/13.1/hw3_tuba/or_32.v(1).
        Region: /slt32 testbench 1/f3/subs/orl
add wave -position insertpoint \
sim:/slt32_testbench_1/A \
sim:/slt32_testbench_1/B \
sim:/slt32_testbench_1/ALUOP \
sim:/slt32_testbench_1/Result
VSIM 5> step -current
# time = 0, A =00000000000000000000000000011, B=000000000000000000000000011, ALUOP=100, Result=0
Windows'u Etkinleştir
VSIM 6>
                                                                                                  Windows'u etkinleştirmek için
```

All opcode have been tried.

Wawe: cont.



Part2: I tried to draw fsm.

