

First Come First Served (Nonpreemptive)

Rand Robin

<u>Process</u>	<u>Next Burst Time</u>	<u>Process</u>	<u>Next Burst Time</u>
P <sub>1</sub>	10	P <sub>1</sub>	10 7 4
P <sub>2</sub>	2	P <sub>2</sub>	2
P <sub>3</sub>	5	P <sub>3</sub>	5 2 0

Time quantum = 3

for every schedule Gantt chart, AV(WT), Number of context switch.

P <sub>1</sub>	P <sub>2</sub>	P <sub>3</sub>
10	12	17

P <sub>1</sub>	P <sub>2</sub>	P <sub>3</sub>	P <sub>1</sub>	P <sub>3</sub>	P <sub>1</sub>	P <sub>1</sub>
3	5	8	11	13	16	17

$$WT(P_1) = 0$$

$$\frac{0+10+12}{3} = \frac{22}{3}$$

$$WT(P_1) = 13 - 6 = 7$$

$$WT(P_2) = 10$$

$$WT(P_2) = 3$$

$$WT(P_3) = 12$$

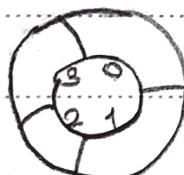
$$WT(P_3) = 11 - 3 = 8$$

Number of context switch = 2

Number of context switch = 6

P = Producer  $\rightarrow$  It produces an item and it inserts into a buffer

C = Consumer  $\rightarrow$  It extracts an item from a buffer



(Circular) Buffer

Buffer size = 4

in = 0 Producer()

while(true)

// produce an item

If the buffer is not full:

Buffer[in] = new Item

in = in + 1 mod (buffer.size())

counter = counter + 1

End

end

Counter = It accounts the number of item stored into the buffer.

Buffer is empty  $\rightarrow$  Counter = 0

```

out = 0 consumer()
while(true)
    if(counter > 0)
        item = buffer[out]
        out = out + 1 mod (buffersize)
        counter = counter - 1
    end
end

```

### Simulation (Nonpreemptive)



in      out      counter

P    1                  1

P    2                  2

P    3                  3

P    0                  4

P                        wait

C                        1      3

C                        2      2

P    1                  3

P    2                  4

P                        wait

Producer

$$\equiv \text{counter}++ = \begin{cases} \text{reg} = \text{counter} \\ \text{reg} = \text{reg} + 1 \\ \text{counter} = \text{reg} \end{cases}$$

Consumer

$$\equiv \text{counter--} = \begin{cases} \text{reg} = \text{counter} \\ \text{reg} = \text{reg} - 1 \\ \text{counter} = \text{reg} \end{cases}$$

Critical Section = The part of the program changing using shared variable.

Semaphore  $S=1 \equiv$  green light  $\equiv$  C.S. free

$S=0 \equiv$  red light  $\equiv$  C.S. busy

Semaphore with Passive Waiting

$S.\text{value} = 1$

$S.L$  // queue associated to S

Semaphore with Active Waiting

$\text{int } s // S=1 \equiv$  C.S. free

$S=0 \equiv$  C.S. busy

$\text{wait}(s) \{$

$\text{while } (s <= 0) \{$

        // wait do nothing

$s--$

}

$\text{signal}(s) \}$

$S++$

Producer

$$\equiv \text{counter}++ = \begin{cases} \text{reg} = \text{counter} \\ \text{reg} = \text{reg} + 1 \\ \text{counter} = \text{reg} \end{cases}$$

Consumer

$$\equiv \text{counter--} = \begin{cases} \text{reg} = \text{counter} \\ \text{reg} = \text{reg} - 1 \\ \text{counter} = \text{reg} \end{cases}$$

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$S--$

$\}$

$\text{signal}(s) \}$

$S++$

$s_1 = 1$     $s_2 = 1$   
 $t_0: \text{wait}(s_1)$     $t_1: \text{wait}(s_2)$   
 $t_2: \text{wait}(s_2)$

$t_3: \text{wait}(s_1)$

Date

No.

Process  $P_i$

Process  $P_j$

$H_p: P_i$  and  $P_j$  need 2 resources

$R_1$

Example:  $S_1$  = the semaphore associated to a file

$R_2$

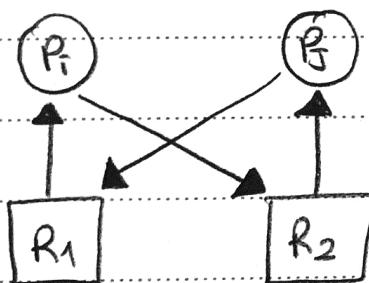
$S_2$  = the semaphore associated to a printer.

$t_0: P_i \text{ wait}(s_1) = P_i$  get access to  $R_1$     $s_1 = 0$

$t_1: P_j \text{ wait}(s_2) = P_j$  get access to  $R_2$     $s_2 = 0$

$t_2: P_i \text{ wait}(s_2) = P_i$  active waiting because  $s_2 = 0$

$t_3: P_j \text{ wait}(s_1) = P_j$  active waiting because  $s_1 = 0$



Circular Wait

$P_i \rightarrow P_j \rightarrow P_i \rightarrow P_j \rightarrow P_i$

~~$R = \{R_1, R_2, R_3, R_4, \dots\}$~~

$P = \{P_1, P_2, P_3\}$

~~$E = \{R_1 \rightarrow P_1, R_2 \rightarrow P_2, P_1 \rightarrow R_1, R_1 \rightarrow P_2, P_2 \rightarrow R_3, R_3 \rightarrow P_3\}$~~

Deadlock? No. the system is not in deadlock.

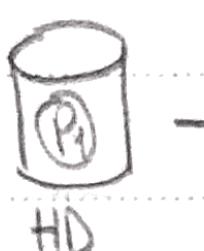
Exit sequence:  $P_3$  uses  $R_3$  and release it

$R_3$  available

$P_2$  can get  $R_3$ . It has all resources, it can run, use them and return them

$R_1$  available

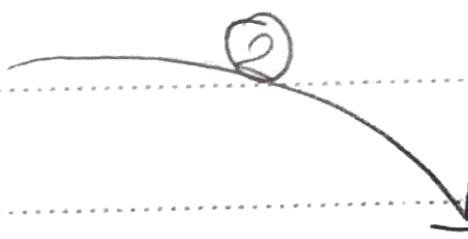
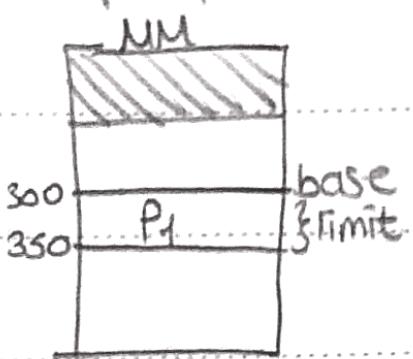
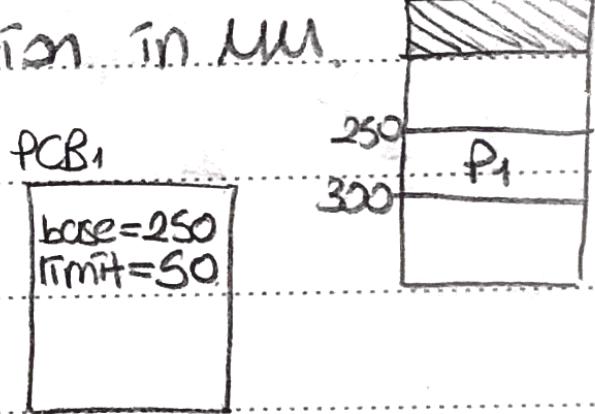
$P_1$  can get  $R_1$ . It has both  $R_1$  and  $R_2$ . It can run, use the R and return them.

$t_0 = P_1$  is in New State $t_1 = P_1$  is in the Ready State

→ ① →

PCB<sub>1</sub>

id=1
base=300
limit=50
MH req=50K

 $t_2 = P_1$  is in the Running State $t_4 = P_1$  waiting $t_5 = \text{Middle Term Scheduler}$ swap out  $P_1$  $t_6 =$  The P that was into the CS left (signal(S)) and moves  $P_1$  into the Ready Suspended State $t_8 =$  The middle term scheduler swap in  $P_1$ The new state of  $P_1$  is "Ready" $P_1$  has a new partition in MM

MM	OS
256.000	320K
hole 1	364.040
P1	3120.900
hole 2	3459.060
P2	3164

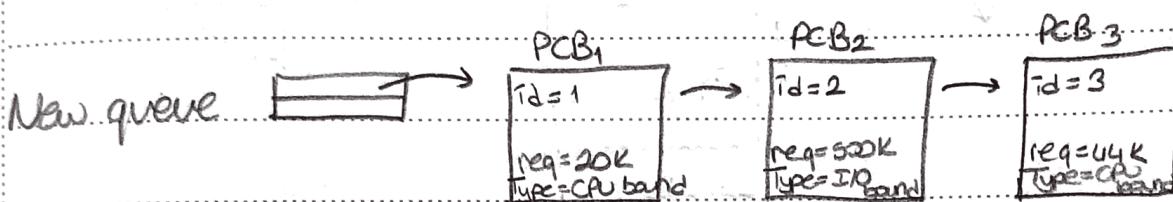
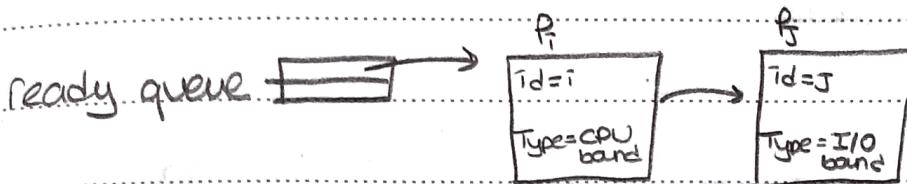
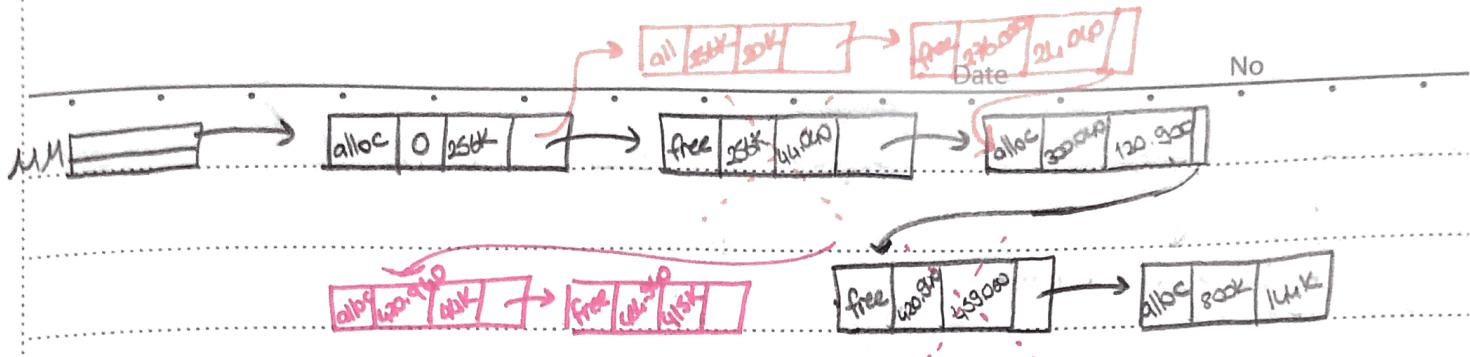
state field = free/allocated

relocation = it stores the base odd no. of part

wide = To store the wide of the part

next field = pointer to the next element of the list.





→ save time if we avoid to split the hole into 2

Long Term Scheduler = Best fit Algorithm → The long term scheduler assigns the nearby hole

★ by assigning the biggest hole.  
The splitting the partition results in the biggest left over.

Worst fit Algorithm

First fit Algorithm

→ The long term scheduler assigns the biggest hole

→ The long term scheduler assigns the 1st hole to  $P_1$

→  $96\text{K}$  → digerken birin listeye batmak zornda

Apply best fit alg. to allocate  $P_3$

↳ Best fit alg. assigns hole1 to  $P_3$ , little difference



↳ No division of the partition

The long term scheduler must look at the complete list.

Use the worst fit alg. to allocate  $P_3$

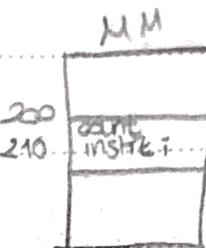
$P_2 \rightarrow$  There is not a hole big enough →  $P_3$  cannot be moved

Prob → External fragmentation

into the ready state

count = 1
10 instructions
max

long term scheduler



logical space of  $P_i = [0, \text{max}]$

4K

base or relocation = 200

0	page 0
1	
2	
3	page 3

logical address  
of  $P_i$

0	
1	
2	page 0
3	
4	page 3
5	page 2
6	
7	page 1
8	

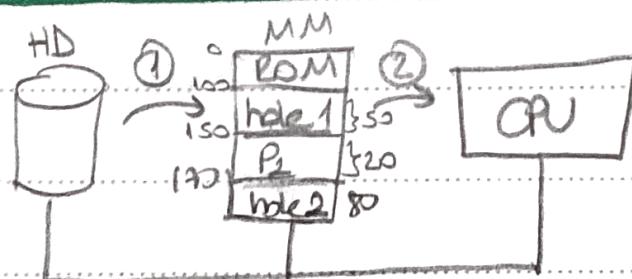
Page	Page
0	2
1	7
2	5
3	4

logical address = page number  
+ offset

size(page) = size(frame)

MM = {frame}

physical address = frame number  
+ offset



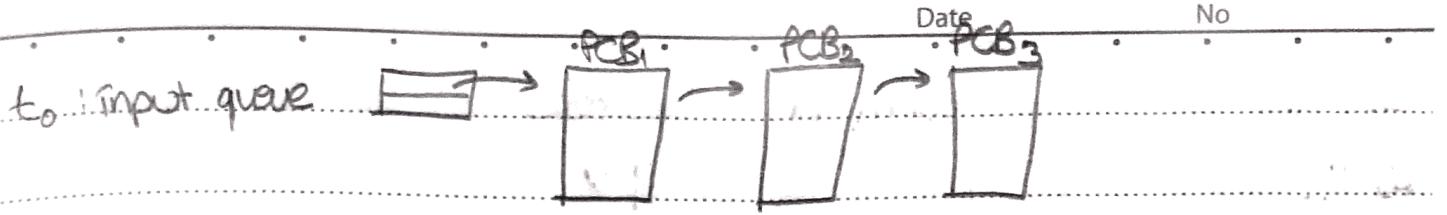
TEKEND

① Long Term Scheduler

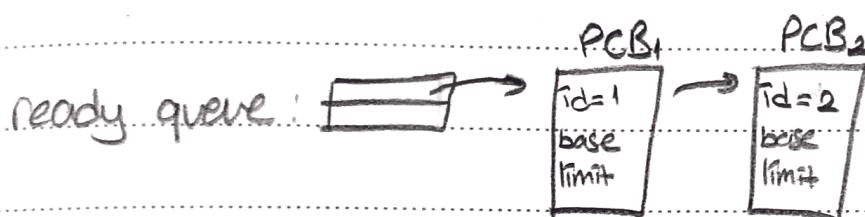
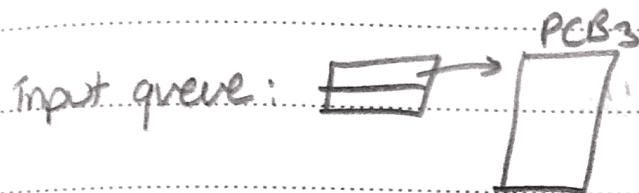
② Short Term Scheduler

Middle Term Scheduler = swap in / swap out

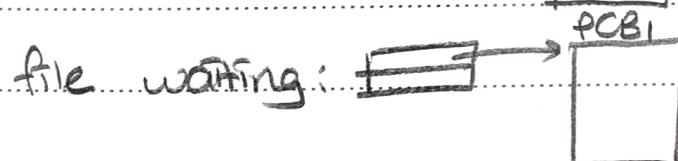
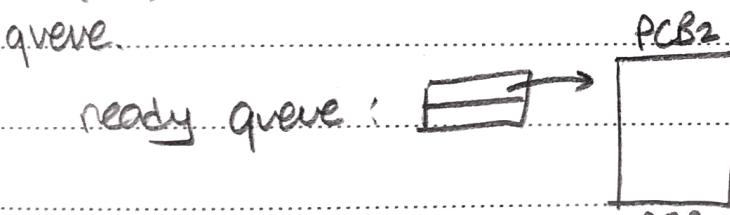
The aim of middle and long term scheduler is to have in MM a good mixture of CPU band / I/O band



t<sub>1</sub>: Assume that the LTS assigns a partition to P<sub>1</sub>, P<sub>2</sub>. Show the input queue and ready queue.

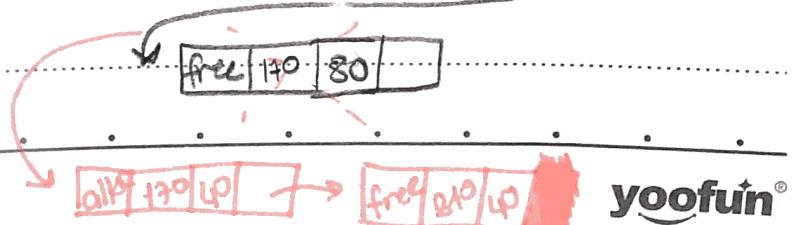


t<sub>2</sub>: The STS moves P<sub>1</sub> into the running state and P<sub>1</sub> asks for a resource and moves into the waiting state. Show the interesting queue.



t<sub>3</sub>: The MTS swap out P<sub>1</sub>. Which is the new state of P<sub>1</sub>?  
waiting suspended

Show the list describing MM



t<sub>6</sub>: The file p<sub>1</sub> was waiting for is available and the OS assigned to p<sub>1</sub>. Which is the new state of p<sub>1</sub>?

Ready Suspended

Simulate worst fit algorithm to assign a new partition to p<sub>1</sub>.

p<sub>1</sub> is assigned to hole 2.

Show the new list describing MU.