

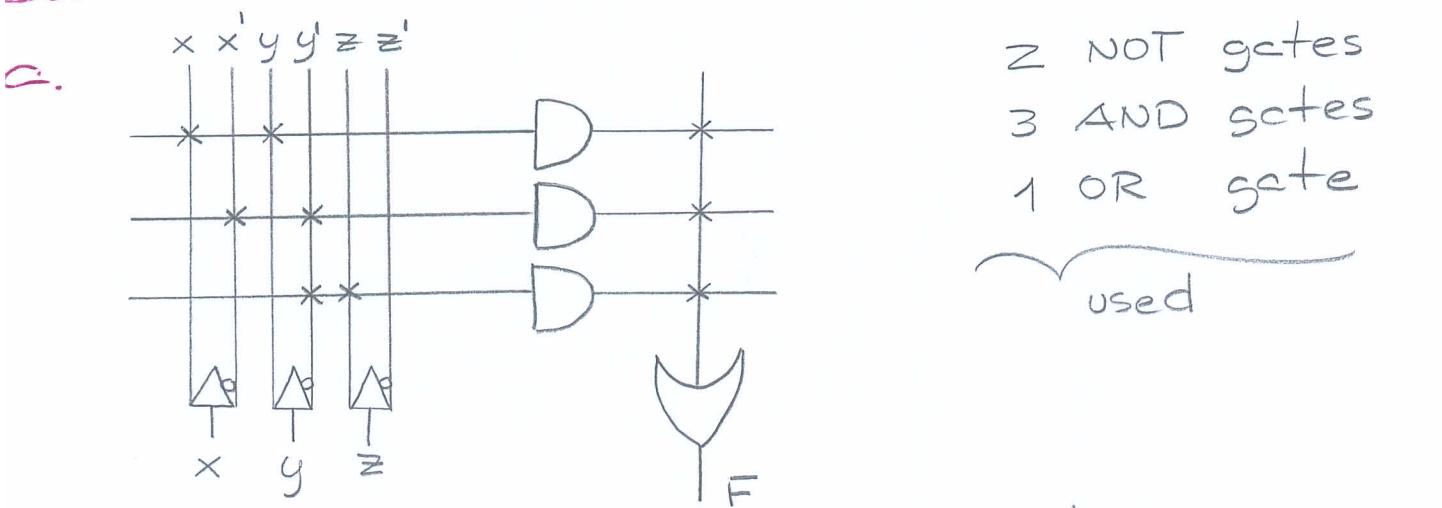
Selected Problems III

Problem 1) Implement the Boolean function

$$F = xy + x'y' + y'z$$

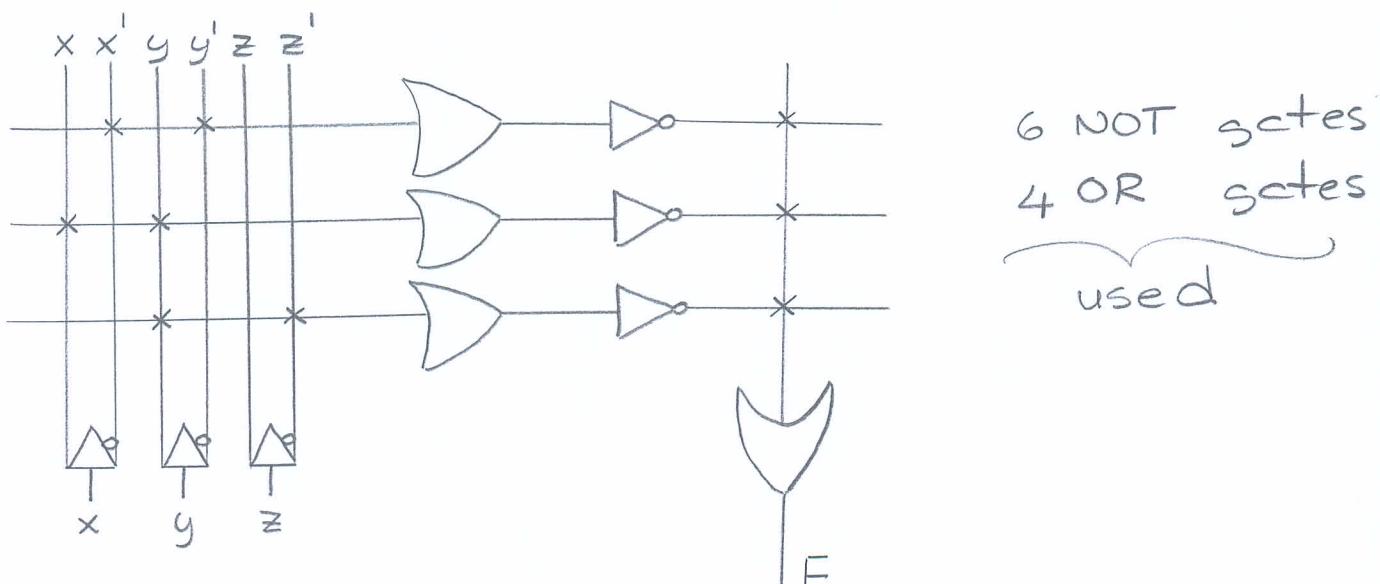
- 2. with AND, OR and NOT gates,
- 2. with OR and NOT gates,
- c. with AND and NOT gates,
- d. with NAND and NOT gates,
- e. with NOR and NOT gates.

Solution.



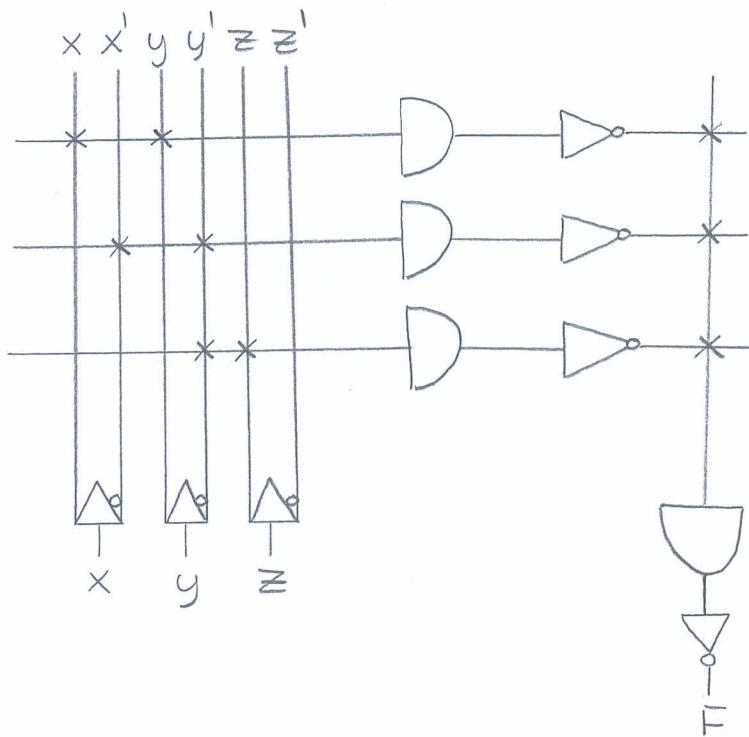
b. We shall rearrange F as follows:

$$F = (x' + y')' + (x + y)' + (y + z')'$$



c. We shall rearrange F as follows:

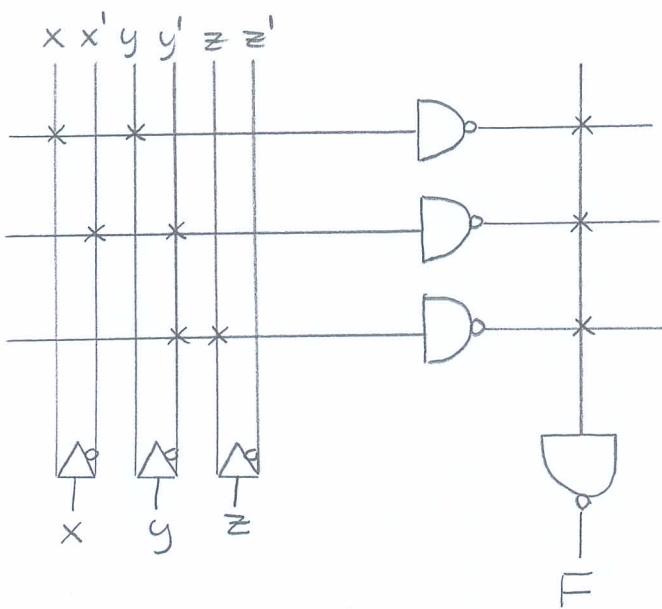
$$F = [(xy)' \cdot (x'y')' \cdot (y'z')']'$$



6 NOT gates
4 AND gates
used

d. We have

$$F = [(xy)' \cdot (x'y')' \cdot (y'z')']'$$



2 NOT gates
4 NAND gates
used

e. We shall convert \vdash into product of sums form as

$$F = (\overbrace{xy+x'}^{\vdash}) (\overbrace{xy+y'}^{\vdash}) + y'z$$

$$= (\overbrace{x+x'}^{\vdash}) (\overbrace{y+x'}^{\vdash}) (\overbrace{x+y'}^{\vdash}) (\overbrace{y+y'}^{\vdash}) + y'z$$

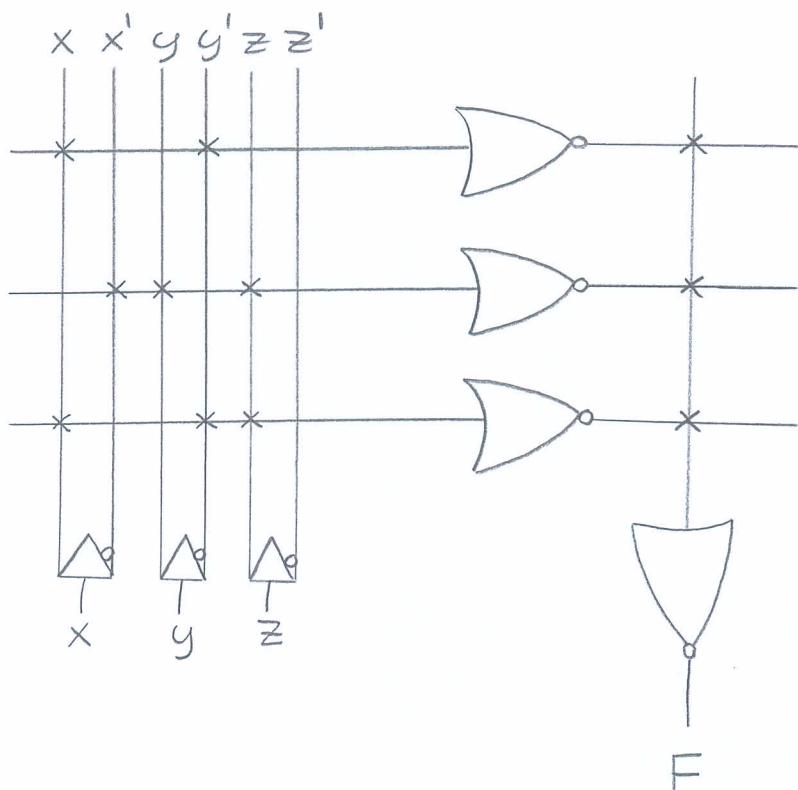
$$= (\overbrace{x'+y}^{\vdash}) (\overbrace{x+y'}^{\vdash}) + y'z$$

$$= [(\overbrace{x'+y}^{\vdash}) (\overbrace{x+y'}^{\vdash}) + y'] [(\overbrace{x'+y}^{\vdash}) (\overbrace{x+y'}^{\vdash}) + z]$$

$$= (\overbrace{x'+y+y'}^{\vdash}) (x+y'+y') (x'+y+z) (x+y'+z)$$

$$= (x+y') (x'+y+z) (x+y'+z)$$

$$= [(x+y')' + (x'+y+z)' + (x+y'+z)']'$$



2 NOT gates
4 NOR gates
used

Problem 2) Show that a positive logic NAND gate is a negative logic NOR gate and vice versa.

Solution. We consider a gate with

x	y	z
L	L	H
L	H	H
H	L	H
H	H	L

Positive logic : $L \equiv 0, H \equiv 1$

x	y	z
0	0	1
0	1	1
1	0	1
1	1	0

\Rightarrow a positive logic NAND gate

Negative logic : $L \equiv 1, H \equiv 0$

x	y	z
1	1	0
1	0	0
0	1	0
0	0	1

\Rightarrow a negative logic NOR gate

Moreover;

-to justify vice versa situation, we consider

x	y	z
L	L	H
L	H	L
H	L	L
H	H	L

x	y	z
0	0	1
0	1	0
1	0	0
1	1	0

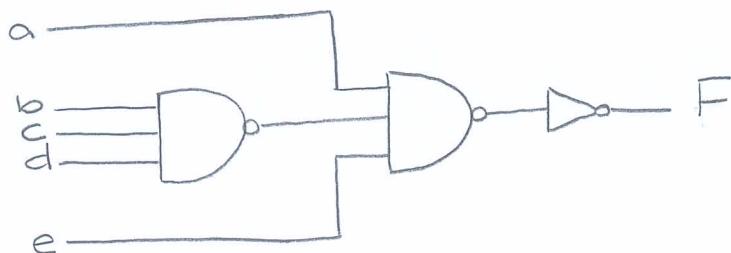
\Rightarrow a positive logic NOR gate

x	y	z
1	1	0
1	0	1
0	1	1
0	0	1

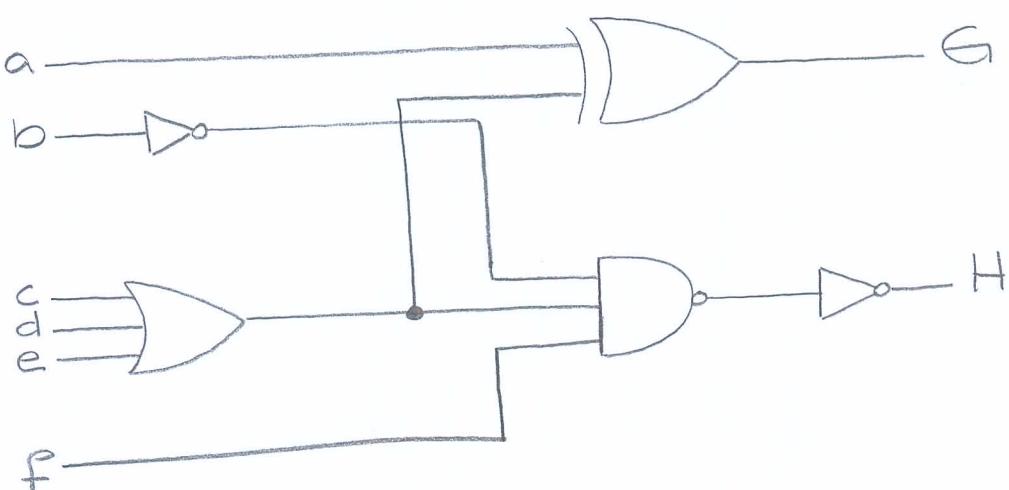
\Rightarrow a negative logic NAND gate

Problem 3) Write Boolean expressions and construct the truth tables describing the outputs of the circuits represented by the following logic diagrams:

a.



b.



Solution.

a. $F = \left((a \cdot (b \cdot c \cdot d)' \cdot e)' \right)'$

$$= a \cdot (b' + c' + d')e$$
$$= ab'e + ac'e + ad'e$$
$$= \sum m (17, 19, 21, 23, 25, 27, 29)$$

a	b	c	d	e	F
0	0	0	0	0	0
1	0	0	0	1	1
1	0	0	1	0	0
1	0	0	1	1	1
1	0	1	0	0	0
1	0	1	0	1	1
1	0	1	1	0	0
1	0	1	1	1	1
1	1	0	0	0	0
1	1	0	0	1	1
1	1	0	1	0	0
1	1	0	1	1	1
1	1	1	0	0	0
1	1	1	0	1	1
1	1	1	1	0	0
1	1	1	1	1	0

b.

$$G = a \oplus (c+d+e)$$

$$= a'(c+d+e) + a(c+d+e)'$$

$$= a'c + a'd + a'e + a c' d' e'$$

$$= \sum m \left(\underbrace{8-31}_{a'c}, \underbrace{4-31}_{a'd}, \underbrace{2-31}_{a'e}, \underbrace{32, 33, 48, 49}_{a c' d' e'} \right)$$

$$= \sum m (2-33, 48, 49)$$

a	b	c	d	e	f	G
0	0	0	0	0	0	0
0	0	0	0	0	1	0
0	0	0	0	1	0	1
						.
						.
1	0	0	0	0	1	1
1	0	0	0	1	0	0
						.
						0
1	0	1	1	1	1	0
1	1	0	0	0	0	1
1	1	0	0	0	1	0
						.
						0
1	1	1	1	1	1	0

$$H = \left((b' \cdot (c+d+e) \cdot f)' \right)'$$

$$= b' \cdot (c+d+e) \cdot f$$

$$= b'cf + b'df + b'ef$$

$$= \sum m (9, 11, 13, 15, 41, 43, 45, 47, \\ b'cf$$

$$5, 7, 13, 15, 37, 39, 45, 47, \\ b'df$$

$$3, 7, 11, 15, 35, 39, 43, 47) \\ b'ef$$

$$= \sum m (3, 5, 7, 9, 11, 13, 15, 35, 37, 39, 41, 43, \\ 45, 47)$$

a	b	c	d	e	f	H
0	0	0	0	0	0	0
0	0	0	0	1	1	1
0	0	0	1	0	0	0
0	0	0	1	0	1	1
0	0	0	1	1	0	0
0	0	0	1	1	1	1
0	0	0	1	1	0	0
0	0	1	0	0	0	0
0	0	1	0	0	1	1
0	0	1	0	1	0	0
0	0	1	0	1	1	1
0	0	1	1	0	0	0
0	0	1	1	0	1	1
0	0	1	1	1	0	0
0	0	1	1	1	1	1
0	0	1	1	1	1	1
0	0	1	1	1	1	1
0	0	1	1	1	1	1
1	0	0	0	1	1	1
1	0	0	1	0	0	0
1	0	0	1	0	1	1
1	0	0	1	1	0	0
1	0	0	1	1	1	1
1	0	0	1	1	1	0
1	0	1	0	0	0	0
1	0	1	0	0	1	1
1	0	1	0	1	0	0
1	0	1	0	1	1	1
1	0	1	1	0	0	0
1	0	1	1	0	1	1
1	0	1	1	1	0	0
1	0	1	1	1	1	1

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Problem 4) Use the tabulation method to simplify the following Boolean function:

$$F(x,y,z) = \sum m(0,1,3,4,5)$$

Solution.

LIST #1

m_i	x	y	z	
0	0	0	0	✓
1	0	0	1	✓
4	1	0	0	✓
3	0	1	1	✓
5	1	0	1	✓

LIST #2

Product terms	x	y	z	
0,1	0	0	-	✓
0,4	-	0	0	✓
1,3	0	-	1	
1,5	-	0	1	✓
4,5	1	0	-	✓

$$PI2 \equiv x'z$$

LIST # 3

Product terms	x	y	z	
0,1,4,5	-	o	-]
0,4,1,5	-	o	-	PI 1 = y'

- No more list !

PI _i / m _i	0	1	3	4	5
PI 1*	x	x		x	x
PI 2*		x	x		

- Both PI1 and PI2 are essential prime implicants

Hence :

$$\begin{aligned}
 F(x, y, z) &= PI1 + PI2 \\
 &= y' + x'z
 \end{aligned}$$

Problem 5) Find all the prime implicants for the following Boolean function and determine which are essential:

$$F(A,B,C,D) = \sum m(0,2,3,5,7,8,10,11,14,15)$$

Solution.

LIST #1

m_i	A	B	C	D	
0	0	0	0	0	✓
2	0	0	1	0	✓
8	1	0	0	0	✓
3	0	0	1	1	✓
5	0	1	0	1	✓
10	1	0	1	0	✓
7	0	1	1	1	✓
11	1	0	1	1	✓
14	1	1	1	0	✓
15	1	1	1	1	✓

LIST #3

LIST #2

Product terms	A	B	C	D	
0,2	0	0	-	0	✓
0,8	-	0	0	0	✓
2,3	0	0	1	-	✓
2,10	-	0	1	0	✓
8,10	1	0	-	0	✓
3,7	0	-	1	1	✓
3,11	-	0	1	1	✓
5,7	0	1	-	1	PI5
10,11	1	0	1	-	✓
10,14	1	-	1	0	✓
7,15	-	1	1	1	✓
11,15	1	-	1	1	✓
14,15	1	1	1	-	✓

Product terms

A B C D

0,2,8,10	- 0 - 0]	PI1 $\equiv B'D'$
0,8,2,10	- 0 - 0]	
2,3,10,11	- 0 1 -]	PI2 $\equiv B'C$
2,10,3,11	- 0 1 -]	
3,7,11,15	- - 1 1]	PI3 $\equiv CD$
3,11,7,15	- - 1 1]	
10,11,14,15	1 - 1 -]	PI4 $\equiv AC$
10,14,11,15	1 - 1 -]	

- No more list !

PI_i/m_i	0v	2v	3	5v	7v	8v	10v	11v	14v	15v
PI 1*	X	X				X	X			
PI 2			X	X				X	X	
PI 3				X				X		X
PI 4*							X	X	X	X
PI 5*					X	X				

↑ ↑ ↑

PI1, PI4, PI5 are all
essential PI's

Reduced PI chart

PI_i/m_i	3
PI 2	X
PI 3	X

} \Rightarrow we can choose either
PI 2 or PI 3 as
essential

Hence;

PI's : $B'D'$, $B'C$, CD , AC , $A'BD$

Essential PI's : $B'D$, AC , $A'BD$, $B'C$ (or CD)

Problem 6) Using the tabulation method, simplify the following Boolean function F together with the don't care conditions, d and then express the simplified function in sum-of-minterms form:

$$F(A, B, C, D) = \sum m(0, 6, 8, 13, 14)$$

$$d(A, B, C, D) = \sum m(2, 4, 10)$$

Draw a 2-level NAND gate logic circuit diagram for $F(A, B, C, D)$.

Solution.

LIST #1

m_i	A	B	C	D	
0	0	0	0	0	✓
2	0	0	1	0	✓
4	0	1	0	0	✓
8	1	0	0	0	✓
6	0	1	1	0	✓
10	1	0	1	0	✓
13	1	1	0	1	PI 4
14	1	1	1	0	✓

LIST #2

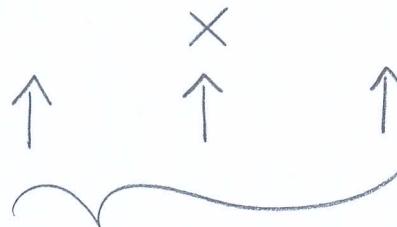
Product terms	A	B	C	D	
0, 2	0	0	-	0	✓
0, 4	0	-	0	0	✓
0, 8	-	0	0	0	✓
2, 6	0	-	1	0	✓
2, 10	-	0	1	0	✓
4, 6	0	1	-	0	✓
8, 10	1	0	-	0	✓
6, 14	-	1	1	0	✓
10, 14	1	-	1	0	✓

LIST # 3

Product terms	A	B	C	D	
0, 2, 4, 6	0	-	-	0]
0, 4, 2, 6	0	-	-	0]
0, 2, 8, 10	-	0	-	0]
0, 8, 2, 10	-	0	-	0	
2, 6, 10, 14	-	-	1	0]
2, 10, 6, 14	-	-	1	0]

- No more list !

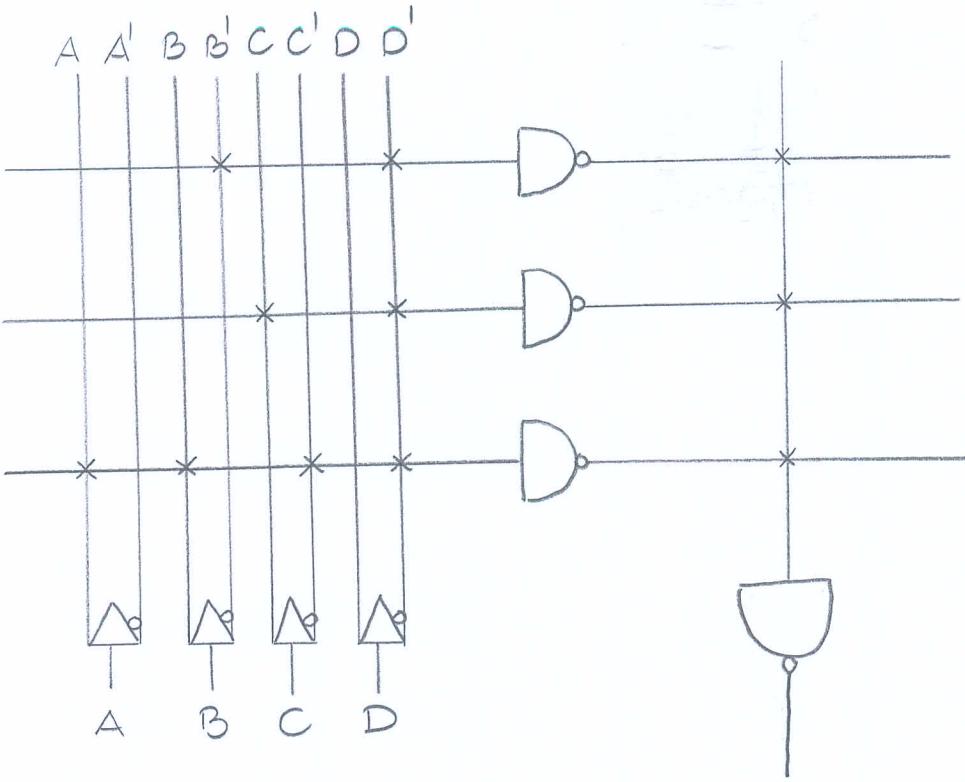
PI _i / m _i	0v	6v	8v	13v	14v
PI 1	x	x			
PI 2*	x		x		
PI 3*		x			x
PI 4*				x	



PI2, PI3, PI4 are
essential PI's

- Since all minterms are covered by the
essential PI's determined

$$\begin{aligned}
 F(A, B, C, D) &= \text{PI2} + \text{PI3} + \text{PI4} \\
 &= B'D' + CD' + ABC'D'
 \end{aligned}$$



2-level NAND gate logic circuit diagram