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David Money Harris Associate Professor of Engineering at Harvey Mudd College in Claremont, CA, holds a Ph.D. from Stanford University and S.B. and M.Eng. degrees from MIT. His research interests include CMOS VLSI design, microprocessors, and computer arithmetic. He holds a dozen patents, is the author of three other books in the field of digital design and three hiking guidebooks, and has ...

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CHAPTER 2 SOLUTIONS 9 2.5 The minimum size diffusion contact is 4 x 5 λ , or 1.2 x 1.5 μ m. The area is 1.8 μ m2 and perimeter is 5.4 μ m. Hence the total capacitance is At a drain voltage of VDD, the capacitance reduces to 2.6 Set the two parts of EQ (2.26) equal at Vds = Vdsat. Assume that EQ (2.27) is true and

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