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Paper : Objective Questions for 8085 Microprocessor. 1. In Synchronous data Transfer type both Transmitter and Receiver will operate in a) Same Clock pulse b) Different Clock pulse c) None of the above 2. The term PSW Program Status word refers a) Accumulator & Flag register b) H and L register c) Accumulator & Instruction register d) B and C register 3.

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1. In 8085 microprocessor, how many interrupts are maskable. a. Two b. Three c. Four d. Five 2. Which stack is used in 8085 microprocessors? a. FIFO b. FILO c. LIFO d. LILO 3. In an instruction of 8085 microprocessor, how many bytes are present? a. One or two b. One, two or three c. ... Read more8085 Microprocessor MCQs

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42. In 8085, the pins for SID and SOD are. A. 4 and 5 respectively B. 5 and 4 respectively C. 3 and 4 respectively D. 4 and 3 respectively. Answer: B. 43. Assertion (A): Each memory chip has its own address latch. Reason (R): ALE signal comes out of microprocessor 8085 and goes to memory chip. A. Both A and R are correct and R is correct explanation of A B.

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Microprocessor 8085 Interview Questions & Answers. The number 5 specifies that it uses +5V power supply. It has 40 pins and uses multiplexed address. It works like a controller and has long life. All computers use the microprocessor 8085. Even tabs, laptops and other gadgets use this. Good knowledge on microprocessors is required for this job.

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Microprocessors and Microcontrollers/ Multiple Choice Questions Architecture of Microprocessors d) During subtraction 19. The advantage of memory mapped I/O over I/O mapped I/O is, a) Faster b) Many instructions supporting memory mapped I/O c) Require a bigger address decoder d) All the above 20.

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