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EECE488: Analog CMOS Integrated Circuit Design Introduction and Background Shahriar Mirabbasi Department of Electrical and Computer Engineering University of British Columbia shahriar@ece.ubc.ca Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged. SM 2 EECE 488 – Set 1: Introduction and Background Marking

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Design of Analog CMOS Integrated Circuits Behzad Razavi Errata p. 6, Section 1.3, first sentence should read: The idea of metal-oxide-semiconductor ...

Design of Analog CMOS Integrated Circuits - Engineering

He joined the University of California, Berkeley in 1971 with the Department of Electrical Engineering and Computer Sciences. Gray's research interests include bipolar and MOS circuit design, electro thermal interactions in integrated circuits, device modeling, telecommunications circuits, and analog-digital interfaces on analog integrated circuits.

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