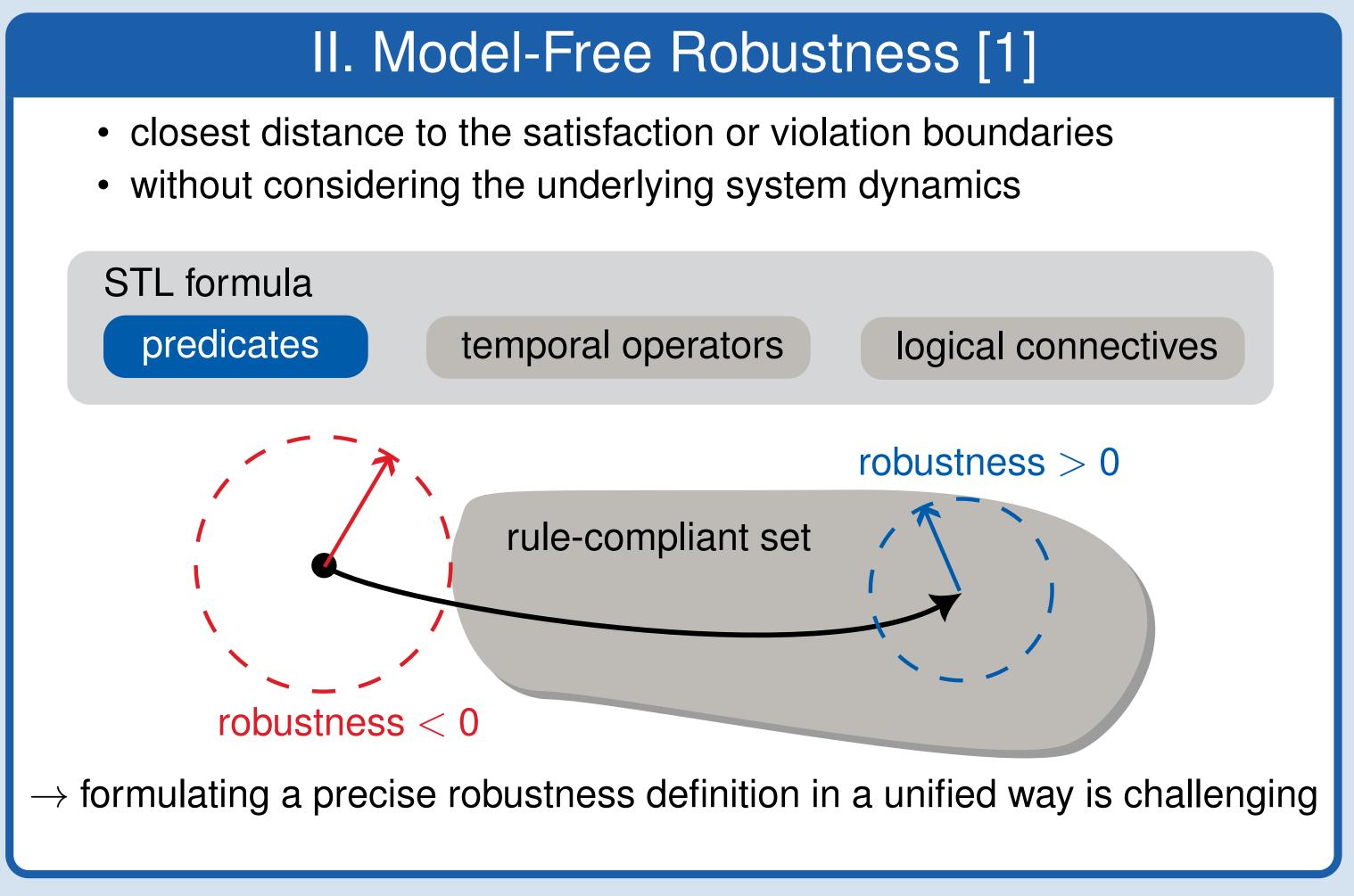
Model Predictive Robustness of Signal Temporal Logic Predicates

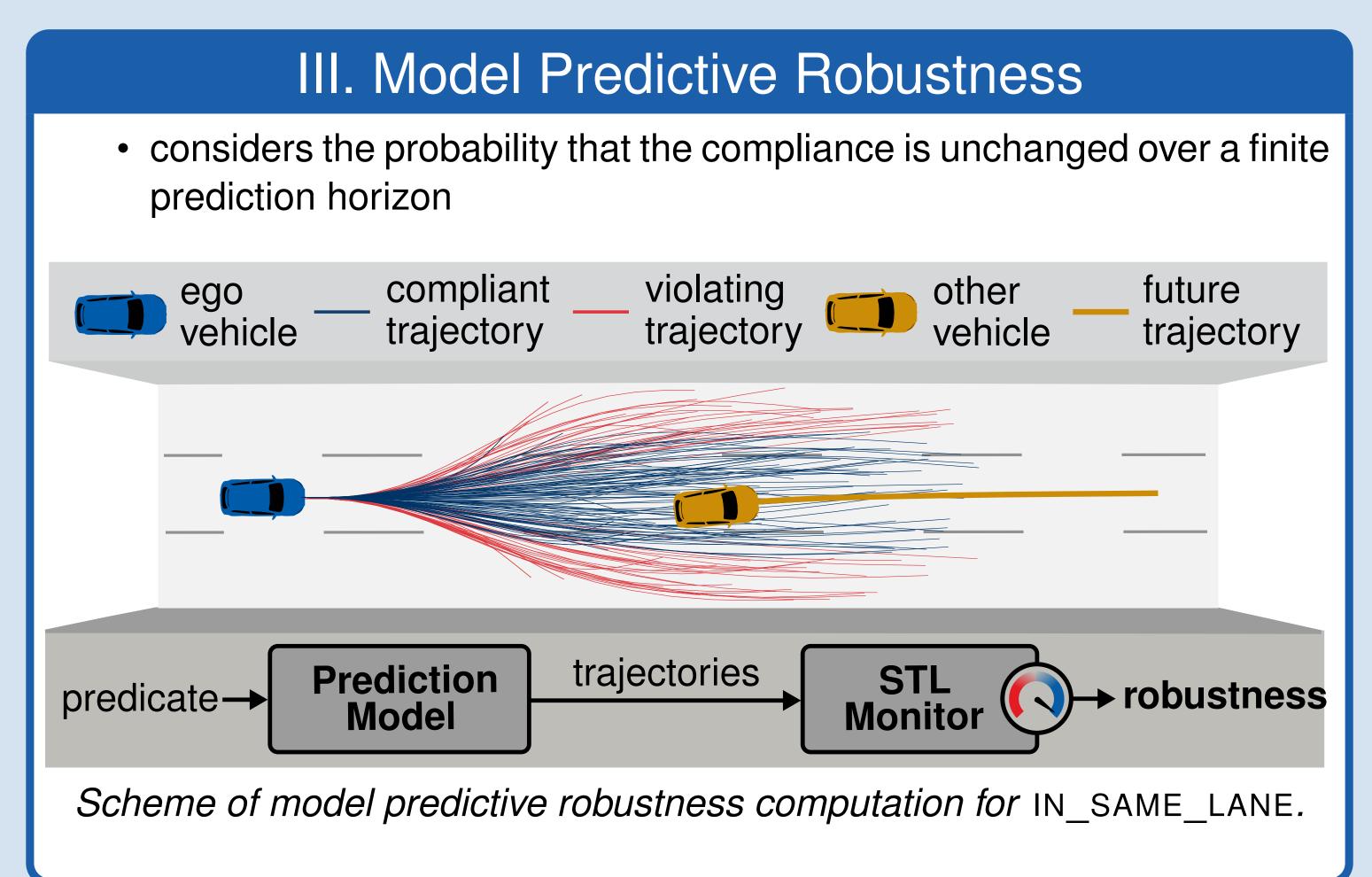
Yuanfei Lin*, Haoxuan Li*, and Matthias Althoff Cyber-Physical Systems Group, Technical University of Munich

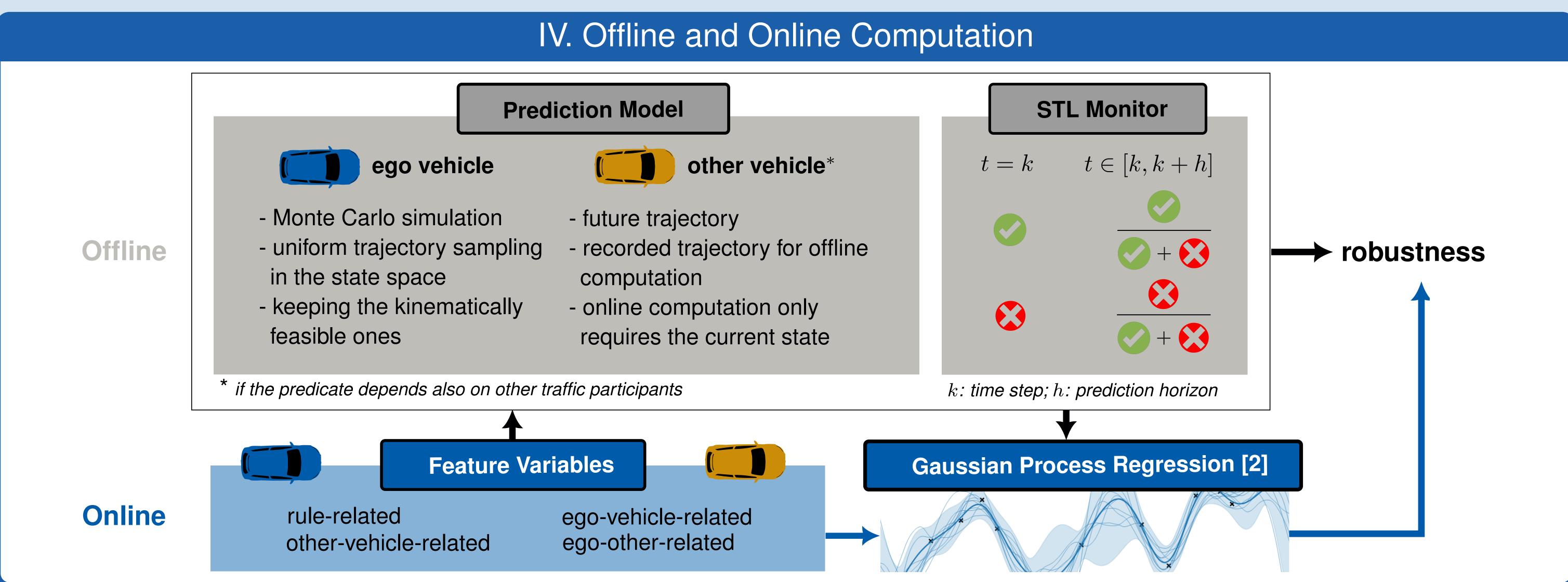
I. Robustness of Signal Temporal Logic

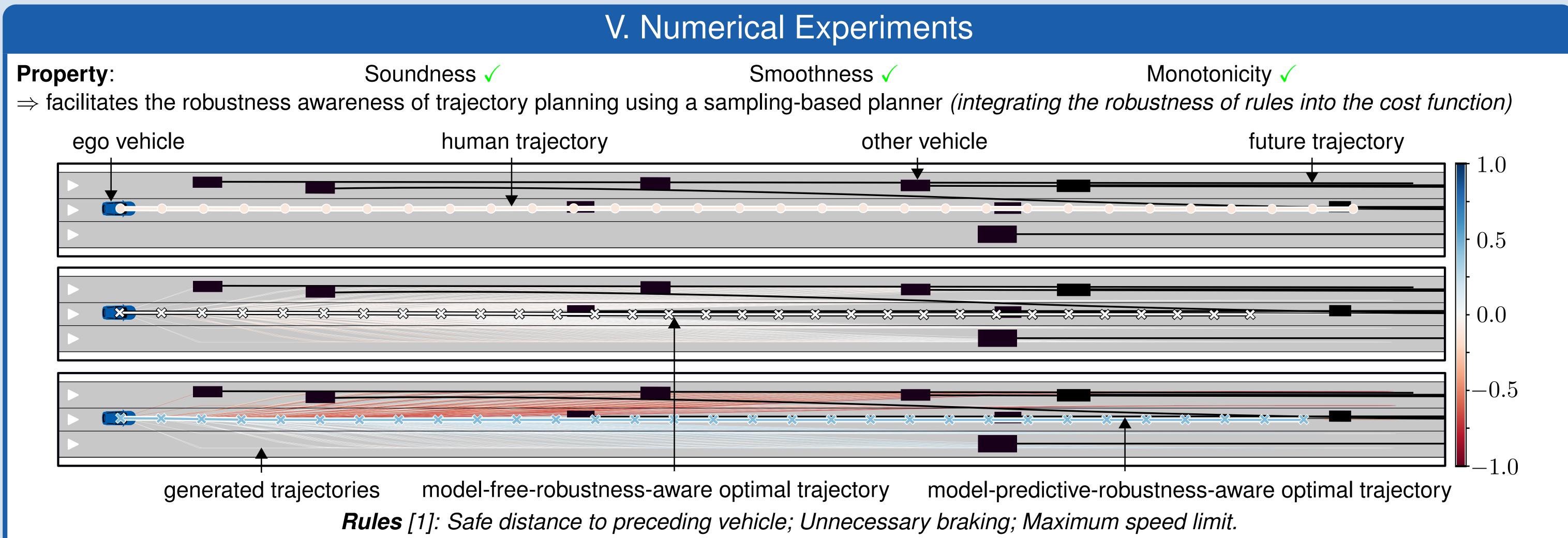
Signal temporal logic (STL) allows one to specify safety properties and unambiguous tasks for a system over time:

- Boolean Evaluation: determining whether a signal of the system meets a given temporal specification (> 0: satisfaction; < 0: violation)
- Robustness Degree: offering a continuous measure of how much a signal fulfills or violates the specification











• [1] L. Gressenbuch and M. Althoff, "Predictive monitoring of traffic rules," in Proc. IEEE Int. Conf. Intell. Transp. Syst., 2021, pp. 915–922.

• [2] C. E. Rasmussen and C. K. Williams, Gaussian Processes for Machine Learning. Cambridge, MA, USA: MIT Press, 2006.







