

A

A

B

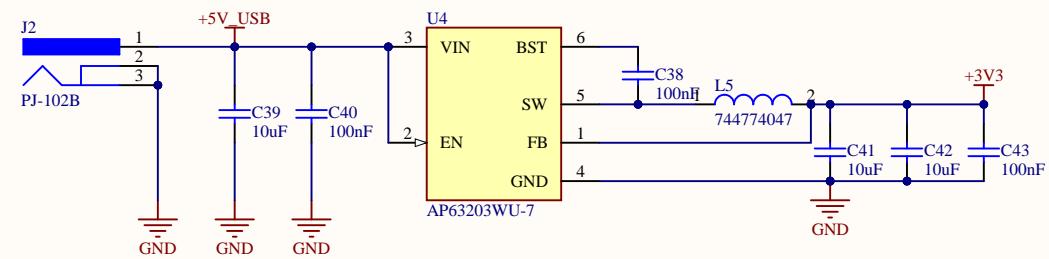
B

C

C

D

D

Title *Smart Thing*

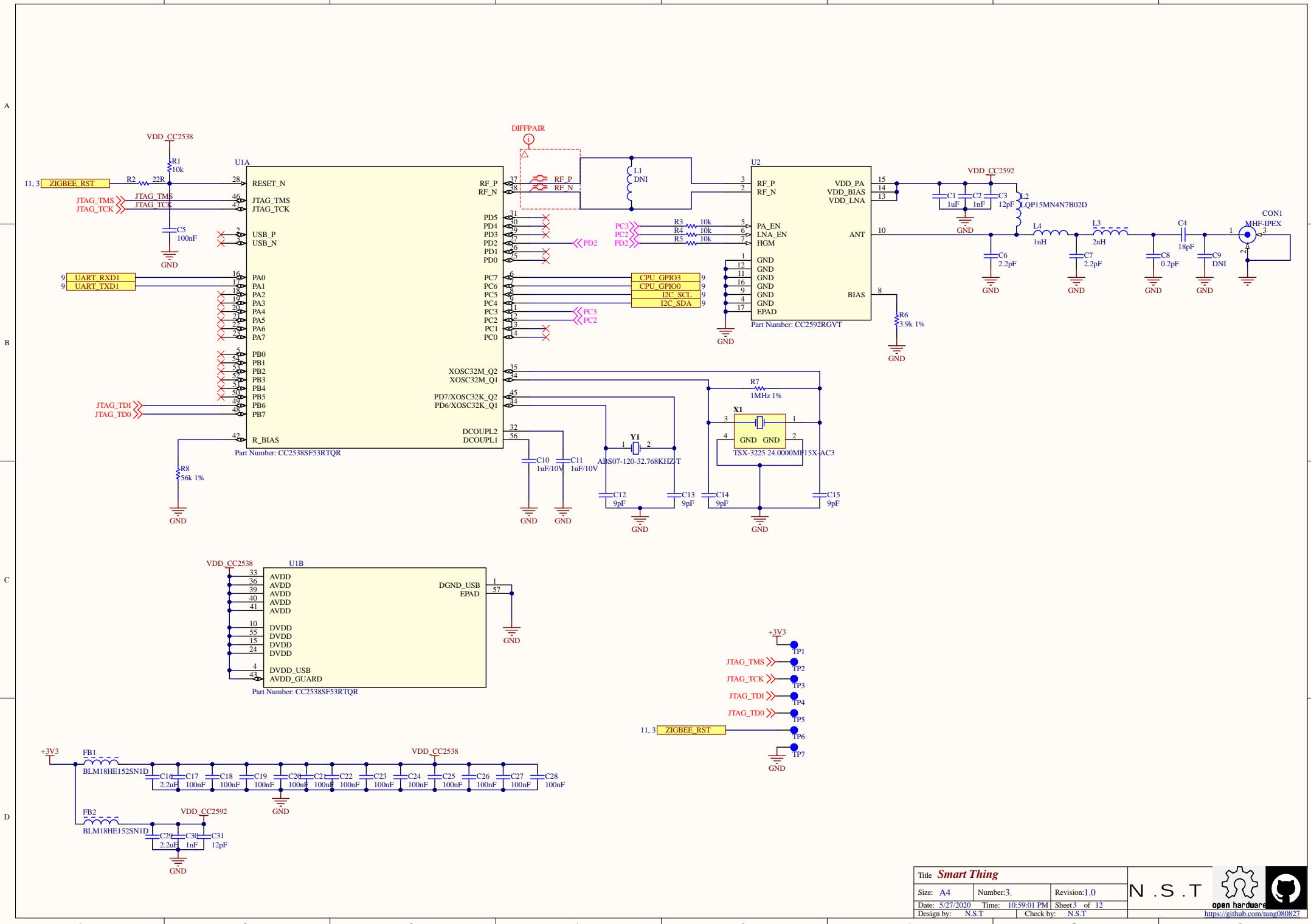
Size: A4 Number:2 Revision:1.0

Date: 5/27/2020 Time: 10:59:01 PM Sheet 2 of 12

Design by: N.S.T Check by: N.S.T

N . S . T

<https://github.com/tung080827>



A

A

B

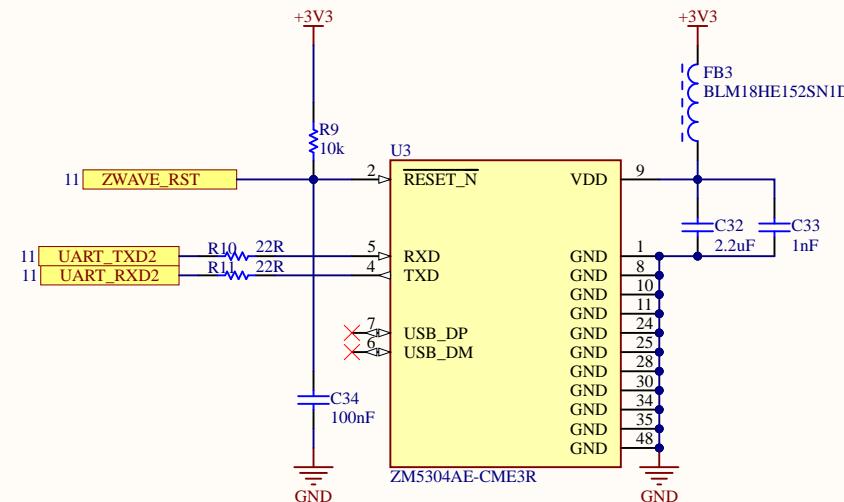
B

C

C

D

D

Title *Smart Thing*

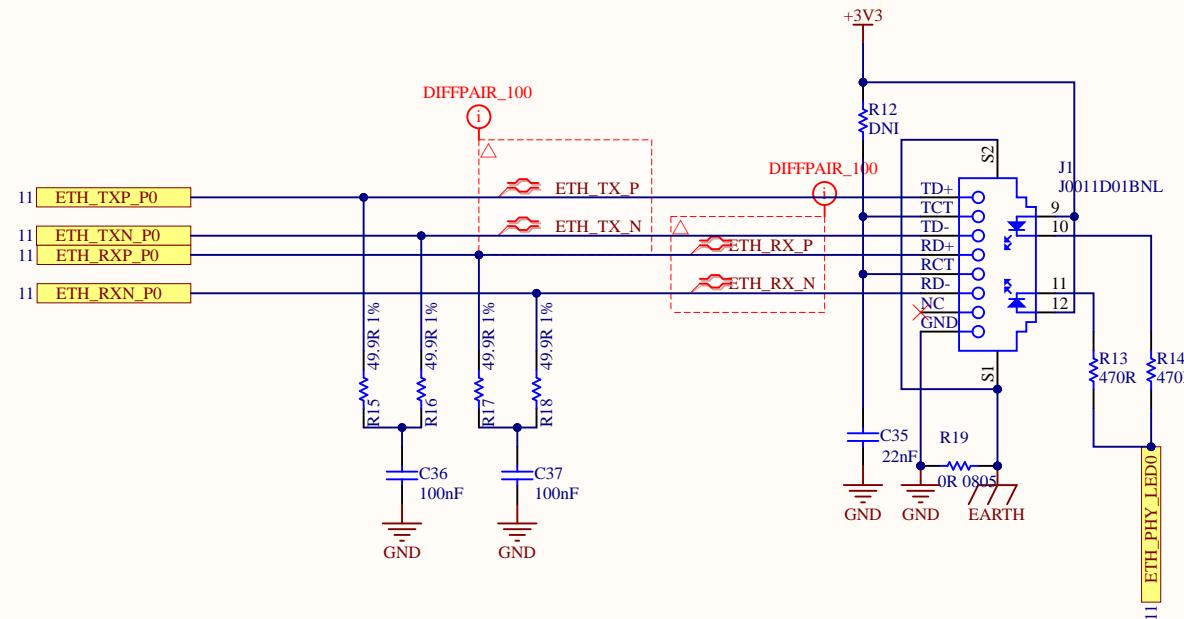
Size: A4 Number:4 Revision:1.0

Date: 5/27/2020 Time: 10:59:01 PM Sheet 4 of 12

Design by: N.S.T Check by: N.S.T

N . S . T

<https://github.com/tung080827>



Title *Smart Thing*

Size: A4 Number:5 Revision:1.0

Date: 5/27/2020 Time: 10:59:01 PM Sheet 5 of 12

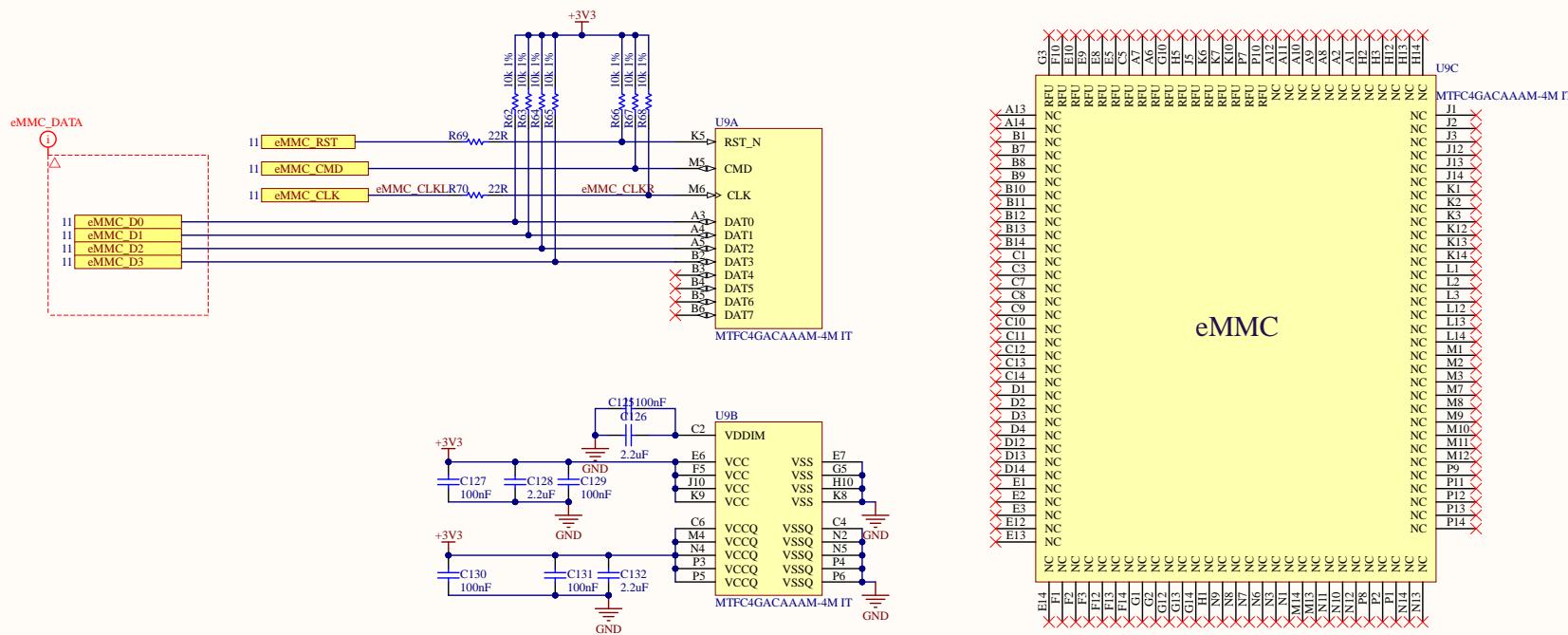
Design by: N.S.T Check by: N.S.T

N . S . T



A

A



F

E

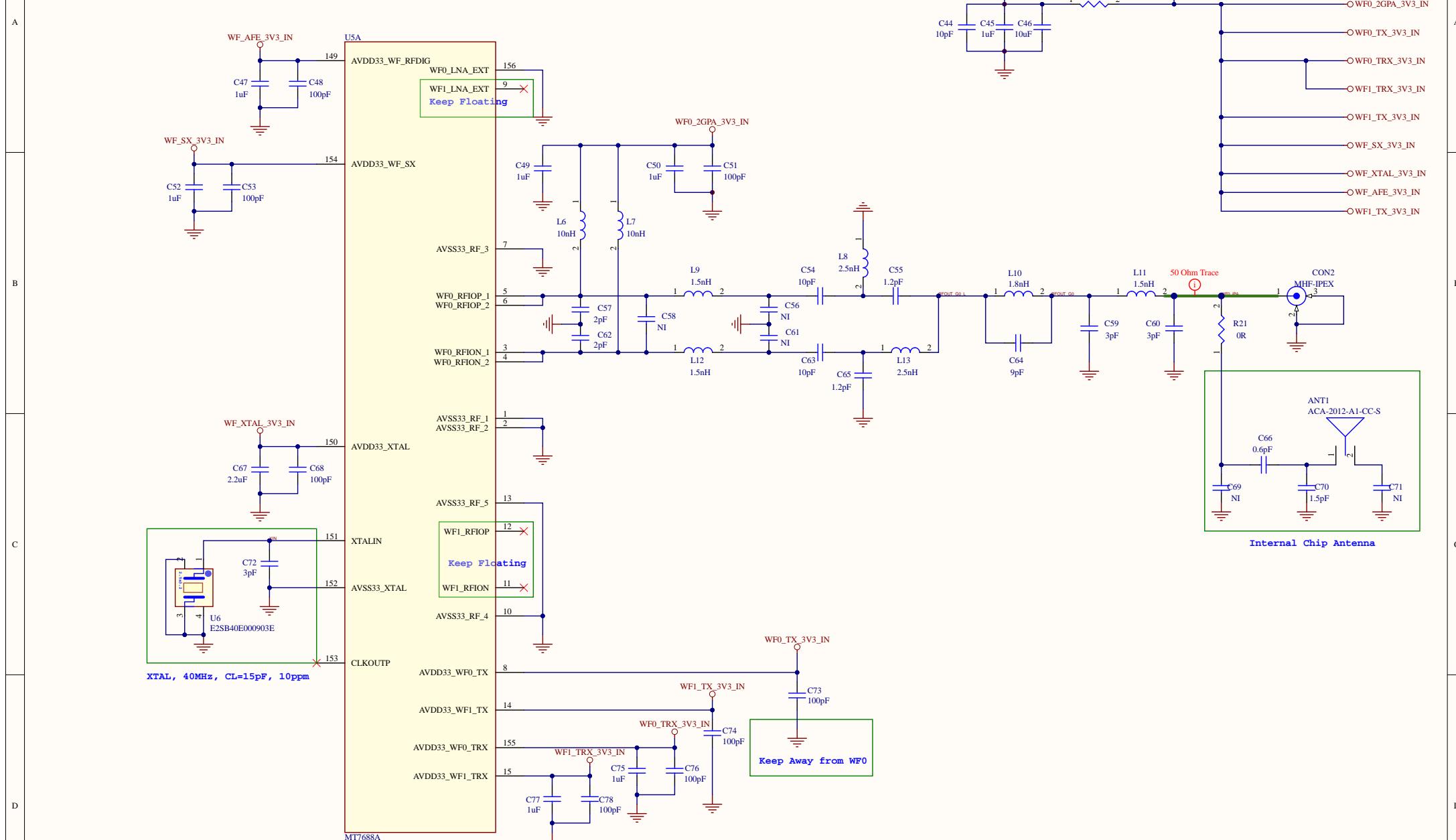
6

6

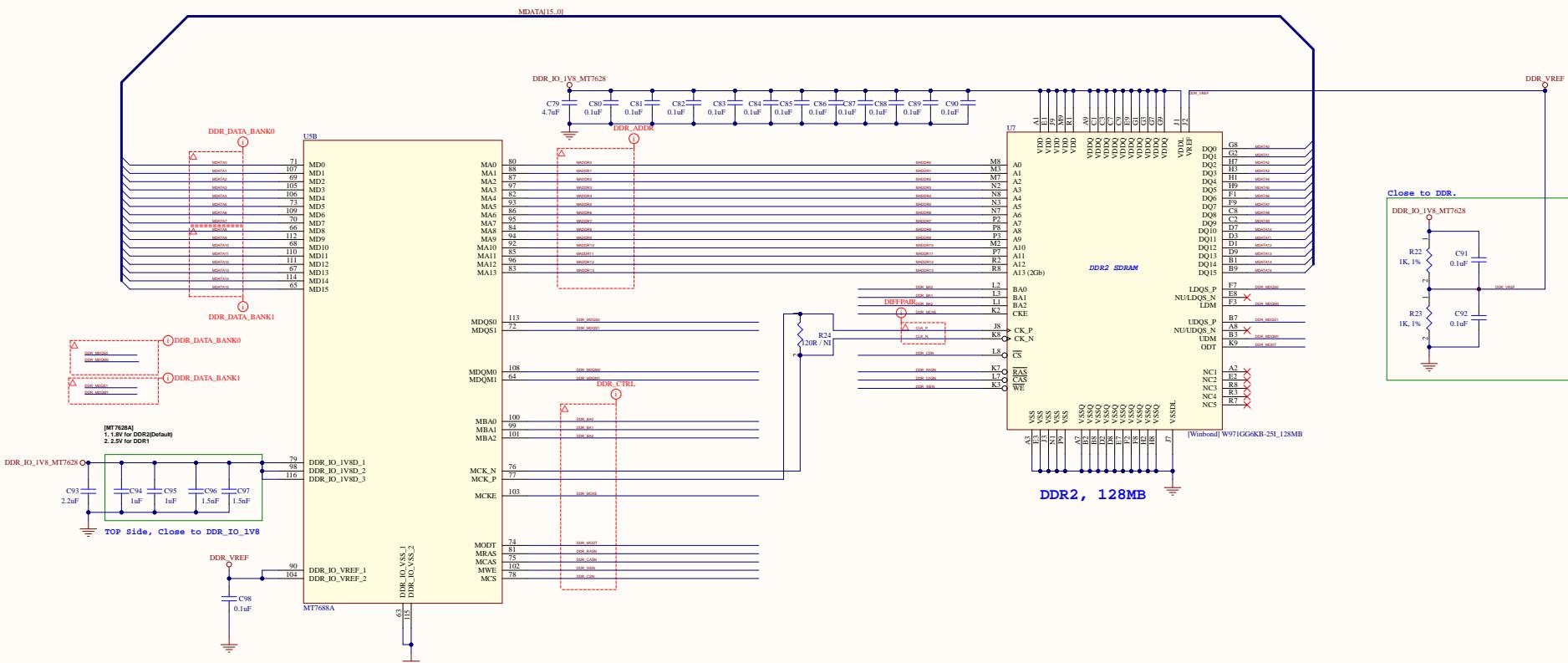
D

E

MT7688AN RF Front End

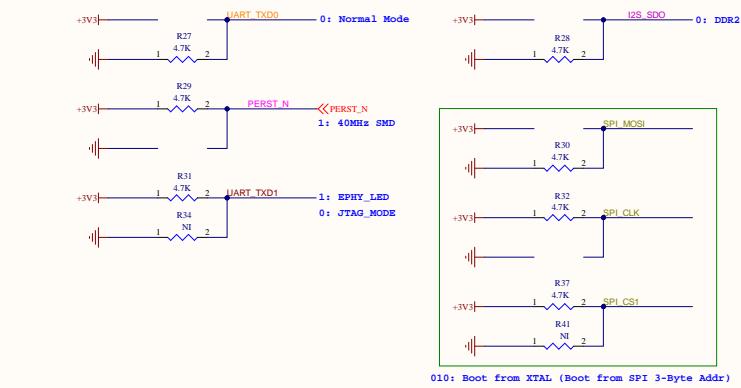
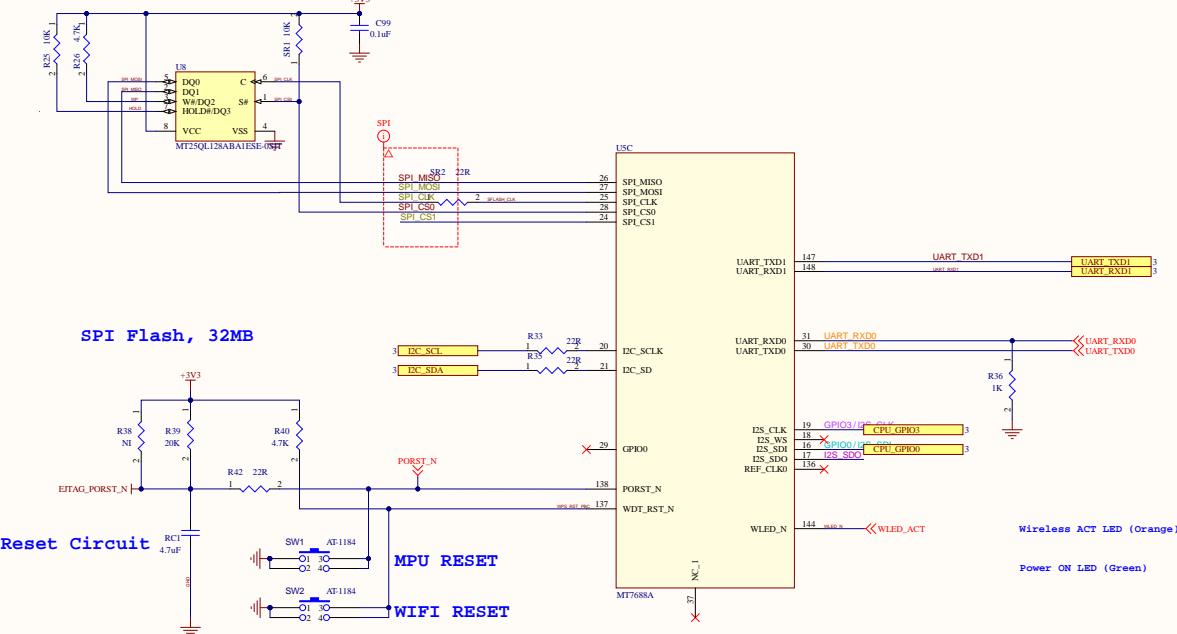


MT7688AN DDR2

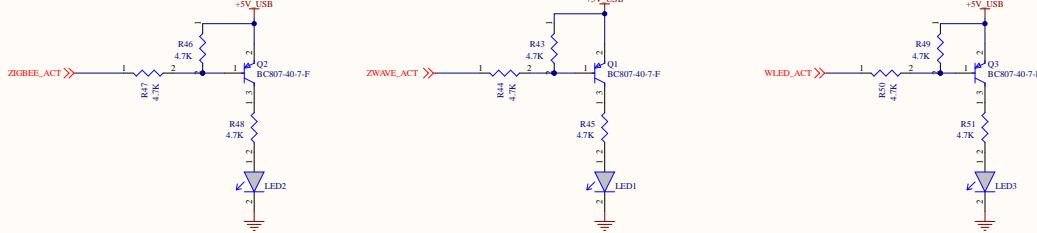


MT7688AN Peripheral / SPI Flash / Reset

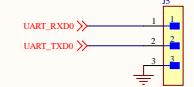
MT7688AN Config



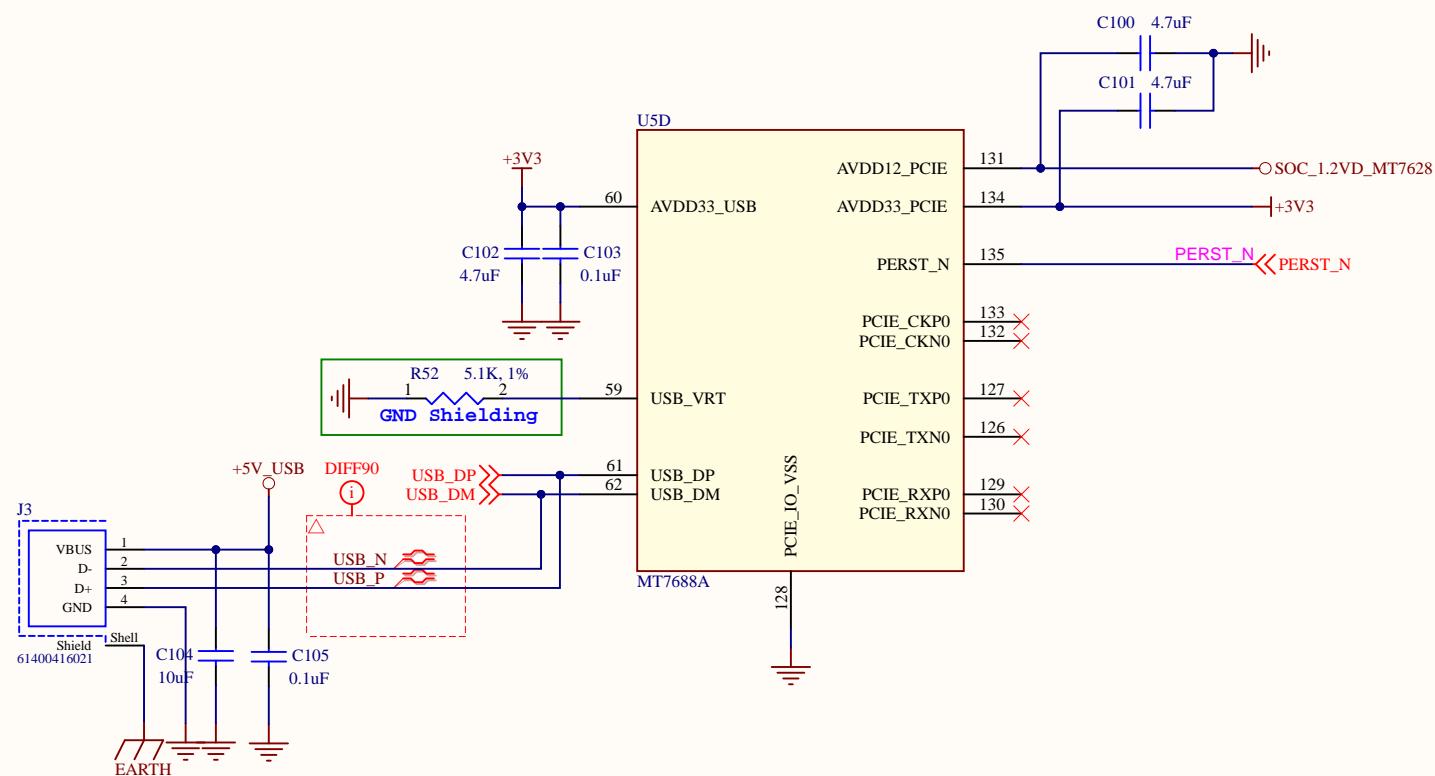
Zigbee / Z-Wave / Wifi Status



Communicate/ Remote



MT7688AN USB-PCIe



Title *Smart Thing*

Size: A4	Number: 10.	Revision: 1.0
Date: 5/27/2020	Time: 10:59:02 PM	Sheet 10 of 12
Design by: N.S.T	Check by: N.S.T	

N . S . T

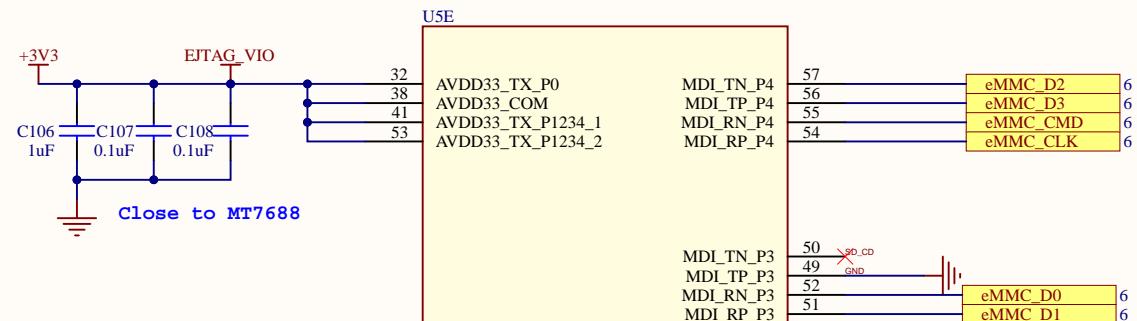


open hardware

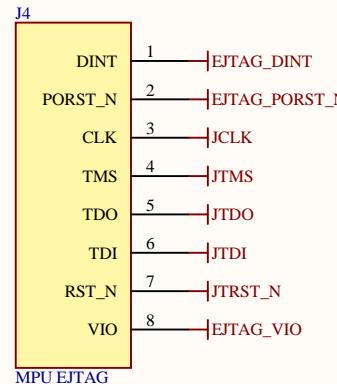
<https://github.com/tung080827>

MT7688AN Ethernet / MicroSD / JTAG

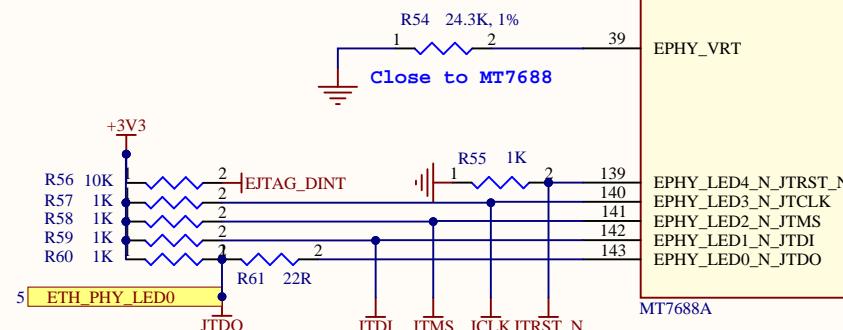
A



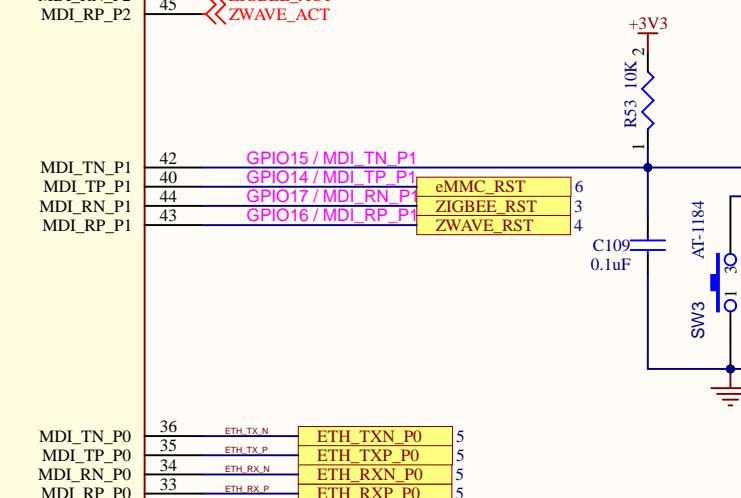
B



C



D

Title *Smart Thing*

Size: A4 Number:11 Revision:1.0

Date: 5/27/2020 Time: 10:59:02 PM Sheet 11 of 12

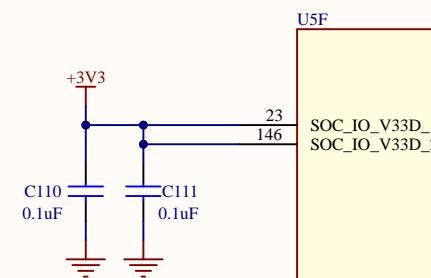
Design by: N.S.T Check by: N.S.T

N . S . T



MT7688AN Power

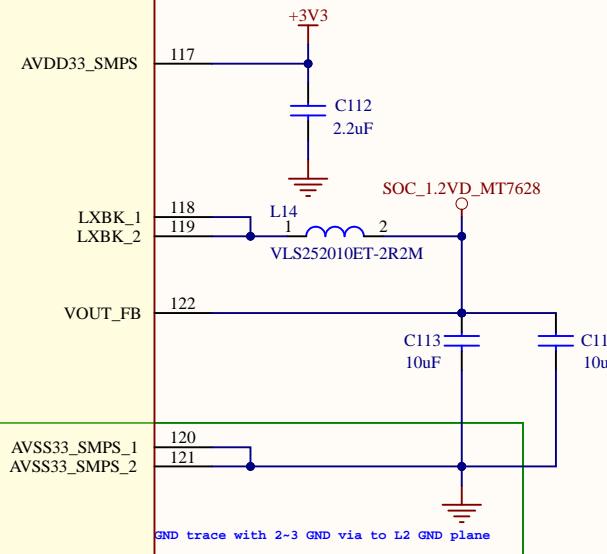
A



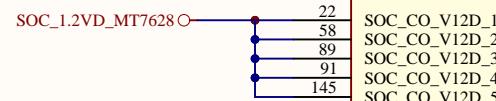
U5F

SOC_IO_V33D_1
SOC_IO_V33D_2

B



C

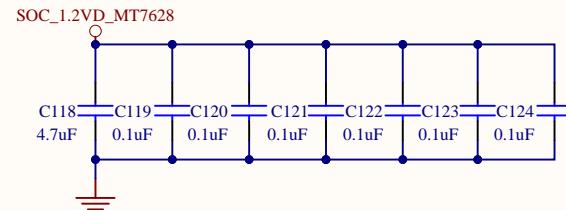


MT7688A

EPAD

157

D

Title *Smart Thing*

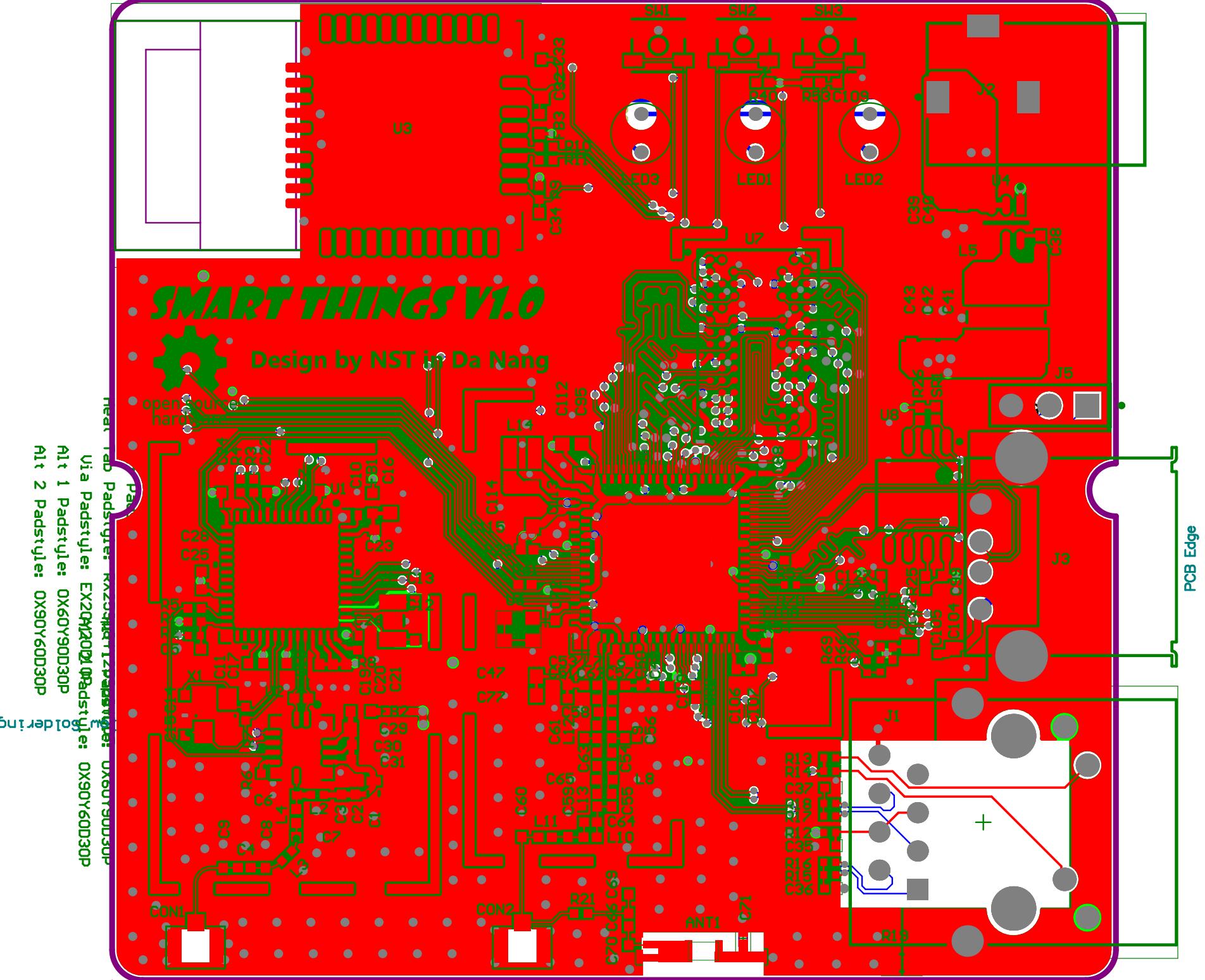
Size: A4 Number:12. Revision:1.0

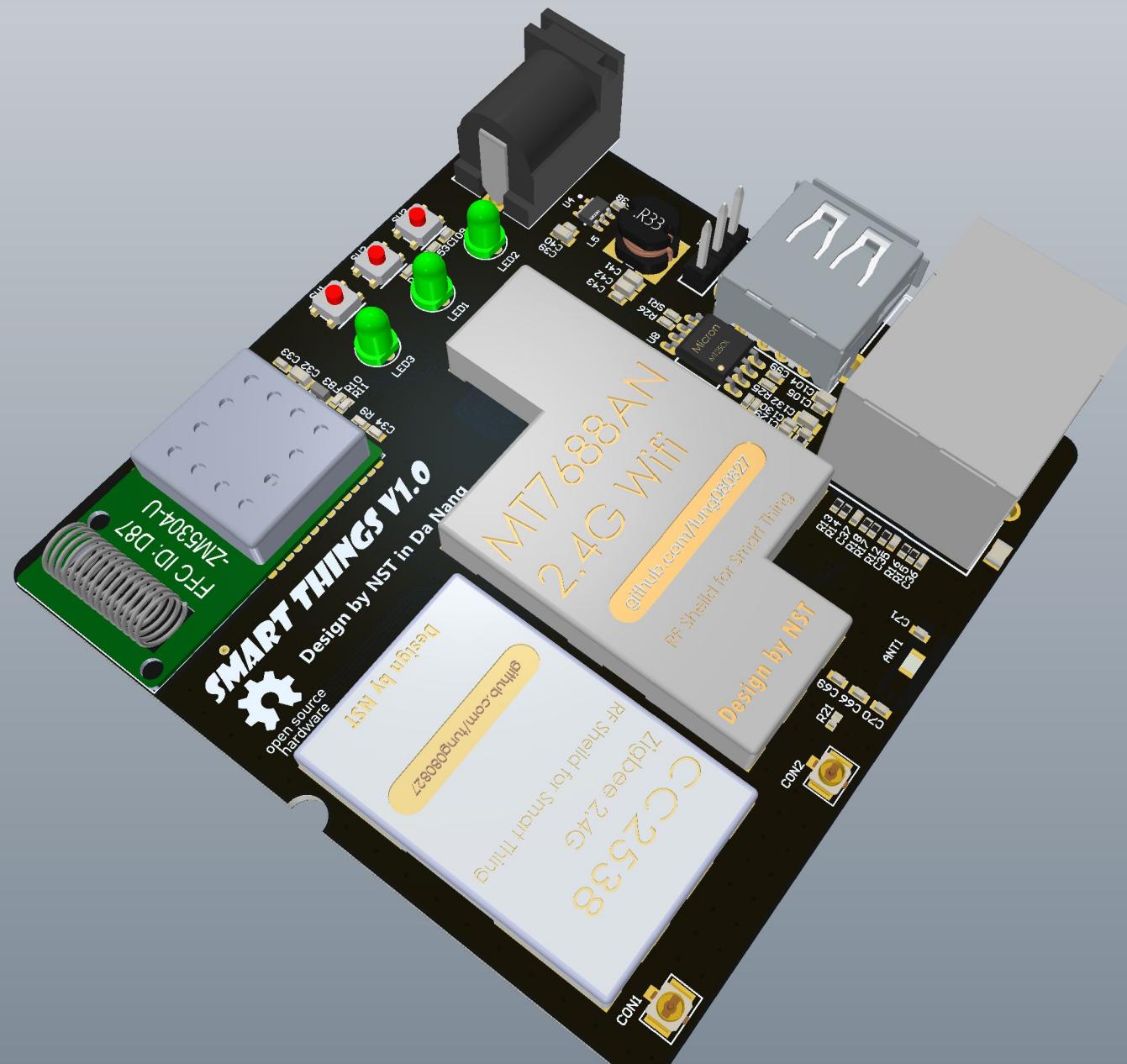
Date: 5/27/2020 Time: 10:59:03 PM Sheet 12 of 12

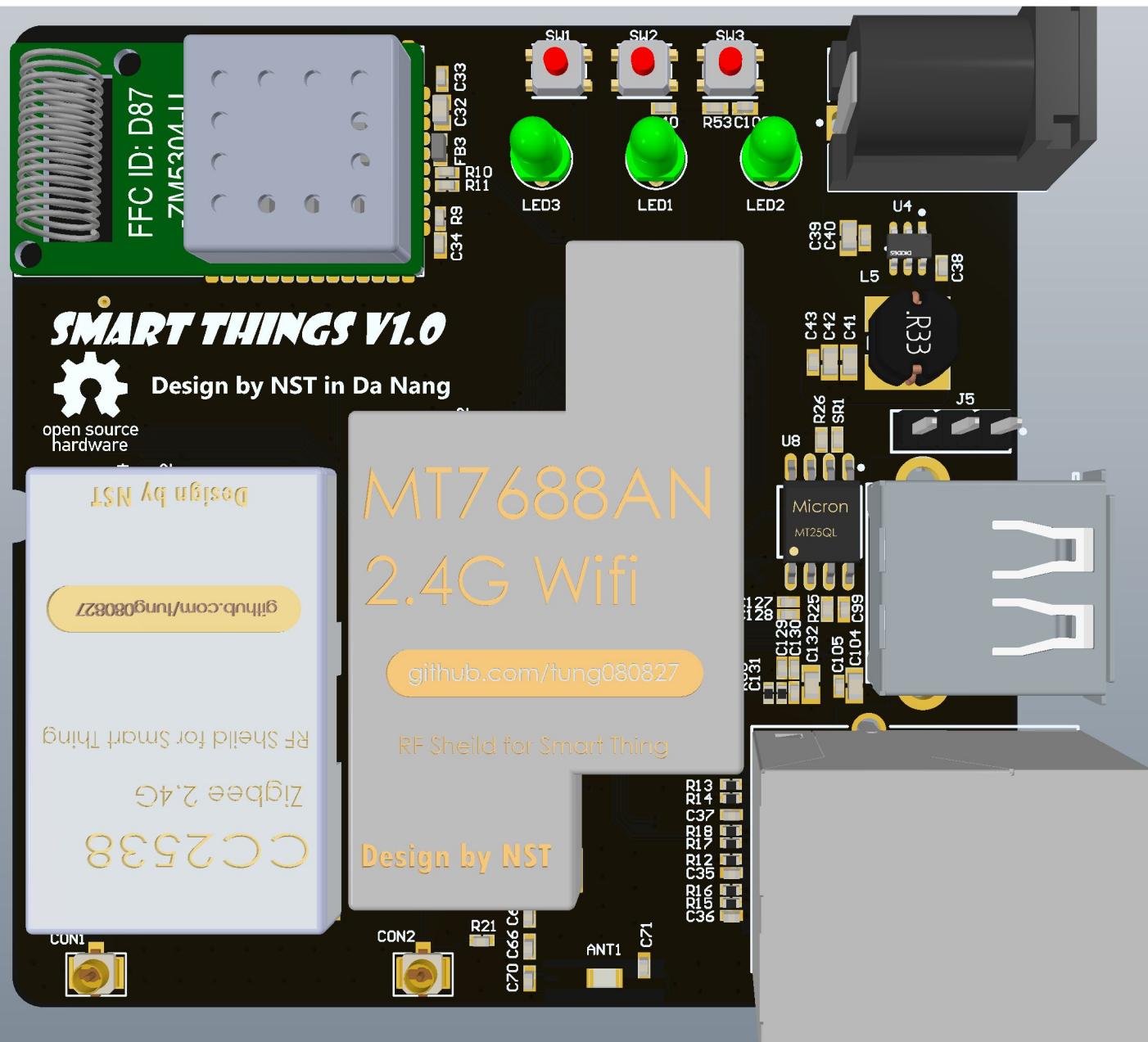
Design by: N.S.T Check by: N.S.T

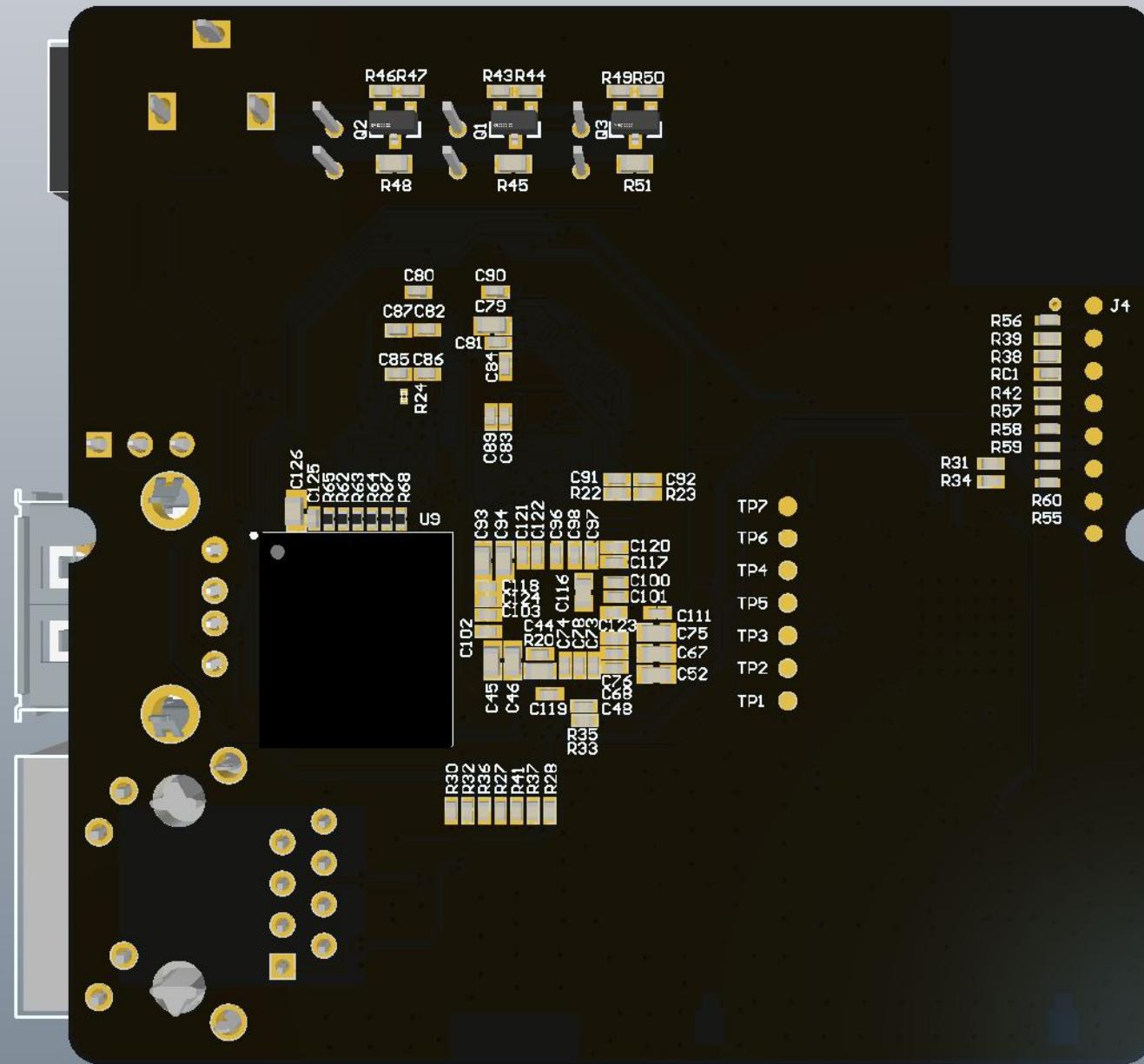
N . S . T

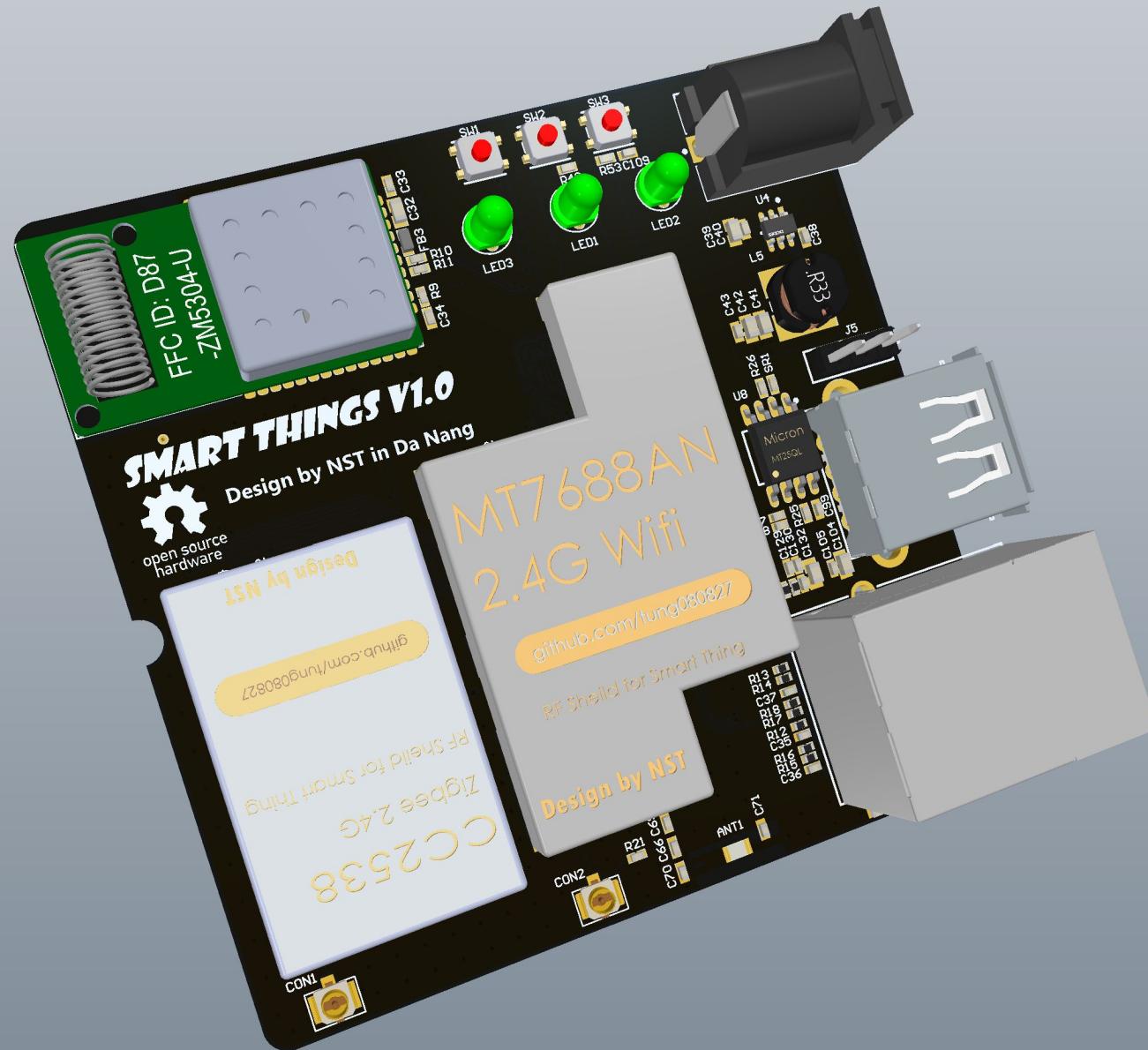


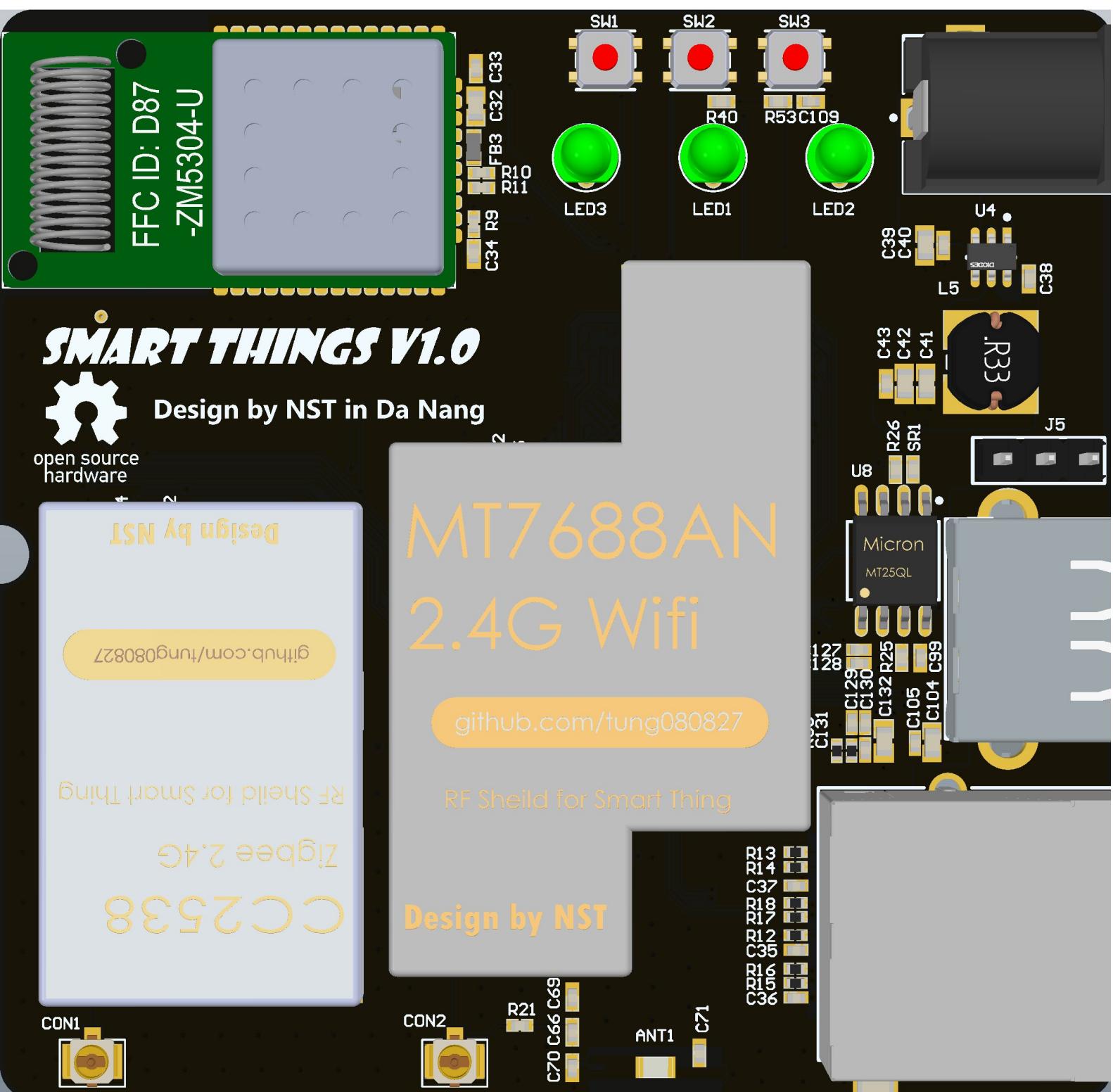












Board Stack Report