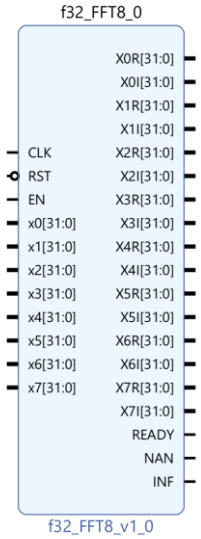
IEEE754 single precision floating point (f32) support 8-points FFT



Pin name	Description	Direction	Property
CLK	CLK input	IN	1bit
RST	Reset input	IN	1bit, active High
EN	Enable	IN	1bit, active High
x0	Sample[0] input	IN	32bit, IEEE754 single precision floating point format
x1	Sample[1] input	IN	32bit, IEEE754 single precision floating point format
x2	Sample[2] input	IN	32bit, IEEE754 single precision floating point format
x3	Sample[3] input	IN	32bit, IEEE754 single precision floating point format
x4	Sample[4] input	IN	32bit, IEEE754 single precision floating point format
x5	Sample[5] input	IN	32bit, IEEE754 single precision floating point format
x6	Sample[6] input	IN	32bit, IEEE754 single precision floating point format
x7	Sample[7] input	IN	32bit, IEEE754 single precision floating point format
X0R	Real part of Output[0]	OUT	32bit, IEEE754 single precision floating point format
X0I	Imaginary part of Output[0]	OUT	32bit, IEEE754 single precision floating point format
X1R	Real part of Output[1]	OUT	32bit, IEEE754 single precision floating point format
X1I	Imaginary part of Output[1]	OUT	32bit, IEEE754 single precision floating point format
X2R	Real part of Output[2]	OUT	32bit, IEEE754 single precision floating point format
X2I	Imaginary part of Output[2]	OUT	32bit, IEEE754 single precision floating point format
X3R	Real part of Output[3]	OUT	32bit, IEEE754 single precision floating point format
X3I	Imaginary part of Output[3]	OUT	32bit, IEEE754 single precision floating point format
X4R	Real part of Output[4]	OUT	32bit, IEEE754 single precision floating point format
X4I	Imaginary part of Output[4]	OUT	32bit, IEEE754 single precision floating point format
X5R	Real part of Output[5]	OUT	32bit, IEEE754 single precision floating point format
X5I	Imaginary part of Output[5]	OUT	32bit, IEEE754 single precision floating point format
X6R	Real part of Output[6]	OUT	32bit, IEEE754 single precision floating point format
X6I	Imaginary part of Output[6]	OUT	32bit, IEEE754 single precision floating point format
X7R	Real part of Output[7]	OUT	32bit, IEEE754 single precision floating point format
X7I	Imaginary part of Output[7]	OUT	32bit, IEEE754 single precision floating point format
READY	Result is valid and ready	OUT	1bit, active High
NAN	Result is not valid: NaN (Not a Number)	OUT	1bit, active High
INF	Result is not valid: +/- Infinite	OUT	1bit, active High