

TITLE DECODE CONTROL

FILE:

PAGE 1

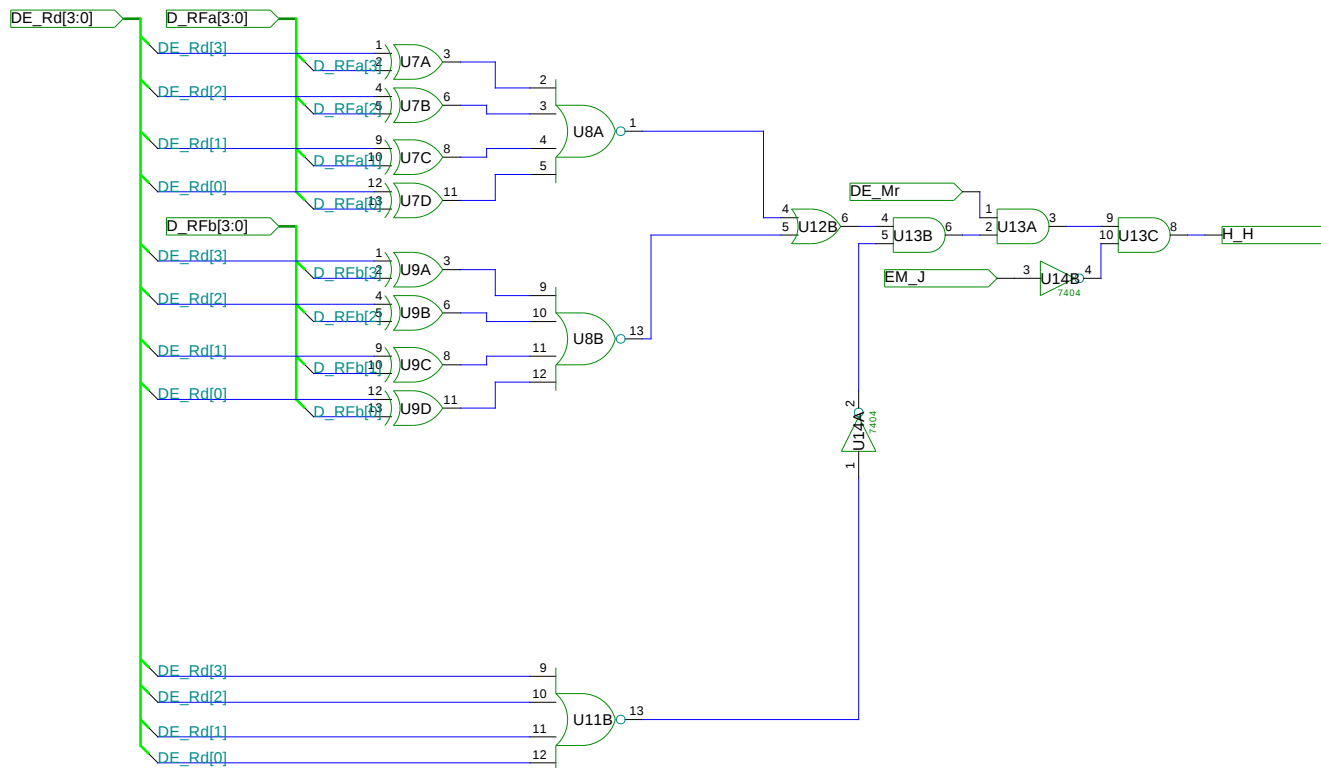
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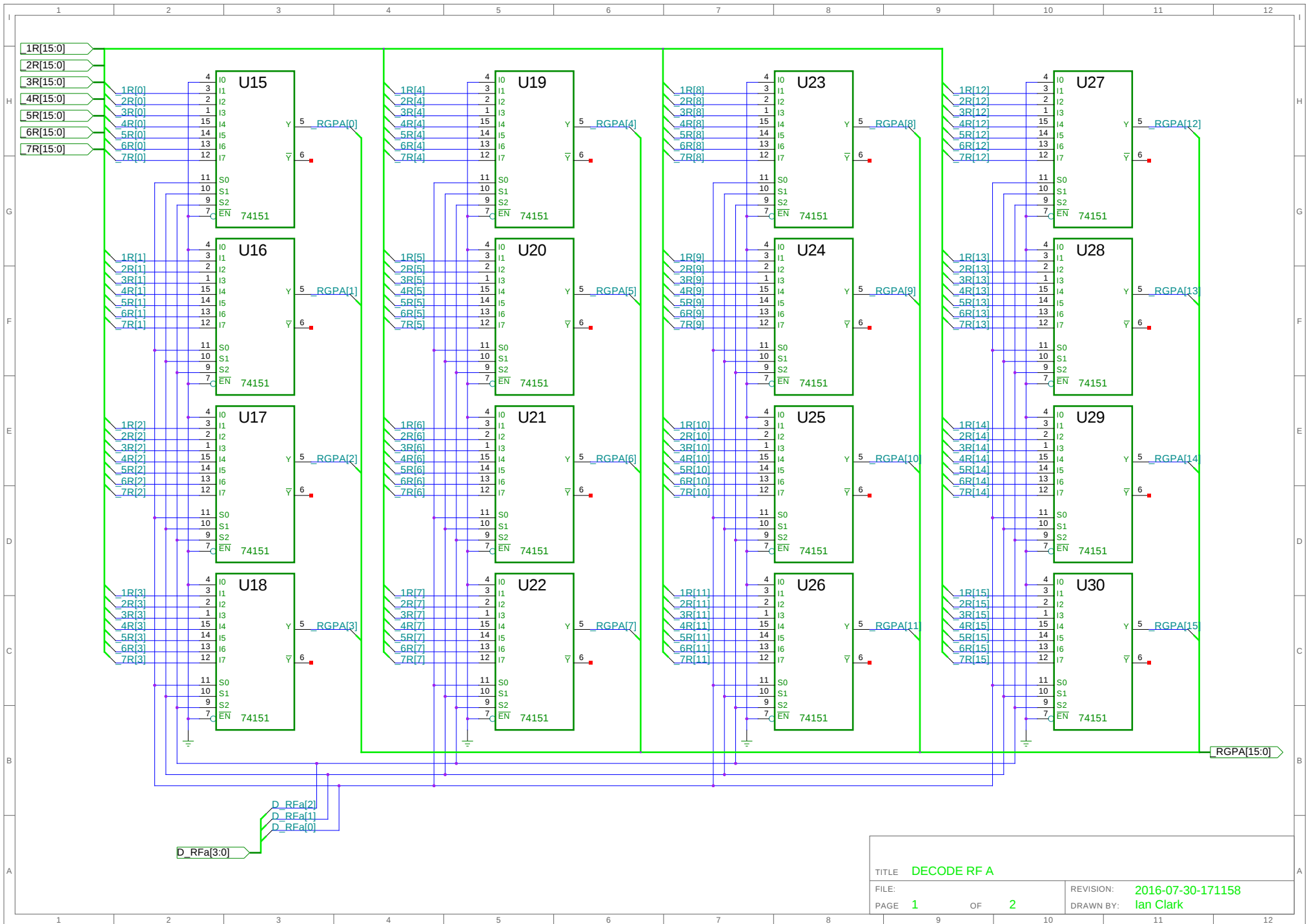
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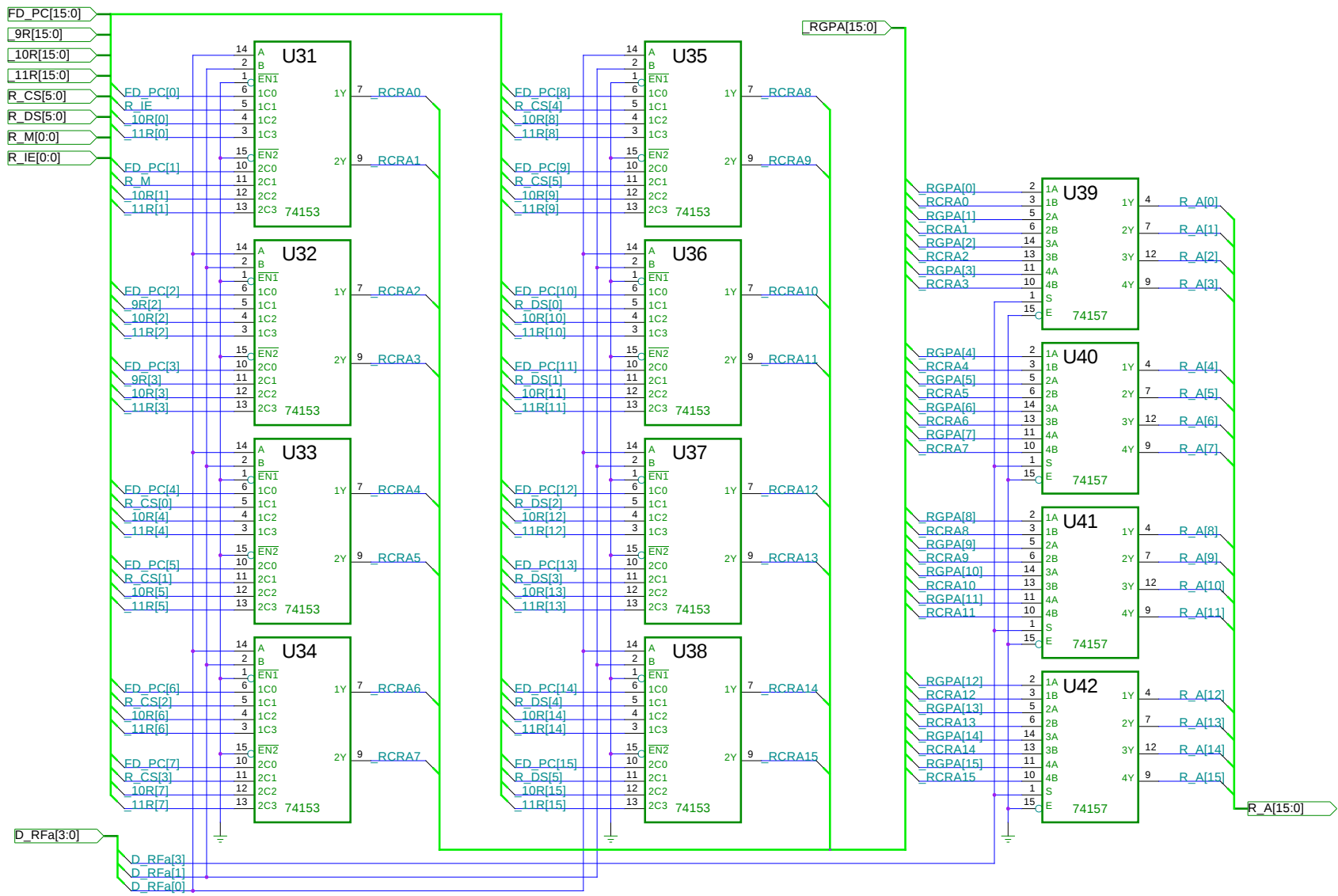
Ian Clark



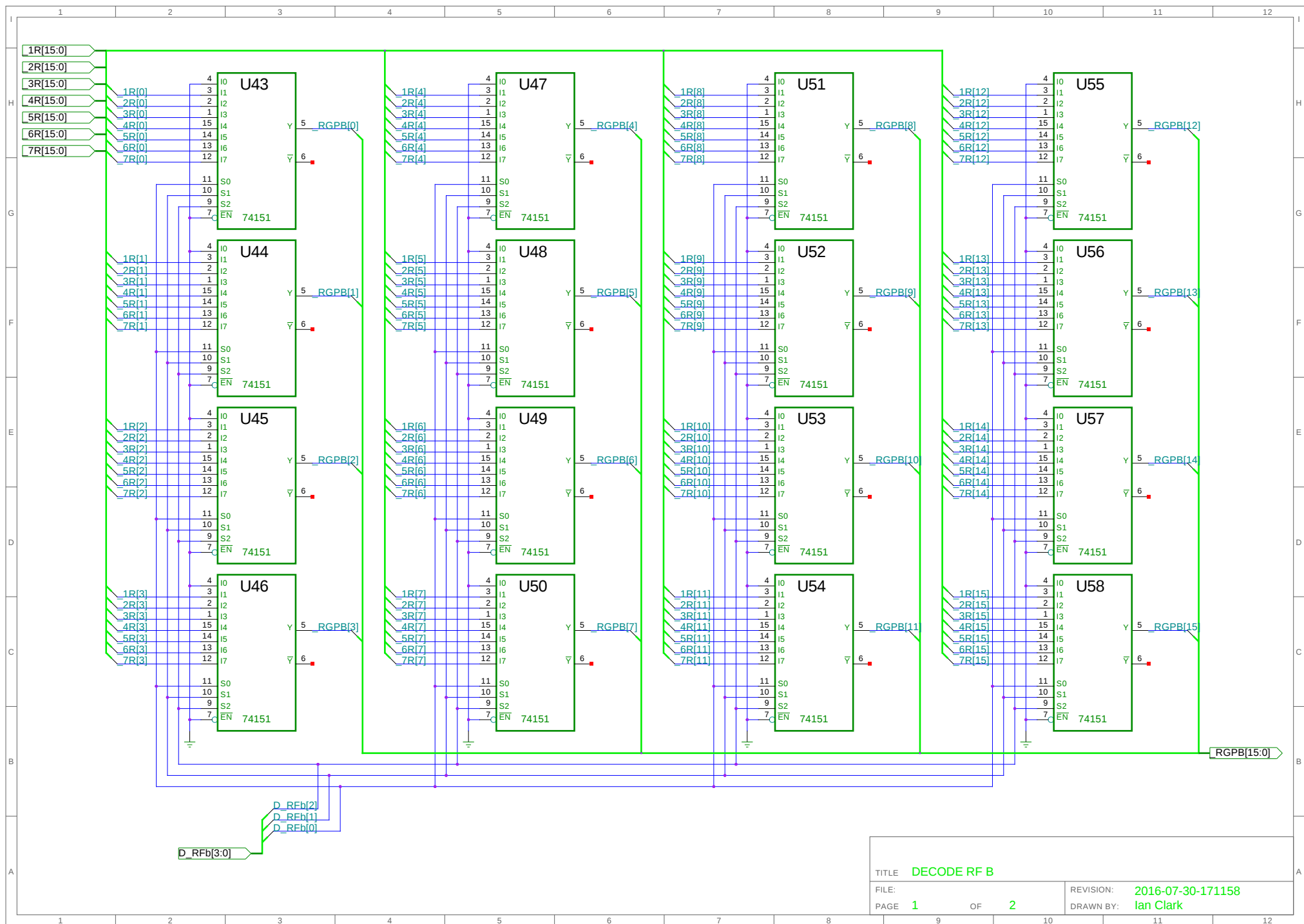
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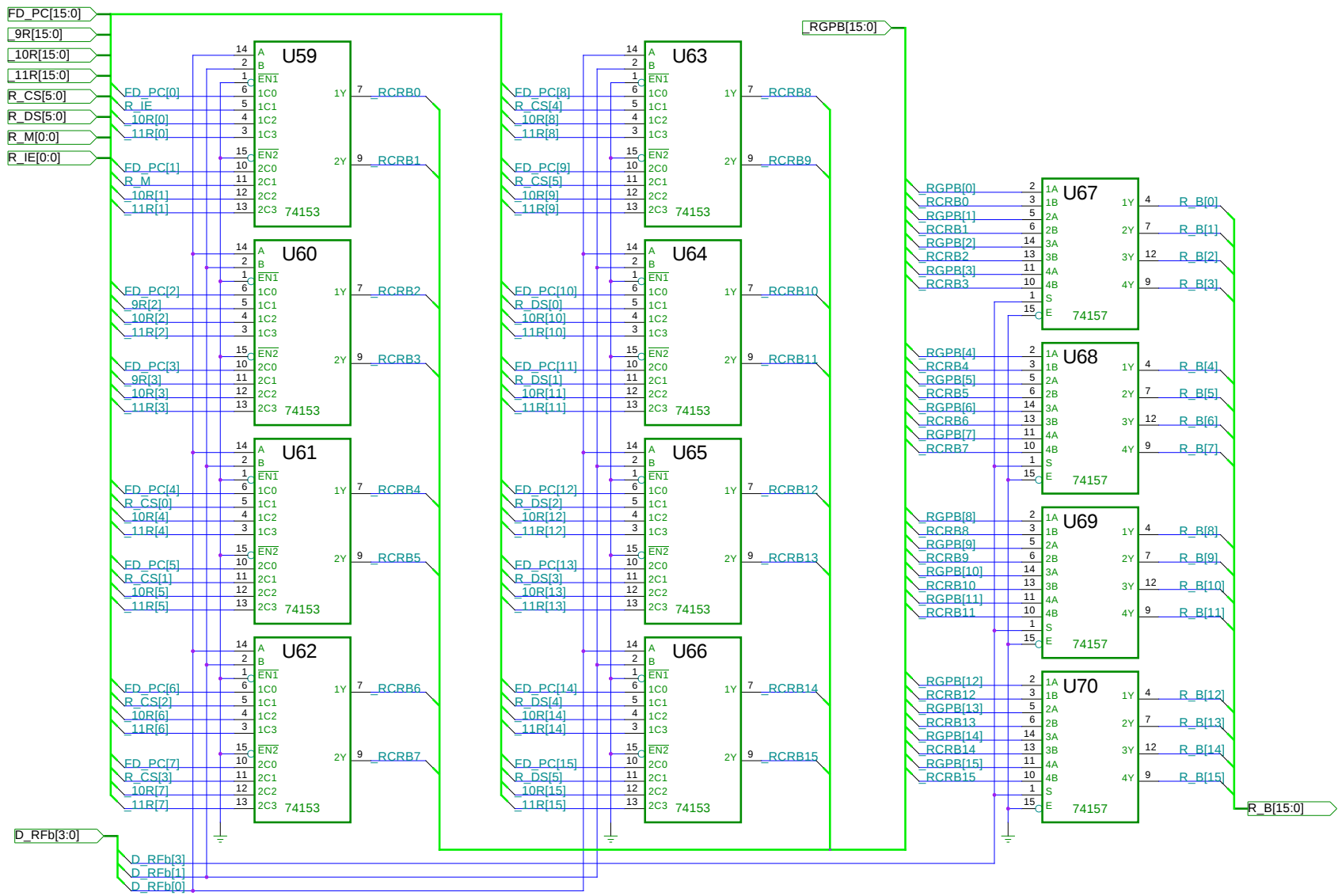


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1		2016-07-30-171158	
OF		Ian Clark	
2			

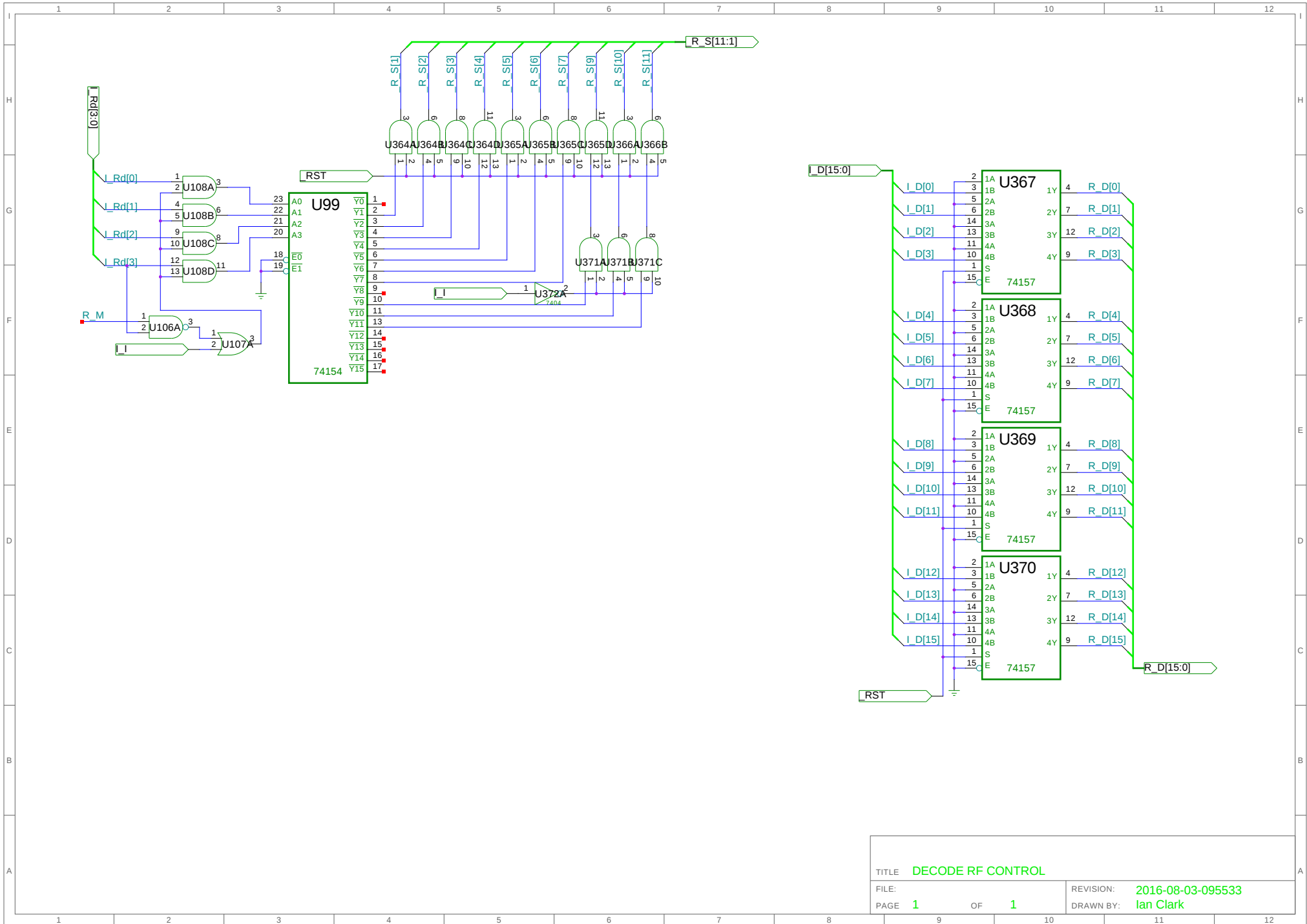


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PAGE	2	OF	2
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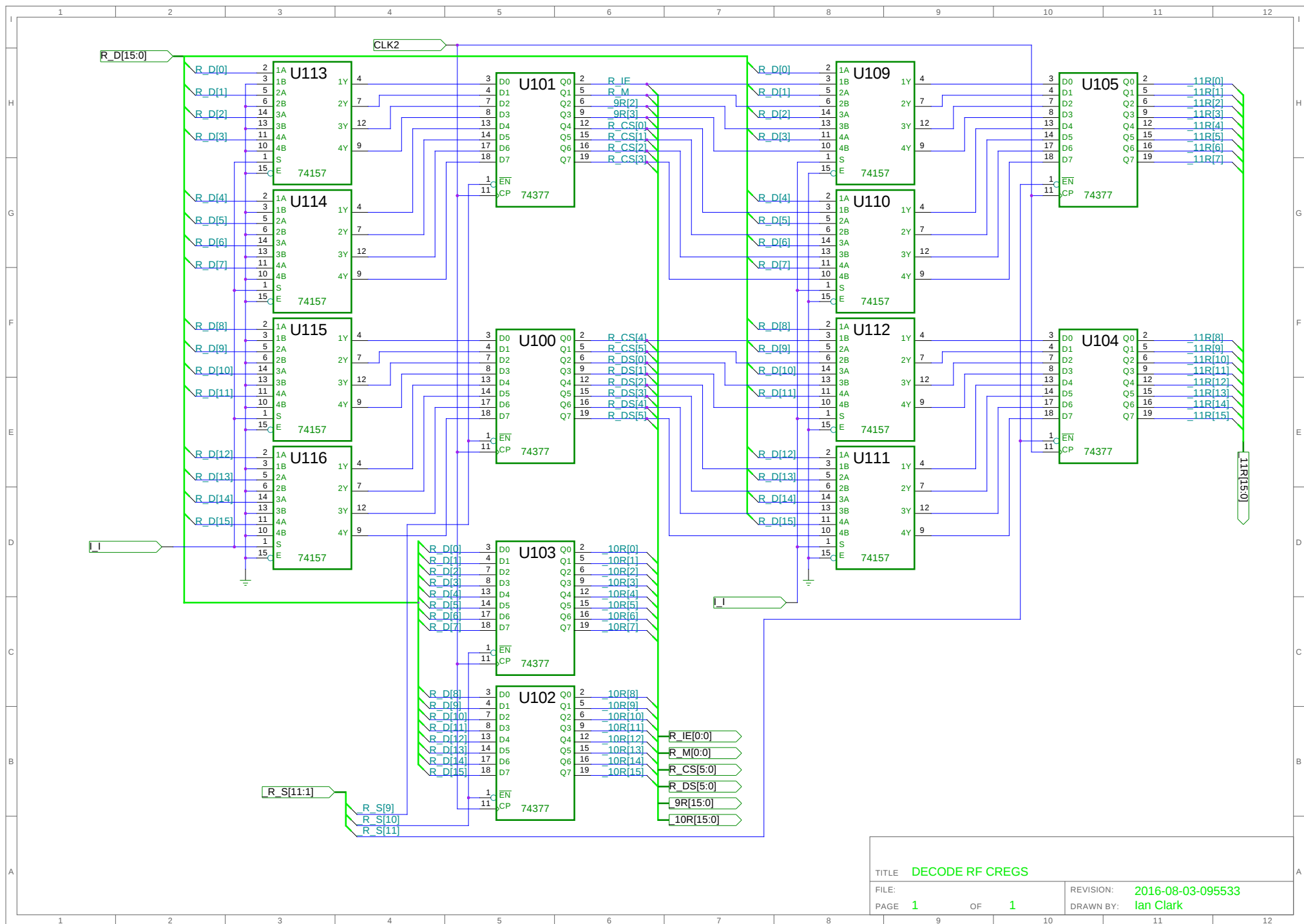


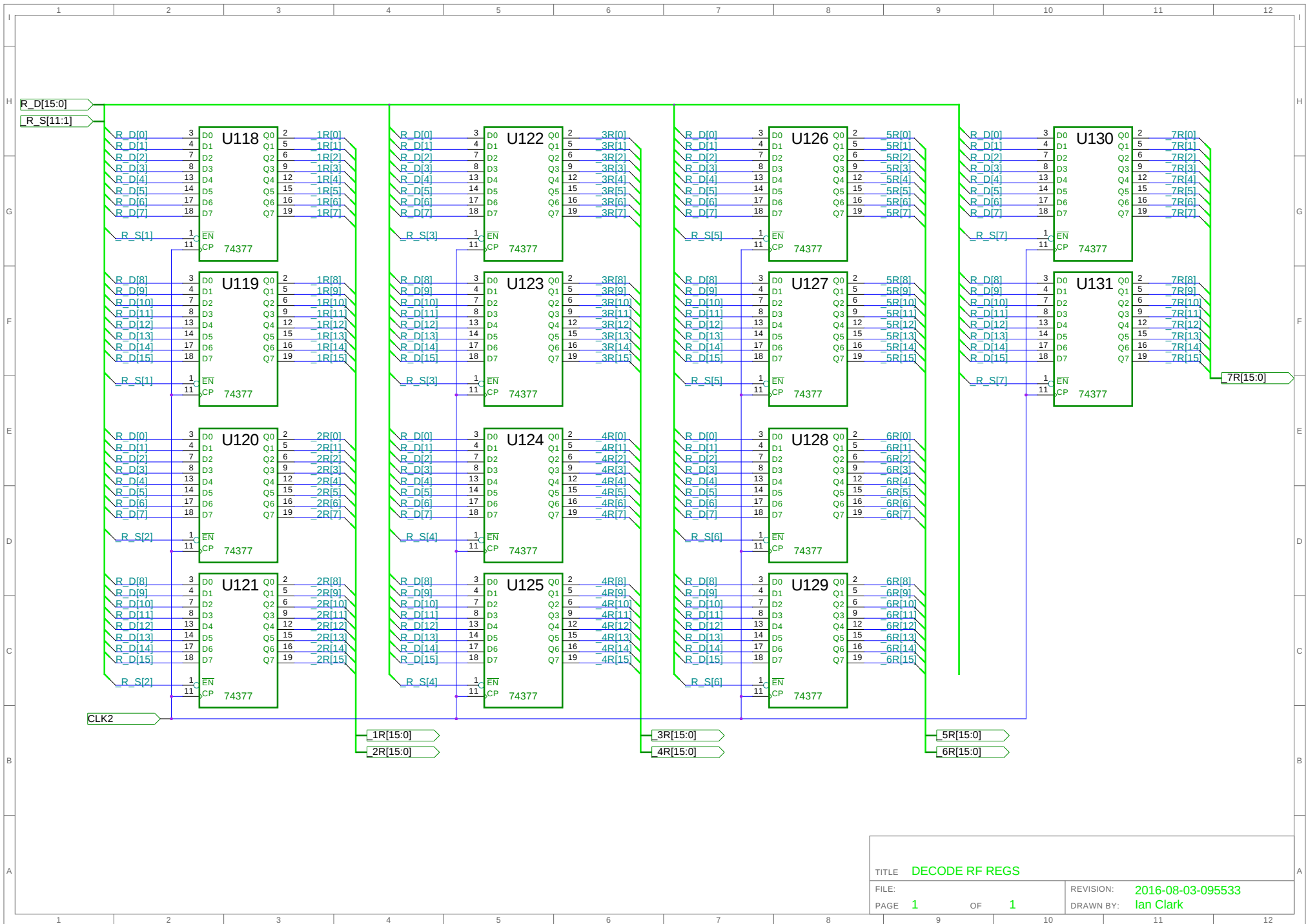
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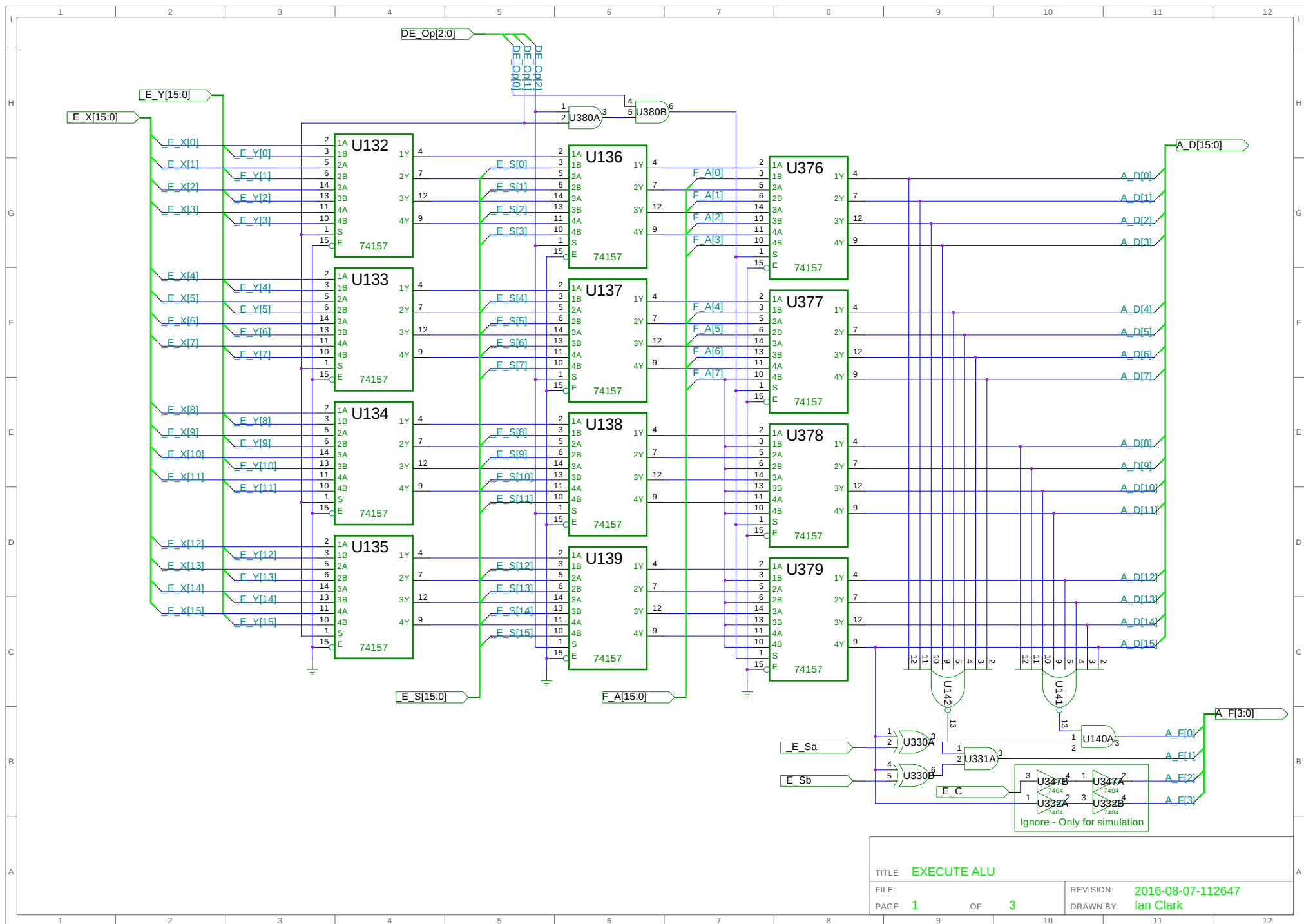
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FILE:	REVISION:	2016-08-03-095533
PAGE 1 OF 1	DRAWN BY:	Ian Clark

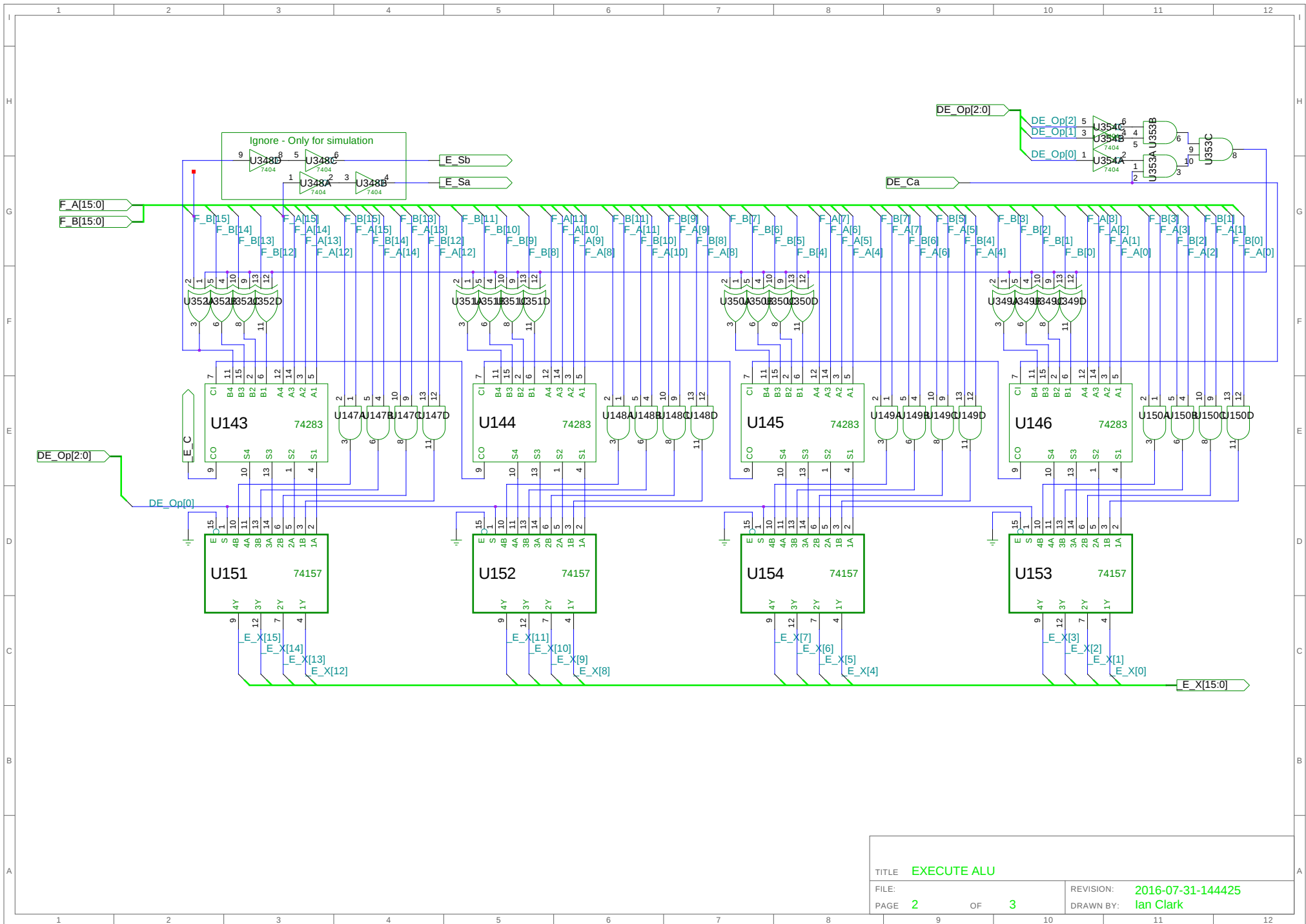


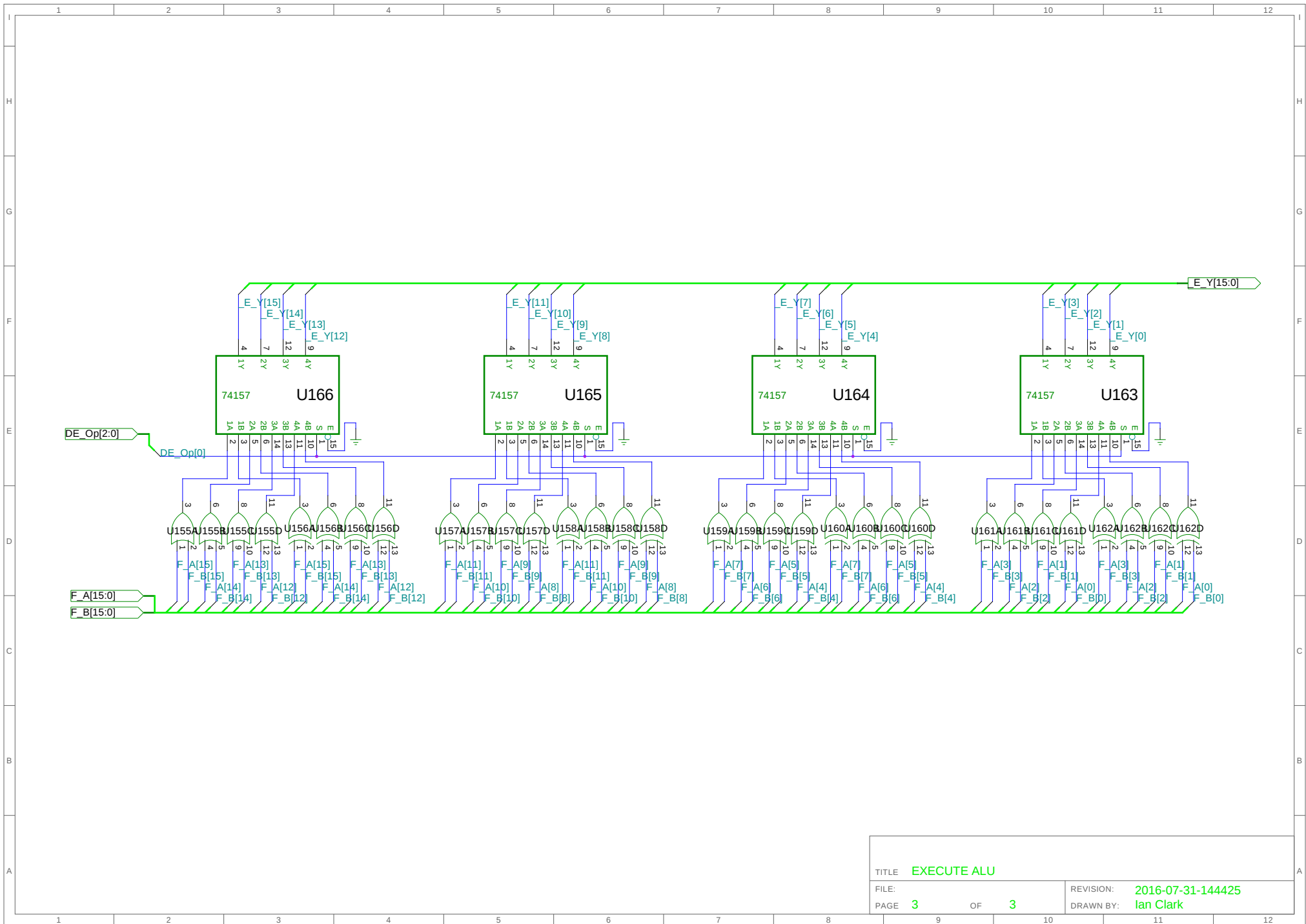




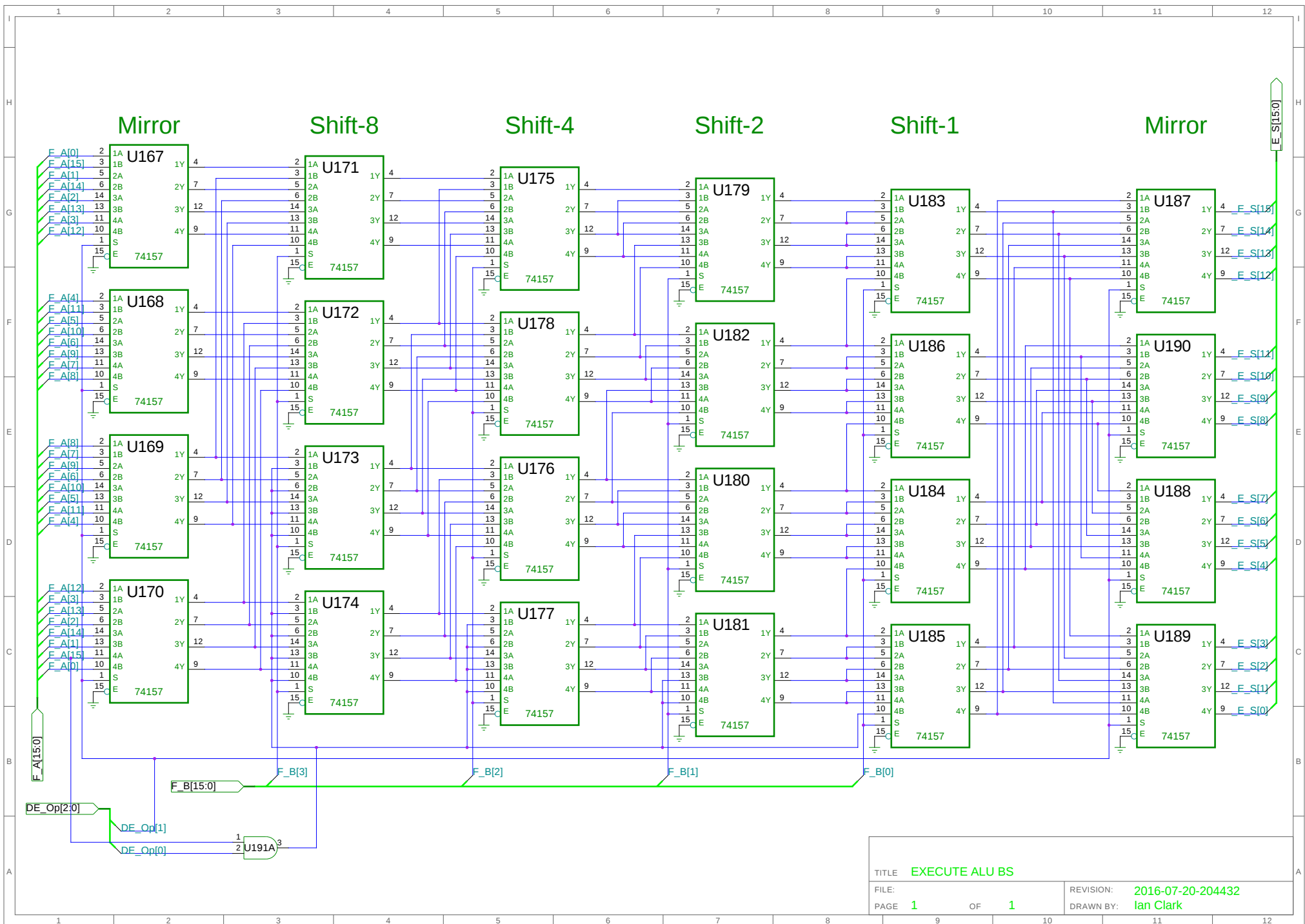
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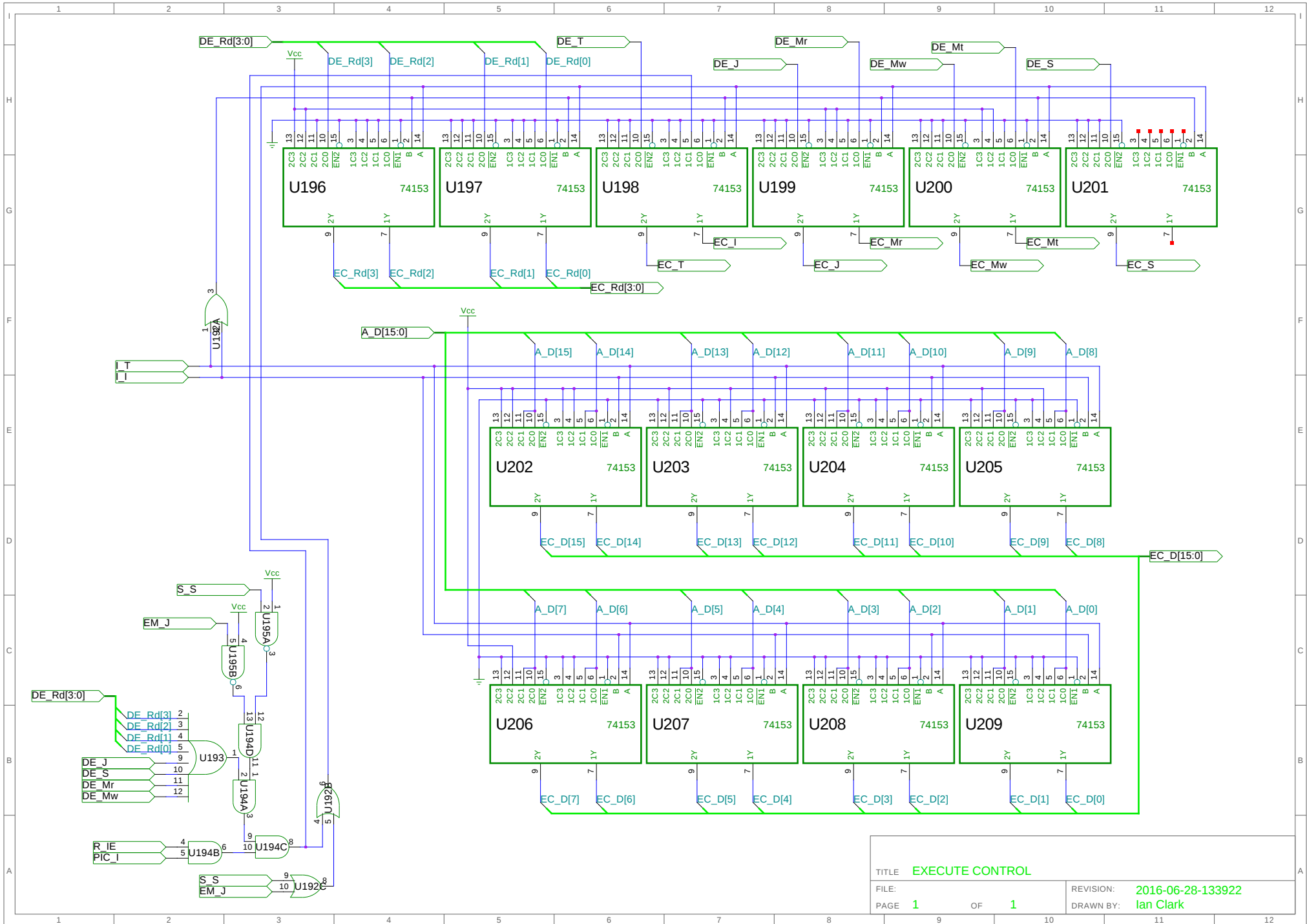




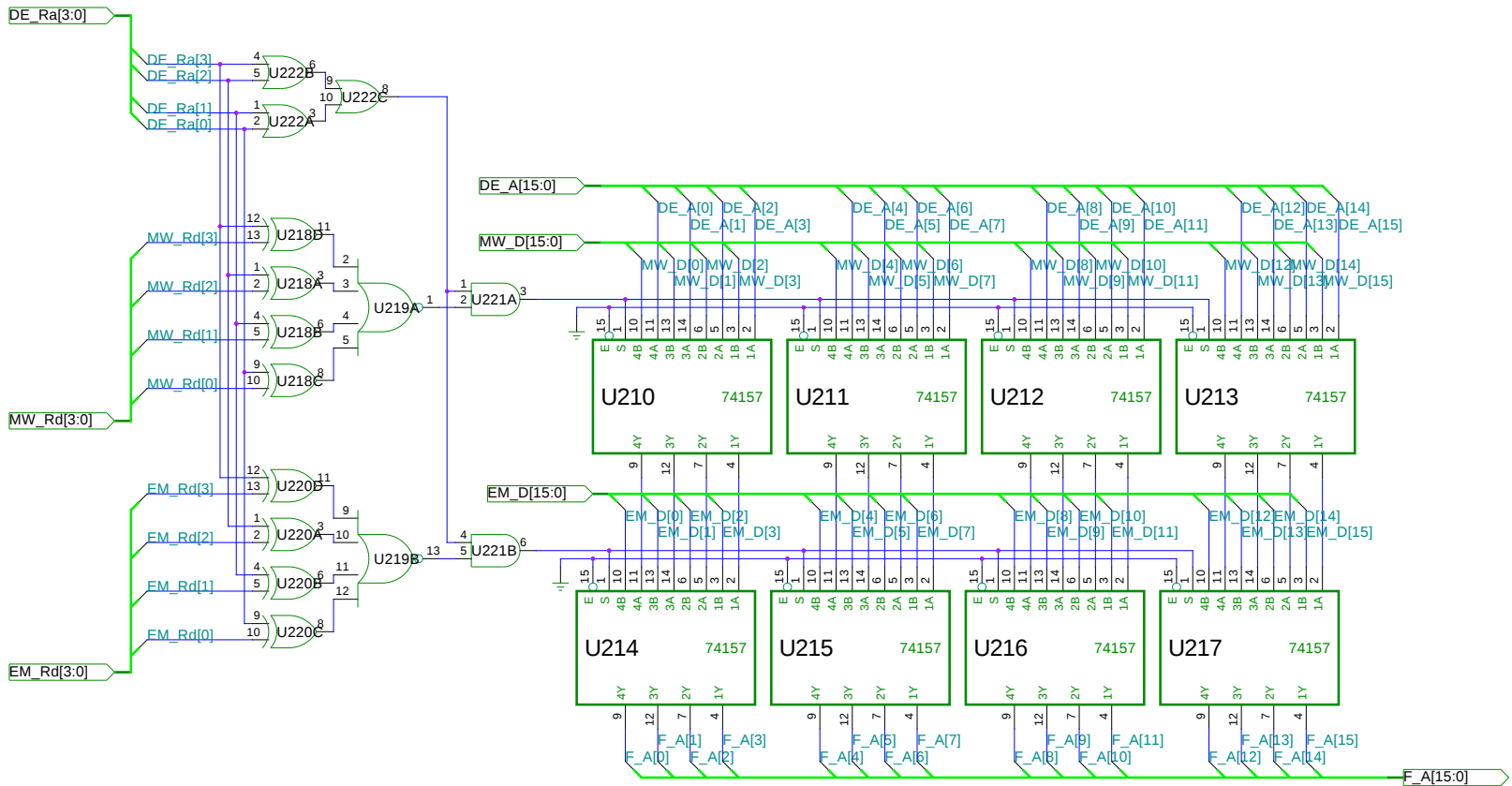
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FILE:	REVISION:	2016-07-31-144425
PAGE 3 OF 3	DRAWN BY:	Ian Clark



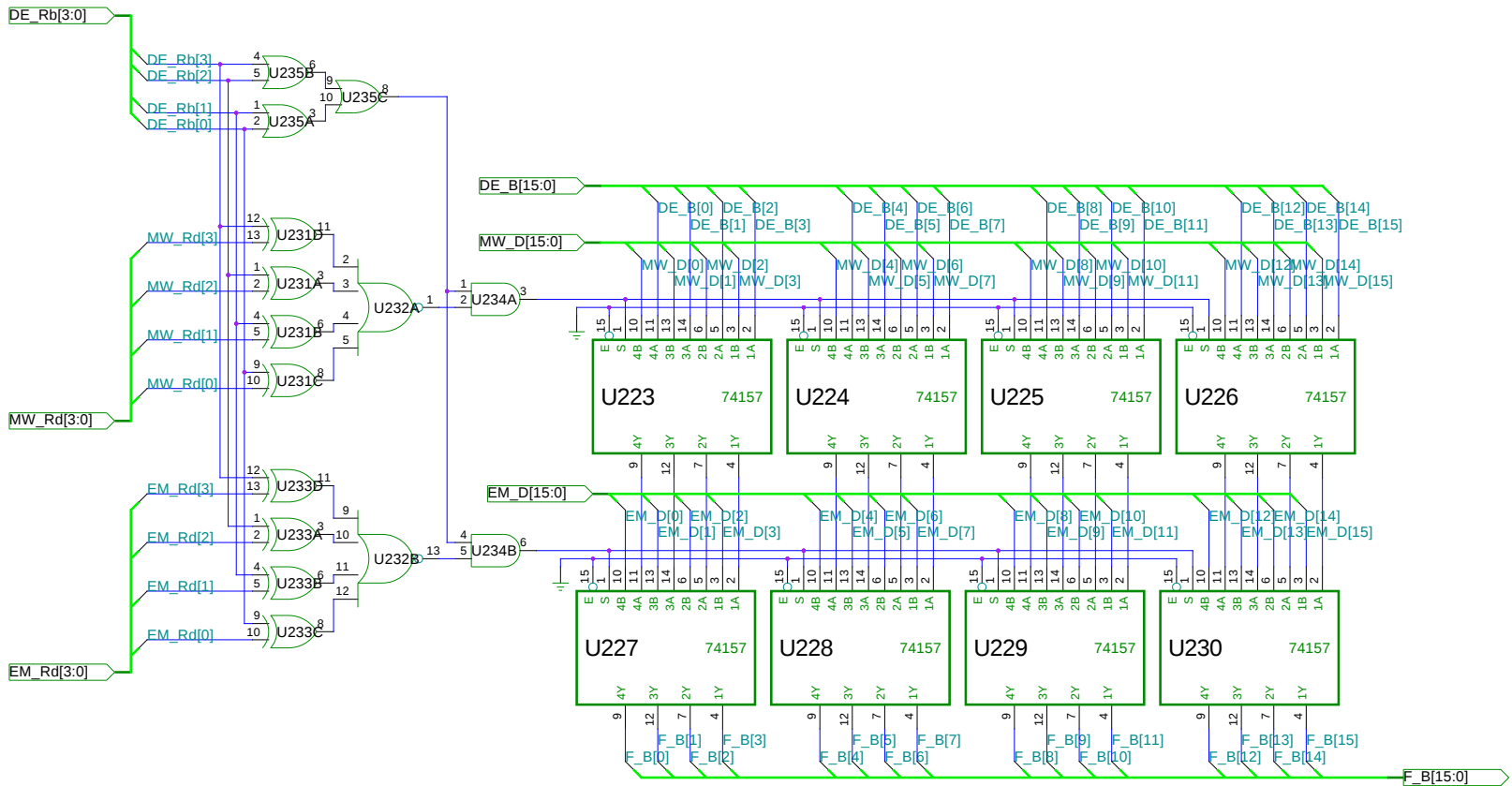
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FILE:			DRAWN BY: Ian Clark		
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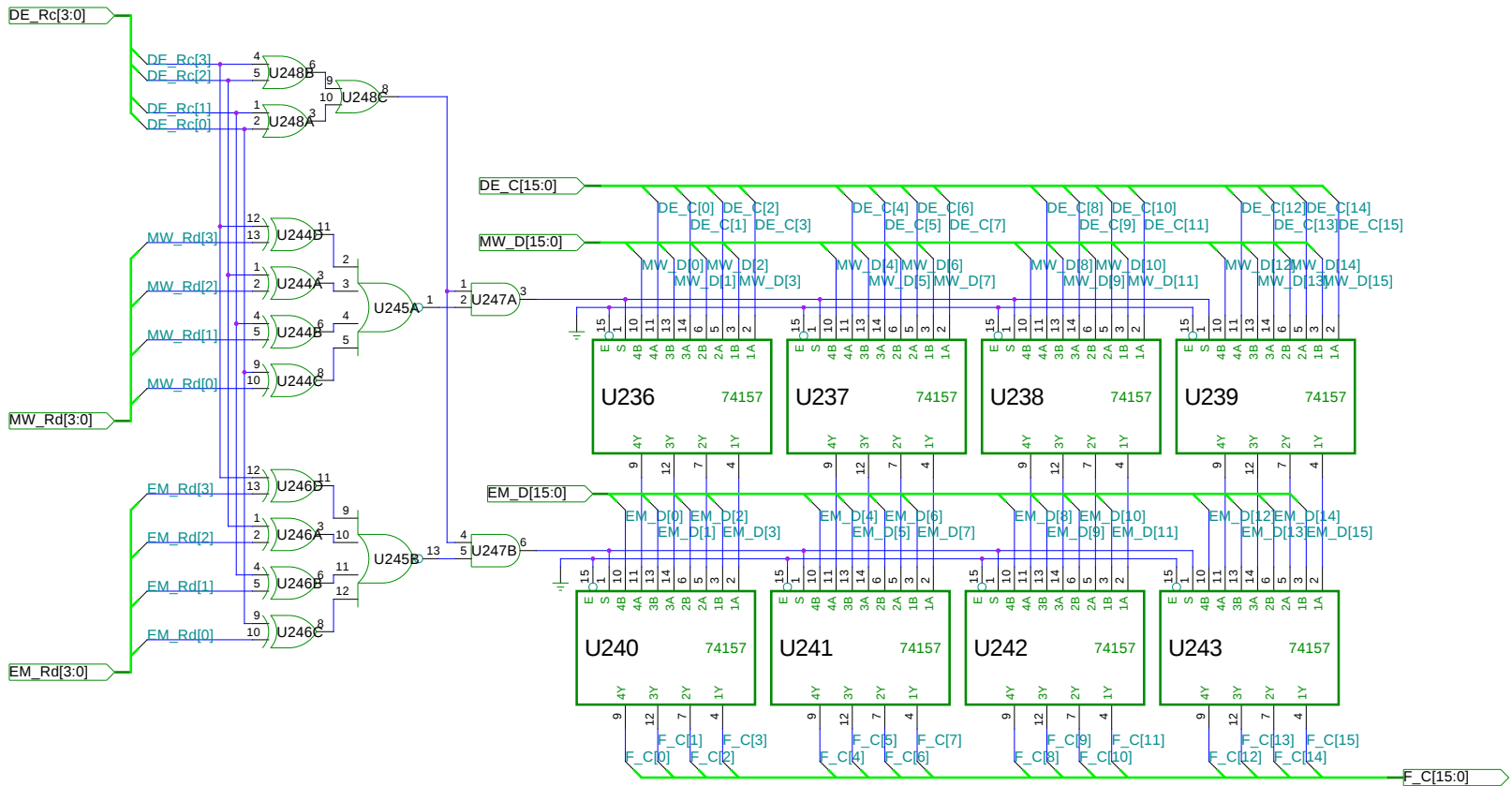


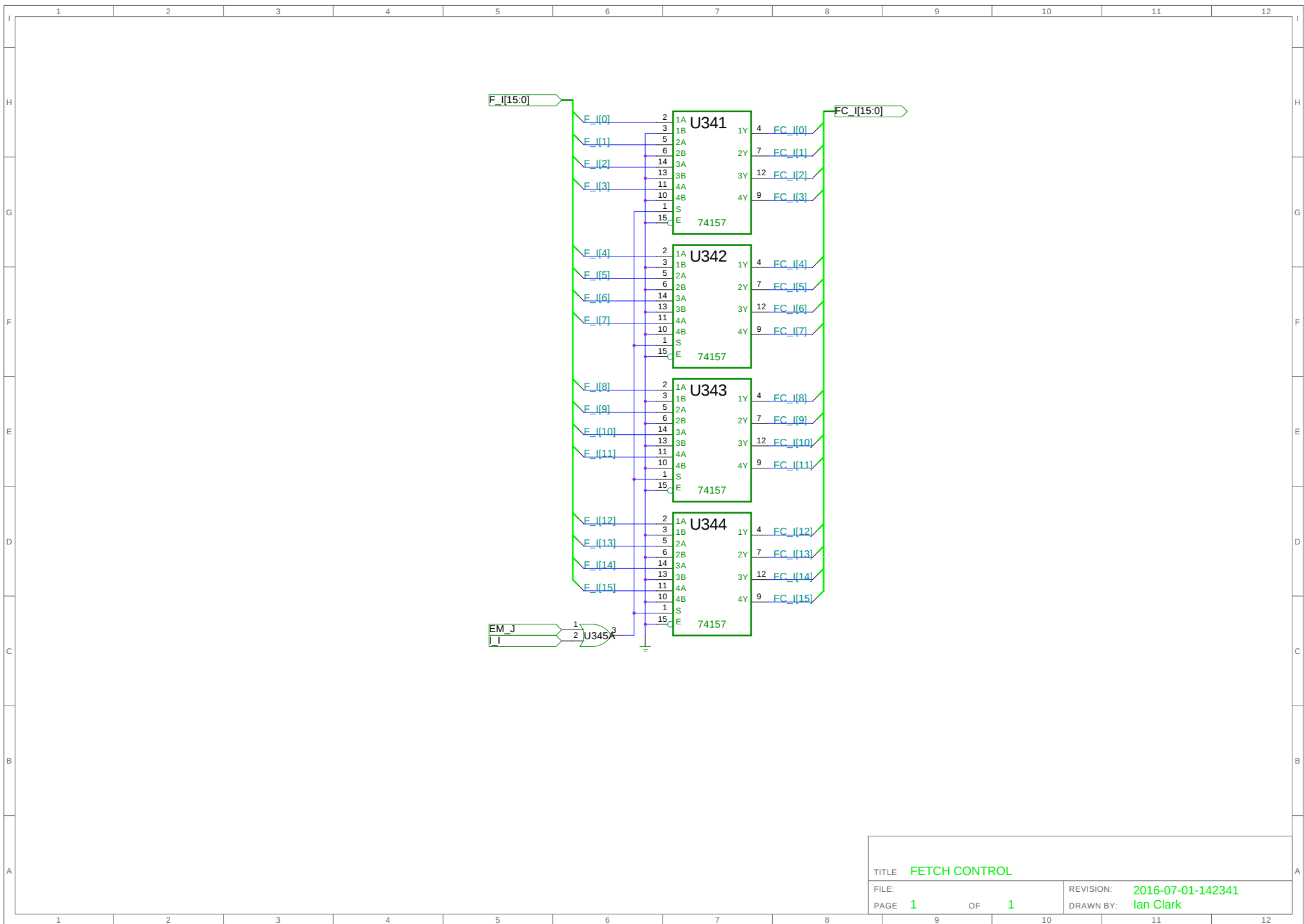
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FILE:		DRAWN BY: Ian Clark	
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TITLE **FETCH CONTROL**

FILE:

PAGE **1**

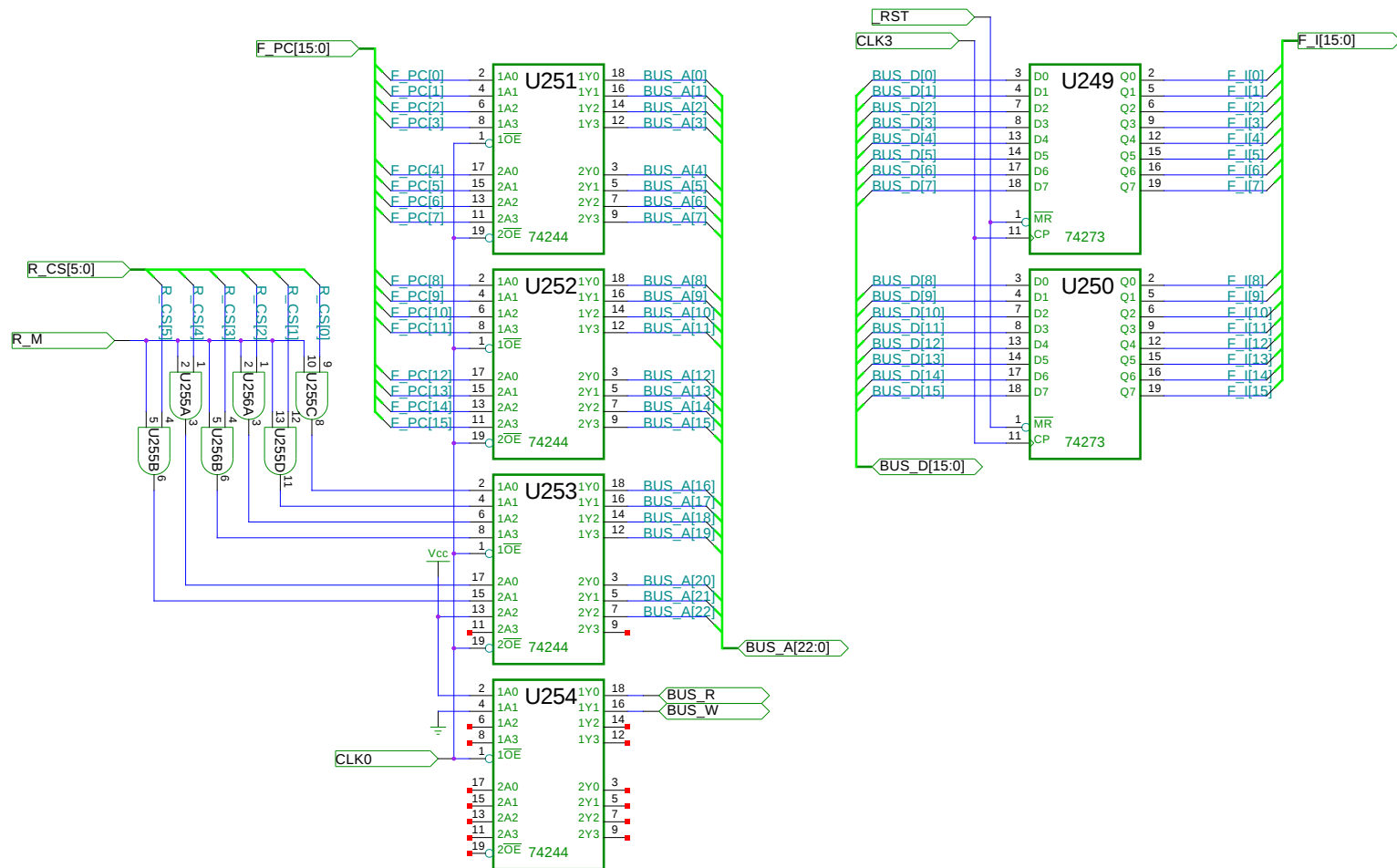
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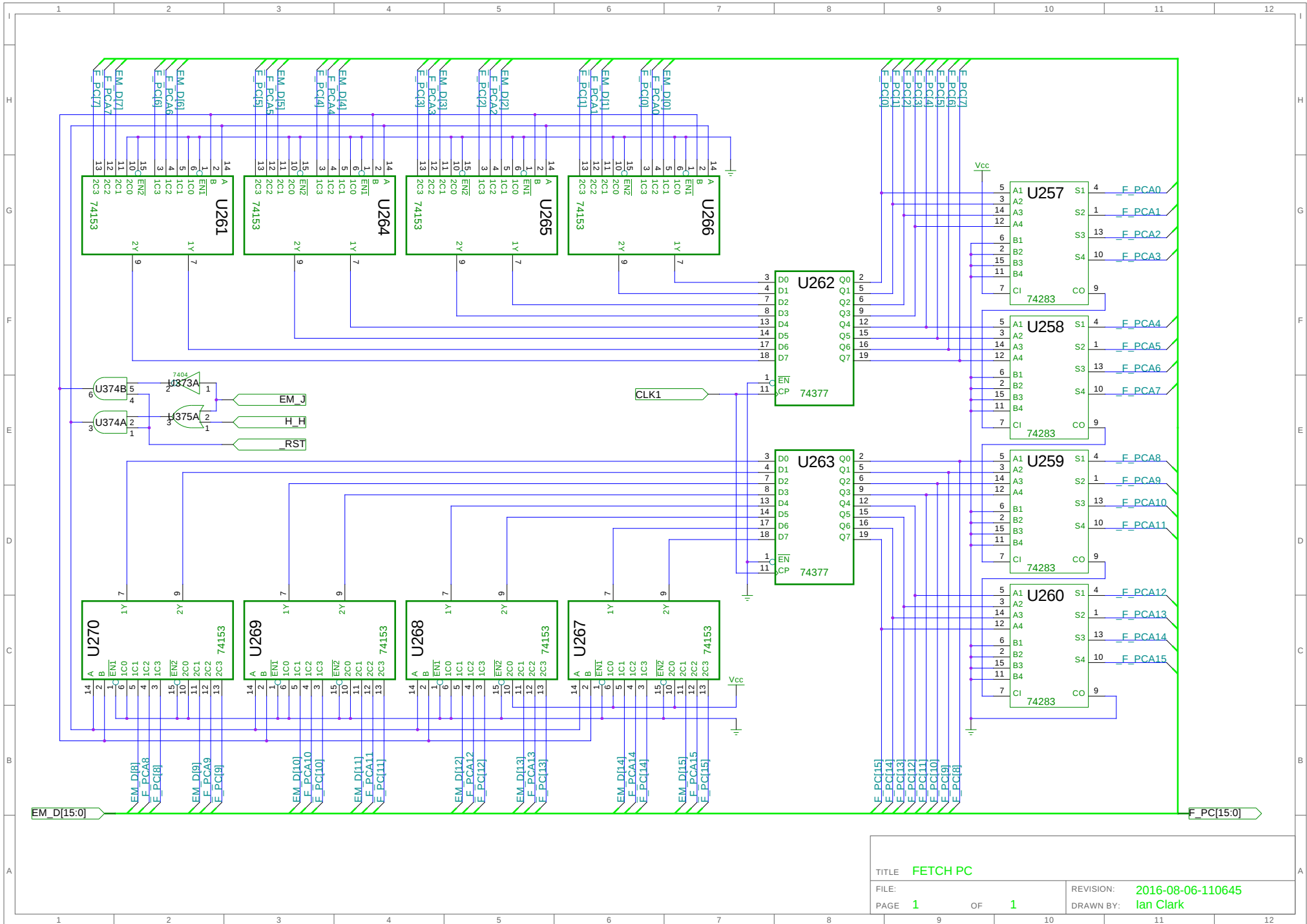
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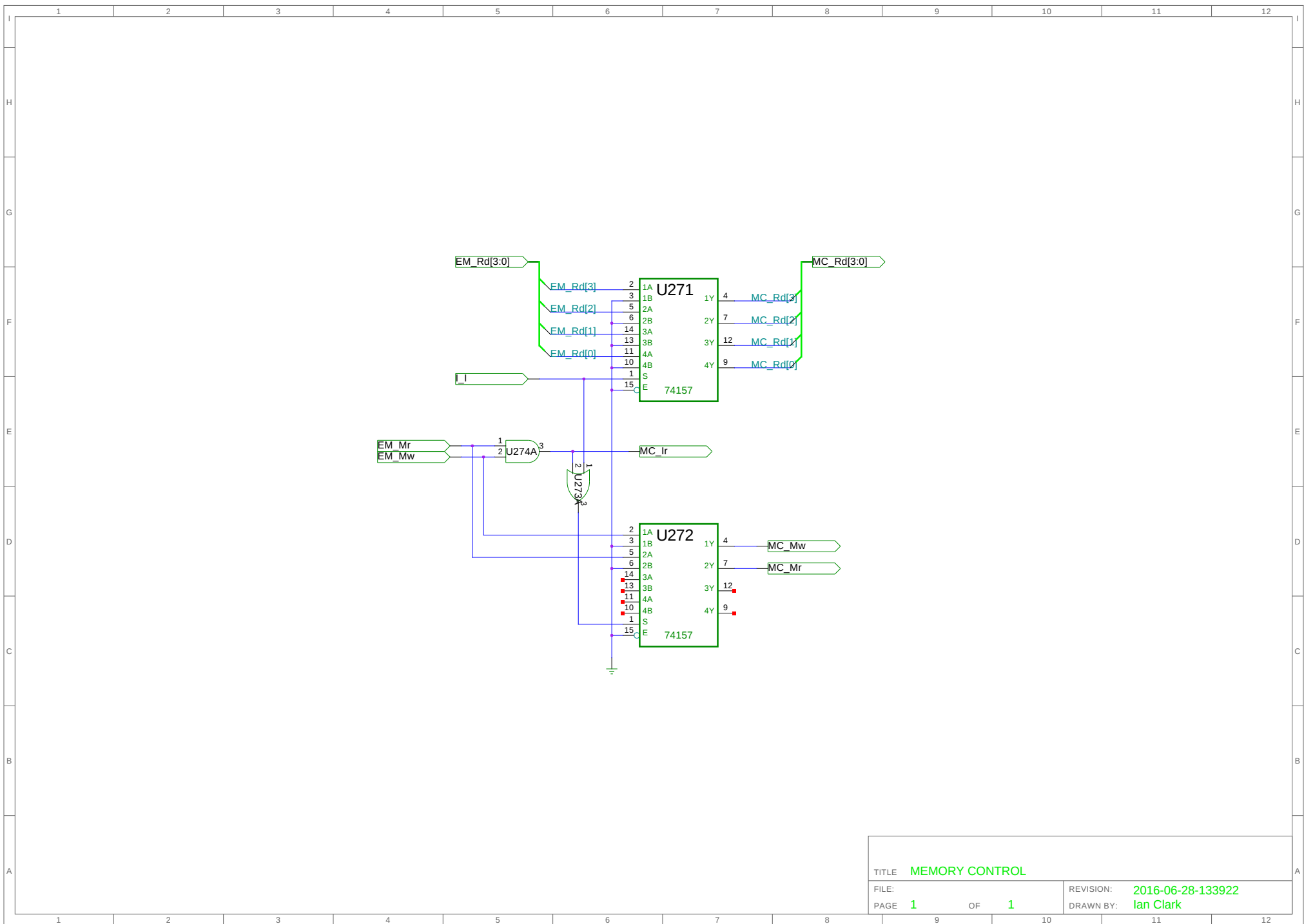
**Ian Clark**

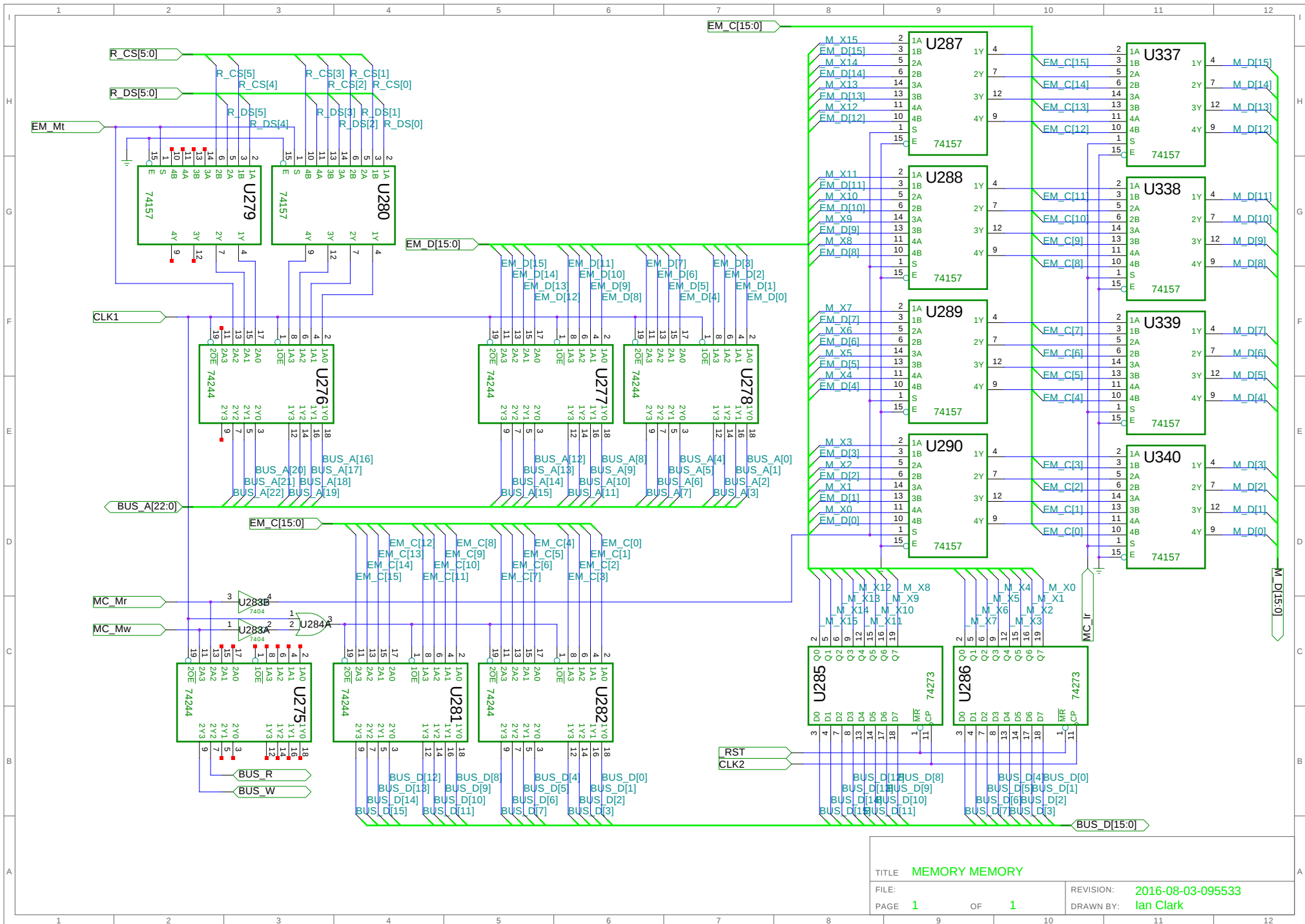


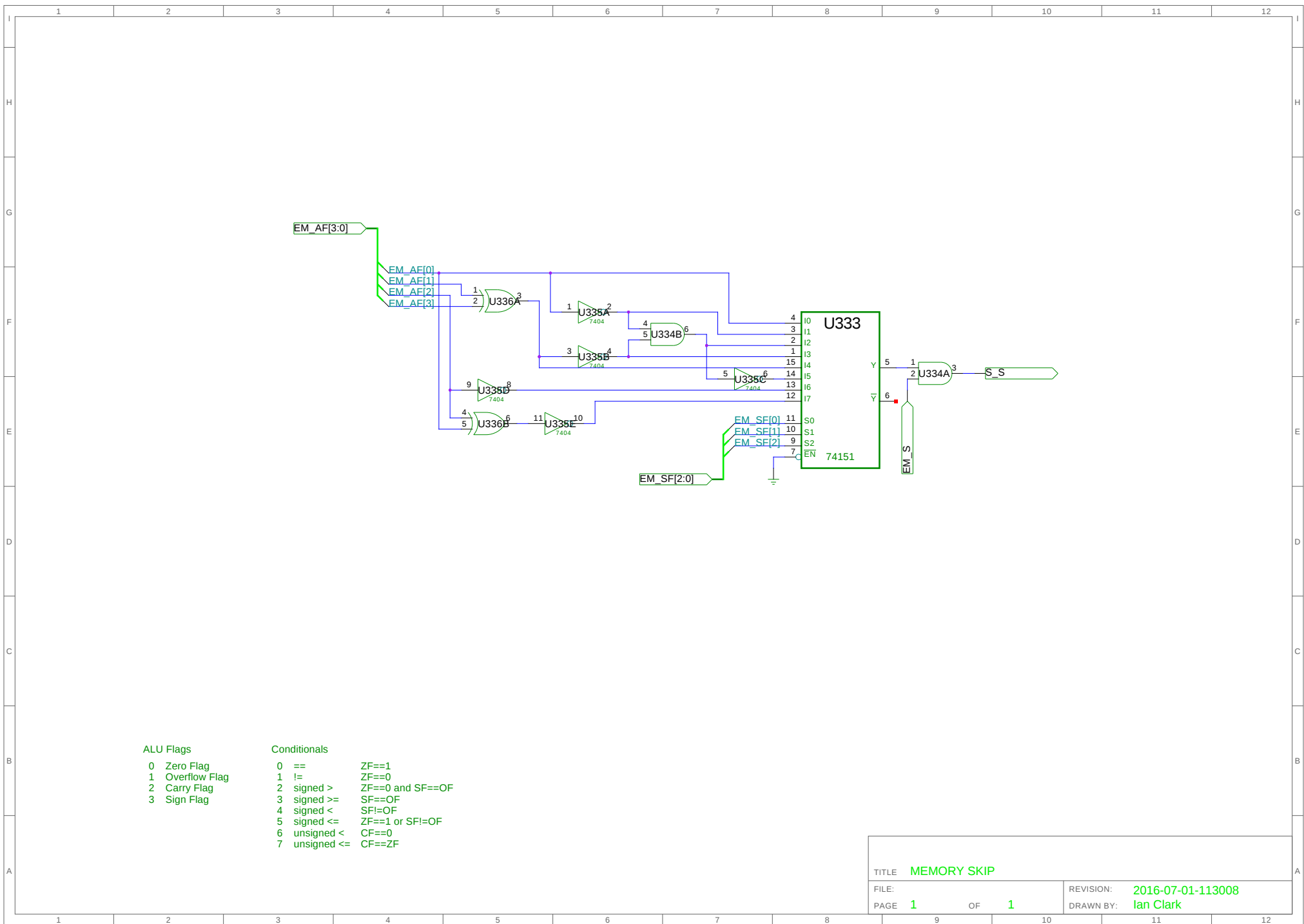
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FILE:		REVISION: 2016-08-03-095533	
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DRAWN BY:		Ian Clark	



TITLE <b>FETCH PC</b>				
FILE:		REVISION: <b>2016-08-06-110645</b>		
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ALU Flags

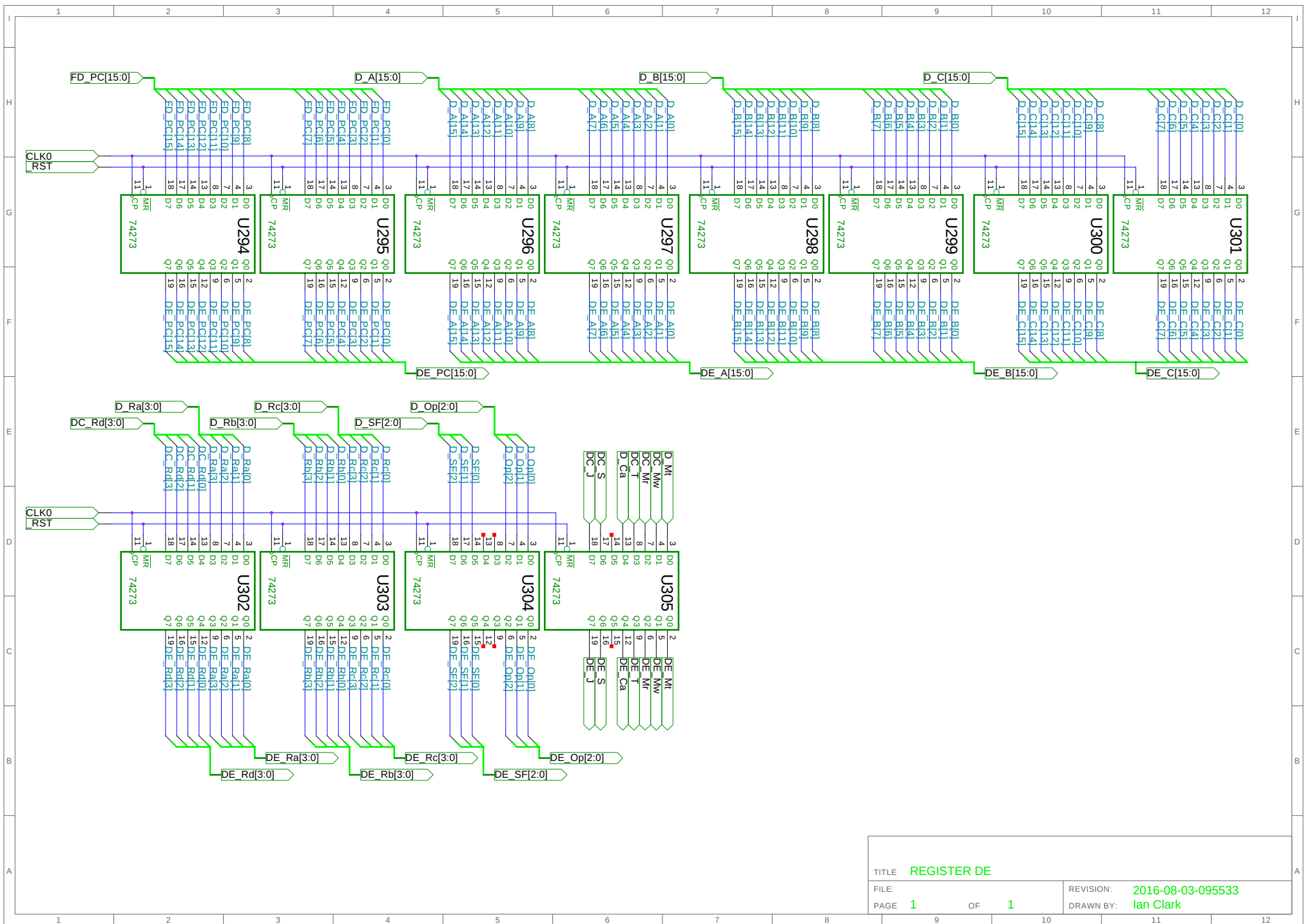
- 0 Zero Flag
- 1 Overflow Flag
- 2 Carry Flag
- 3 Sign Flag

Conditionals

- 0 == ZF==1
- 1 != ZF==0
- 2 signed > ZF==0 and SF==OF
- 3 signed >= SF==OF
- 4 signed < SFI=OF
- 5 signed <= ZF==1 or SFI=OF
- 6 unsigned < CF==0
- 7 unsigned <= CF==ZF

TITLE MEMORY SKIP			REVISION: 2016-07-01-113008		
FILE:			DRAWN BY: Ian Clark		
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TITLE REGISTER DE

FILE:

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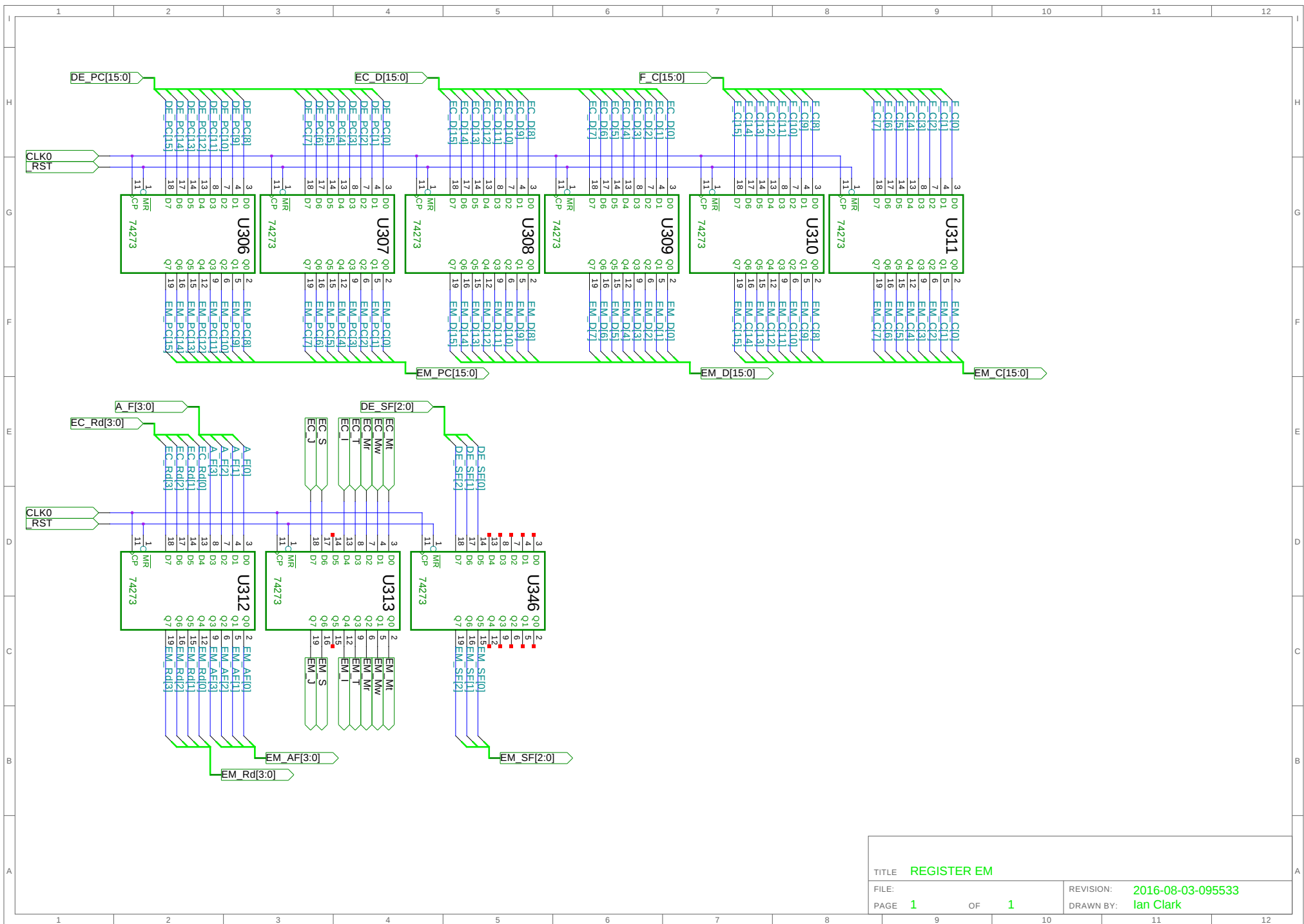
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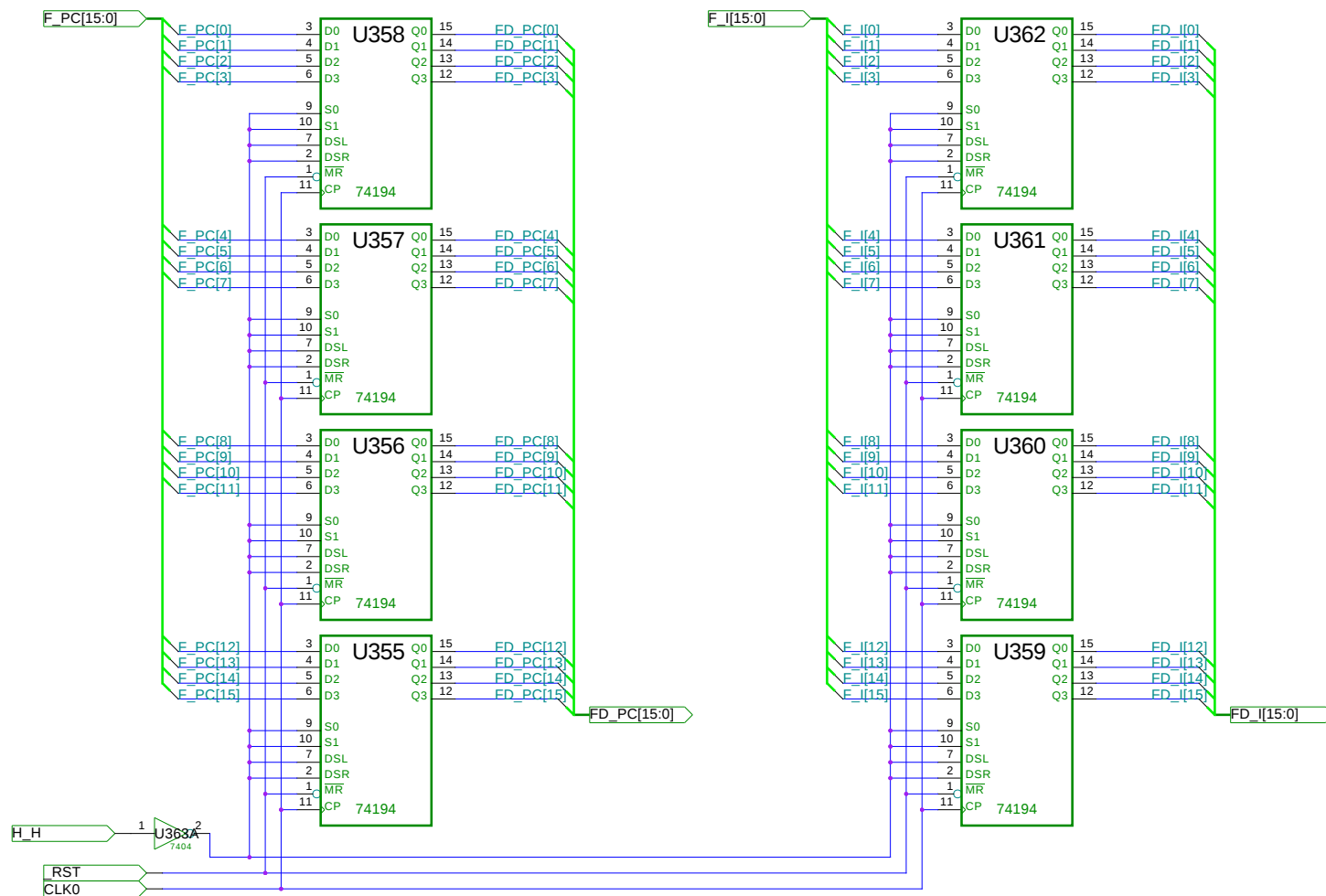
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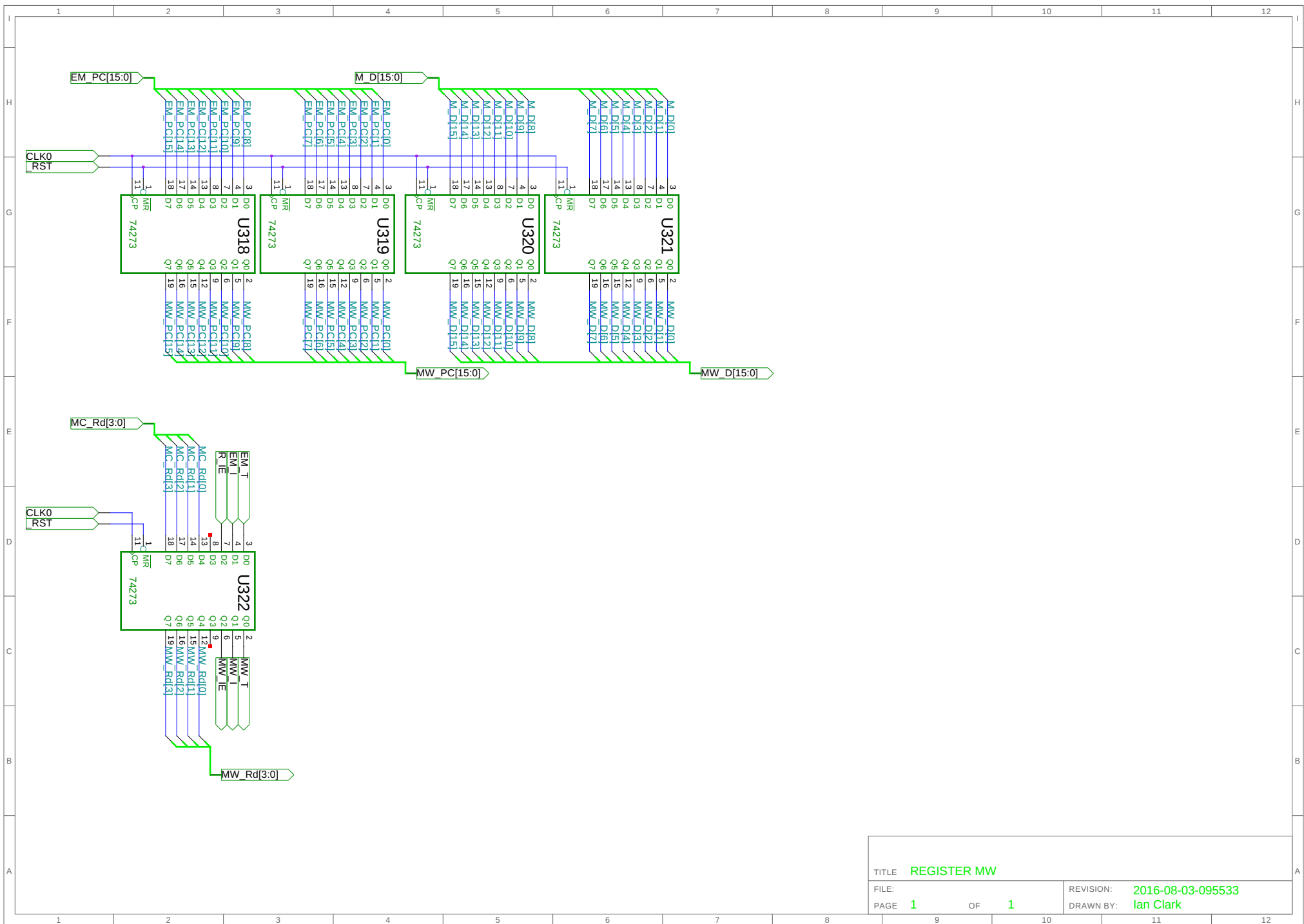
Ian Clark



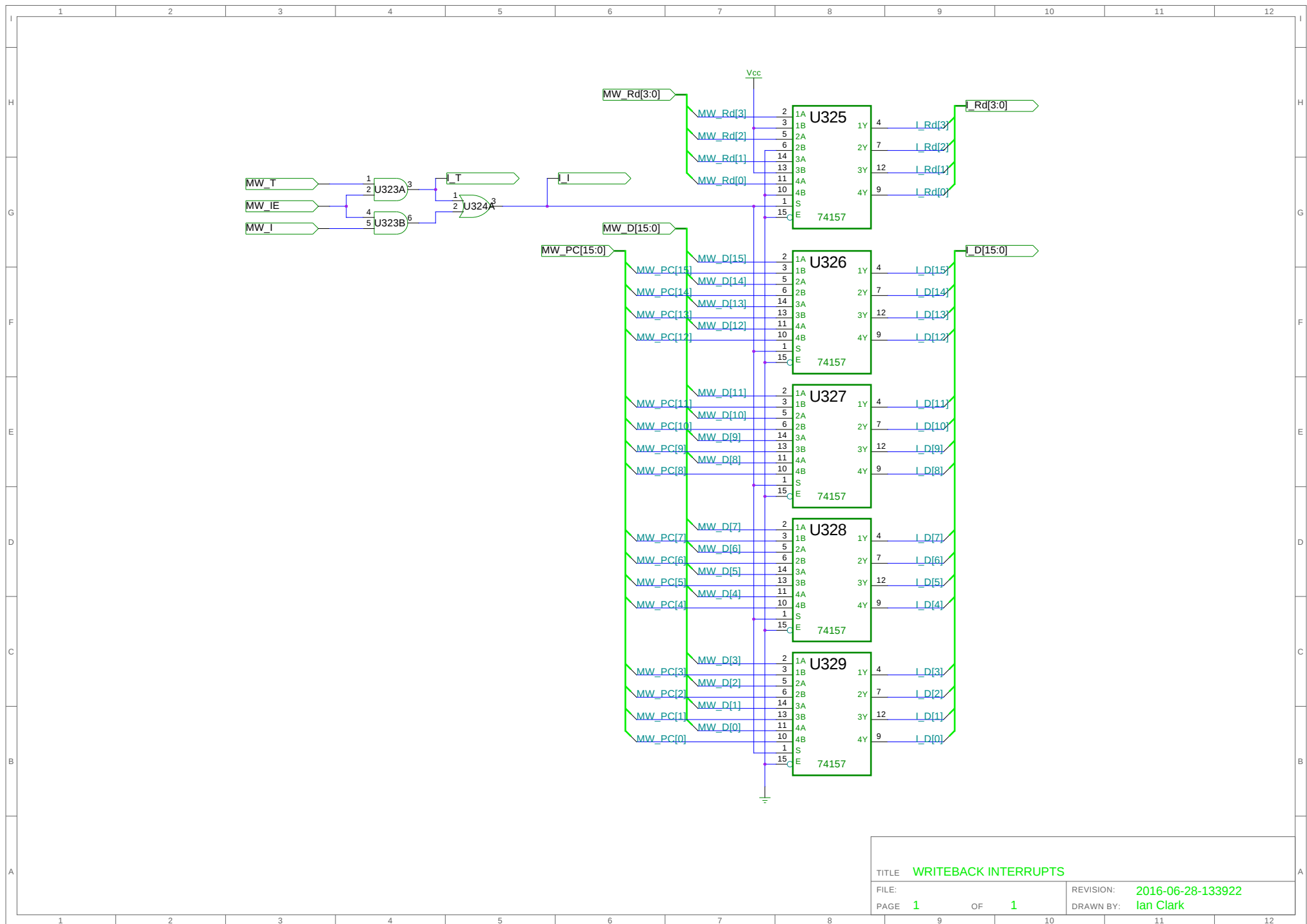
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FILE:			DRAWN BY: Ian Clark		
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TITLE REGISTER MW			REVISION: 2016-08-03-095533		
FILE:			DRAWN BY: Ian Clark		
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TITLE WRITEBACK INTERRUPTS					
FILE:			REVISION: 2016-06-28-133922		
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