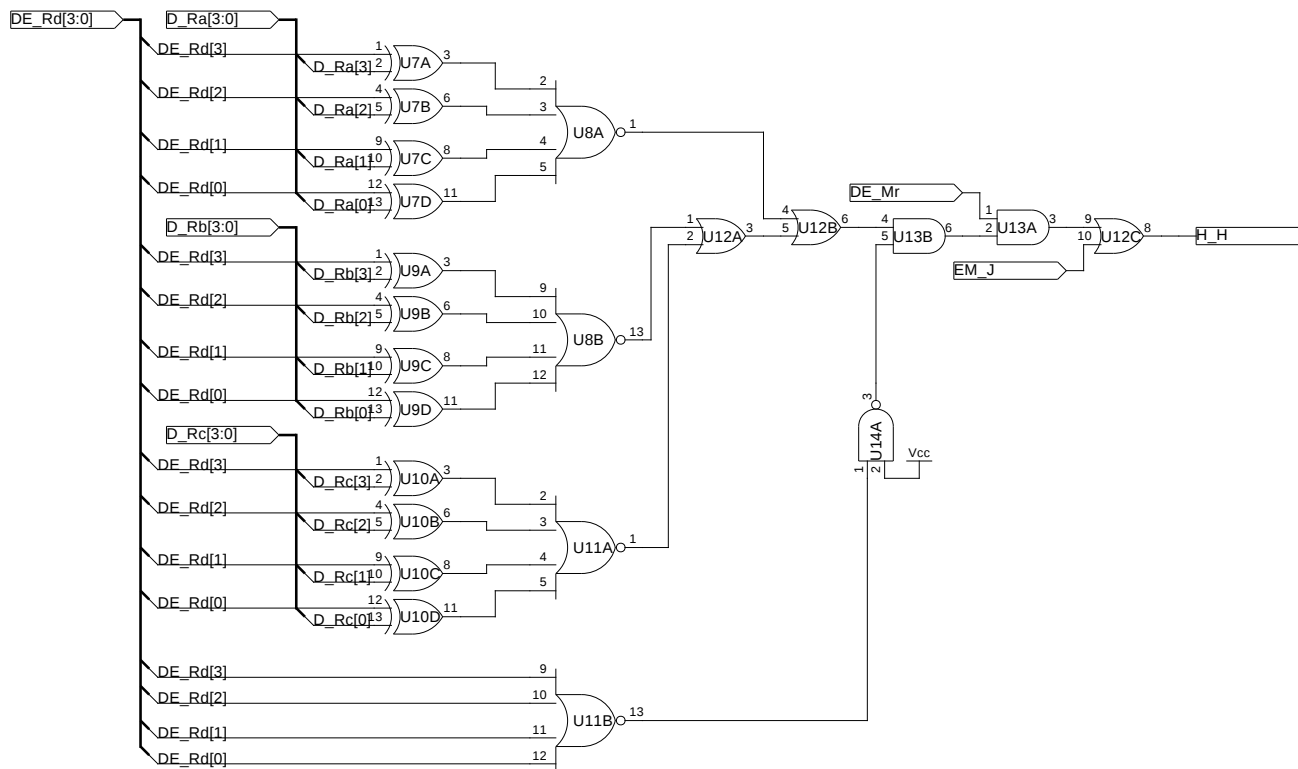


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FILE:		REVISION:	2016-06-28-133922
PAGE 1	OF 1	DRAWN BY:	Ian Clark



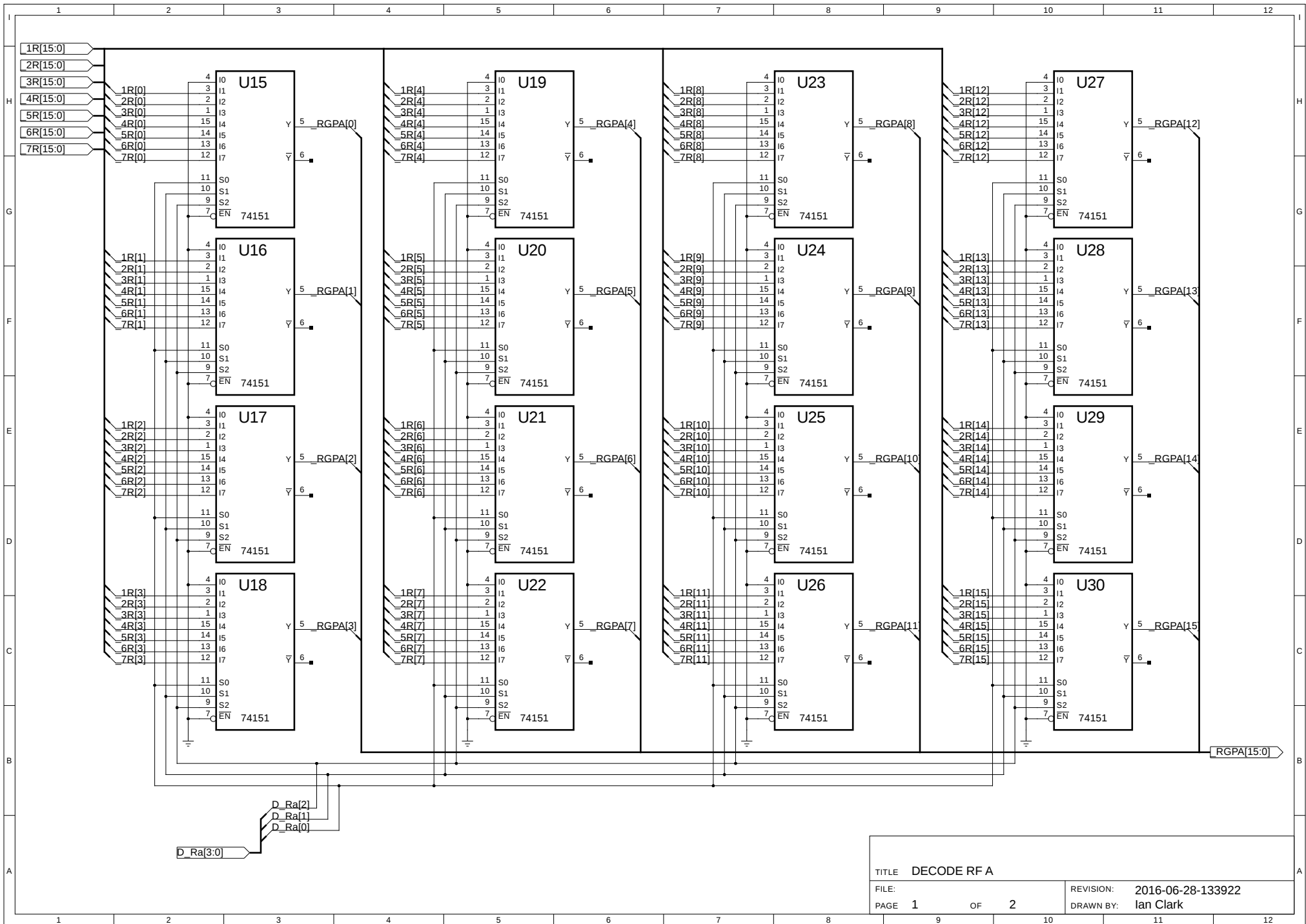
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PAGE 1 OF 1

REVISION: 2016-06-28-133922

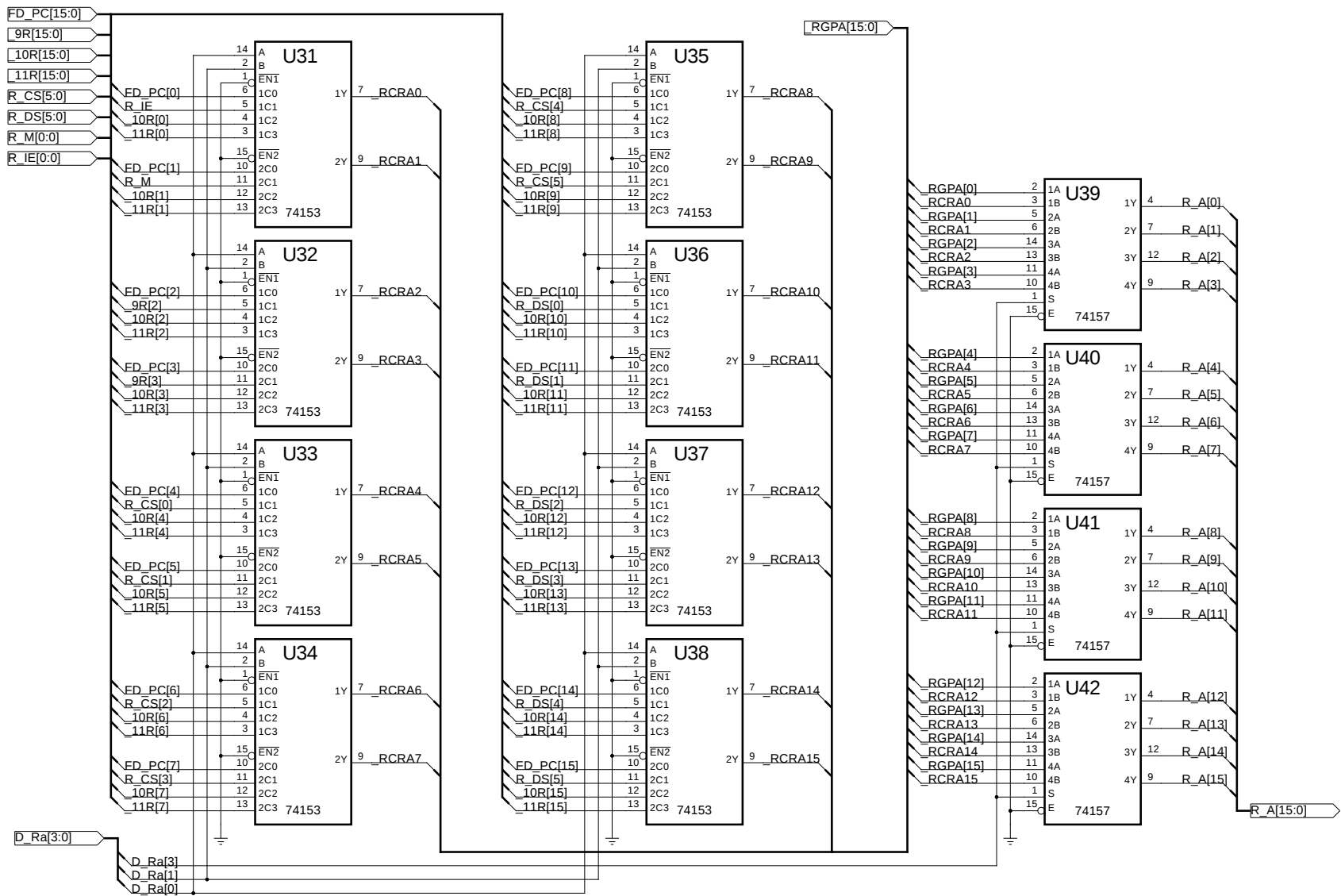
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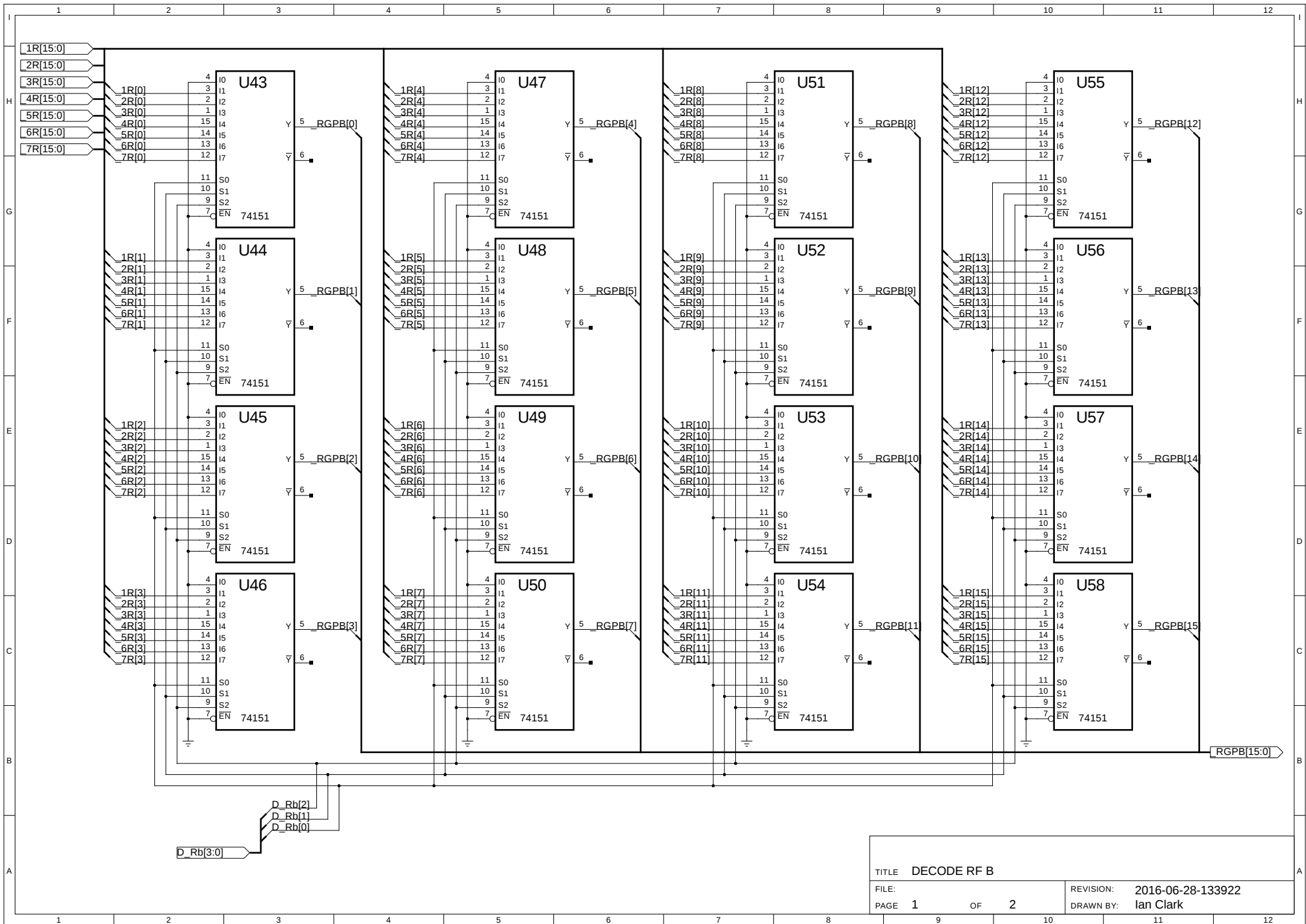
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PAGE 1 OF 2

REVISION: 2016-06-28-133922
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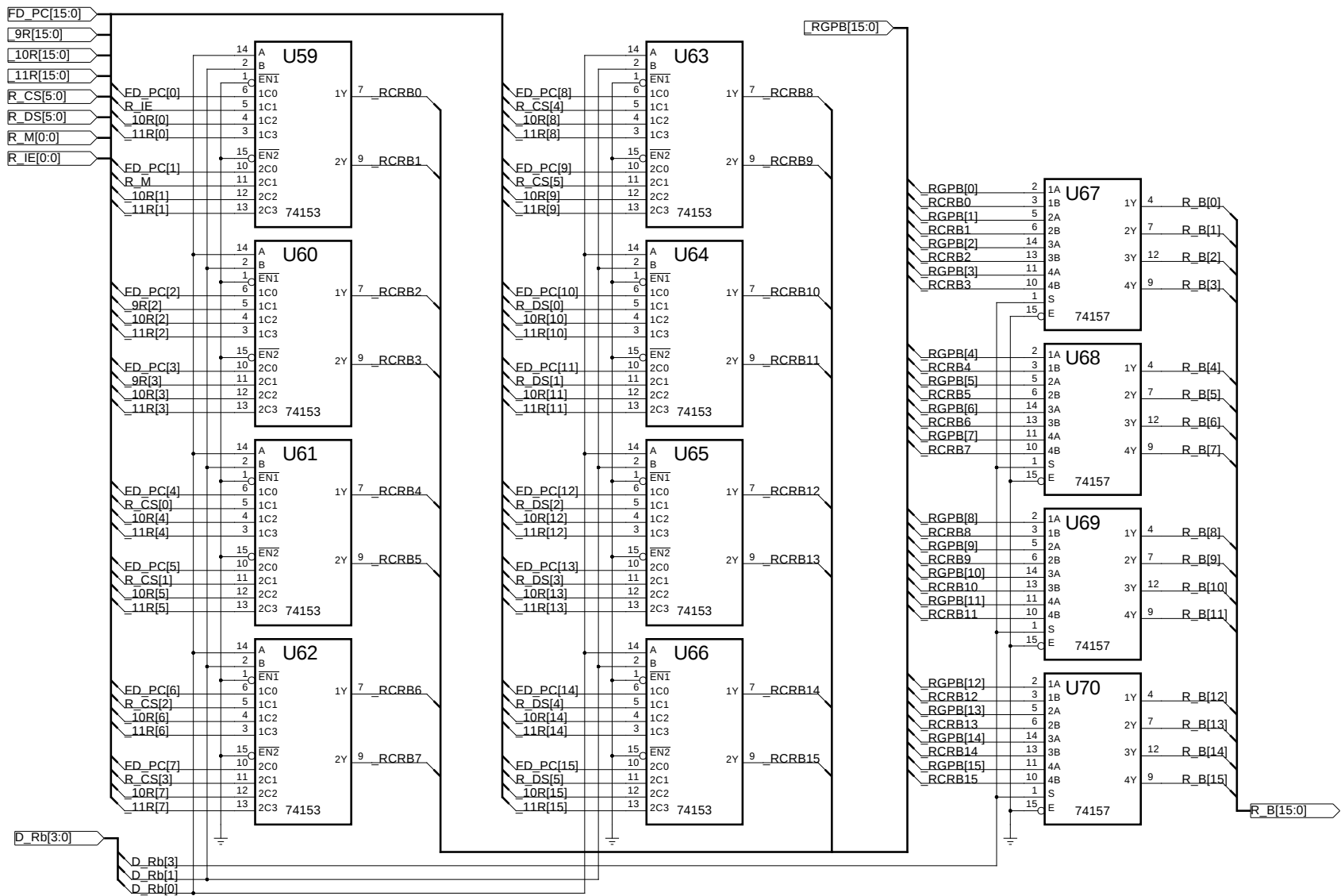
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2		2				Ian Clark	



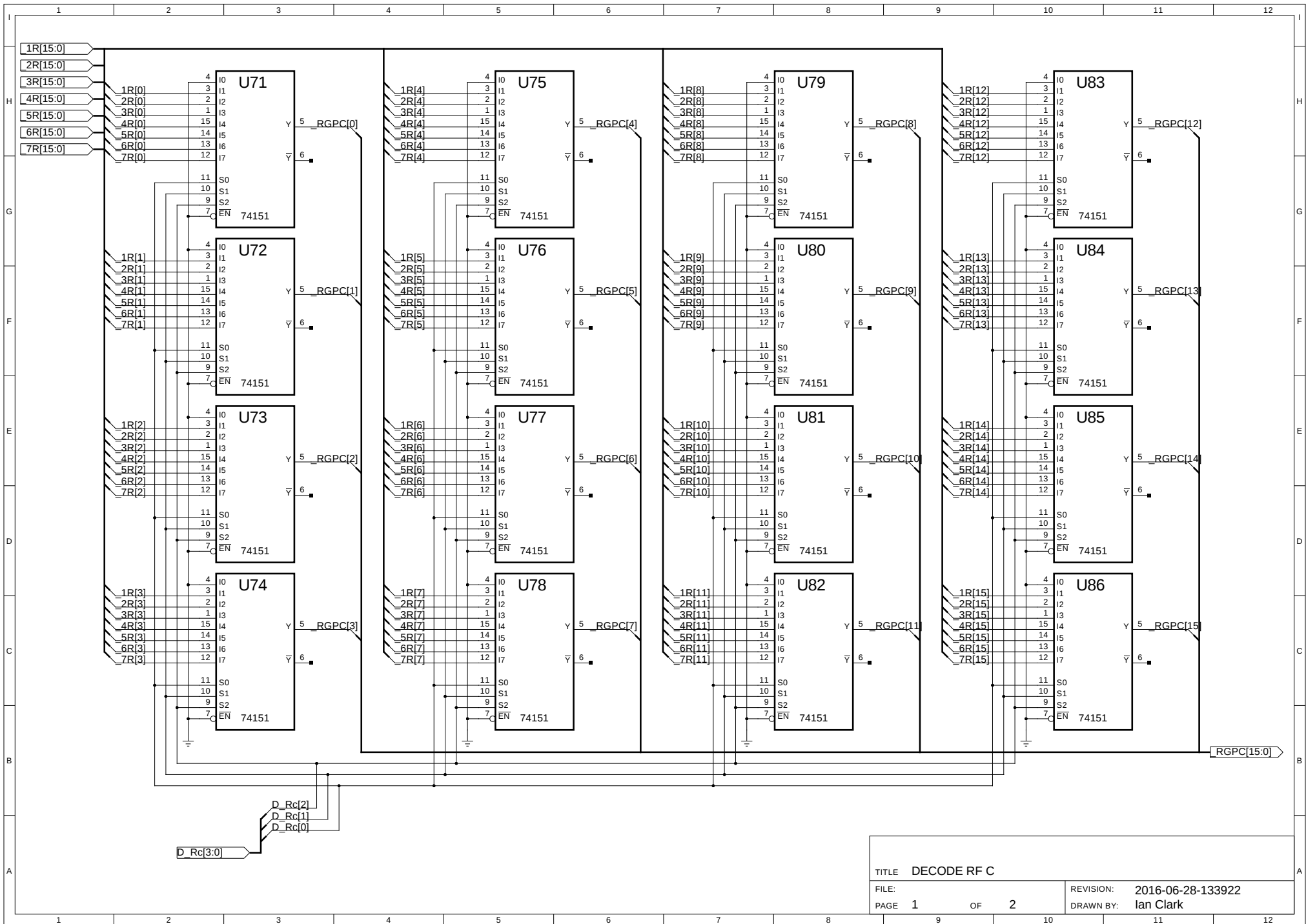
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PAGE 1 OF 2

REVISION: 2016-06-28-133922
DRAWN BY: Ian Clark



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FILE:	REVISION:	2016-06-28-133922	
PAGE 2	OF 2	DRAWN BY:	Ian Clark



TITLE DECODE RF C

FILE:

PAGE

1

OF

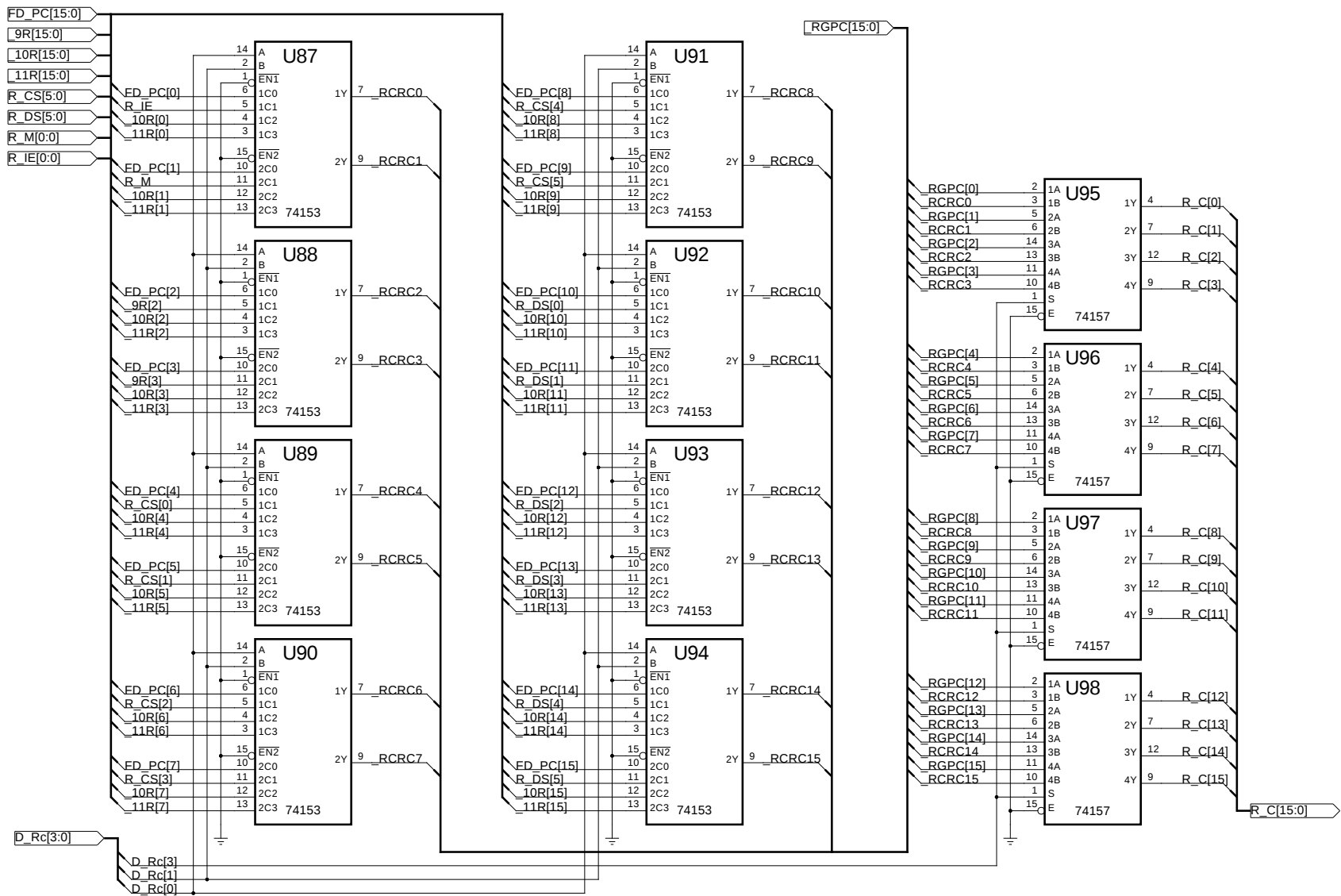
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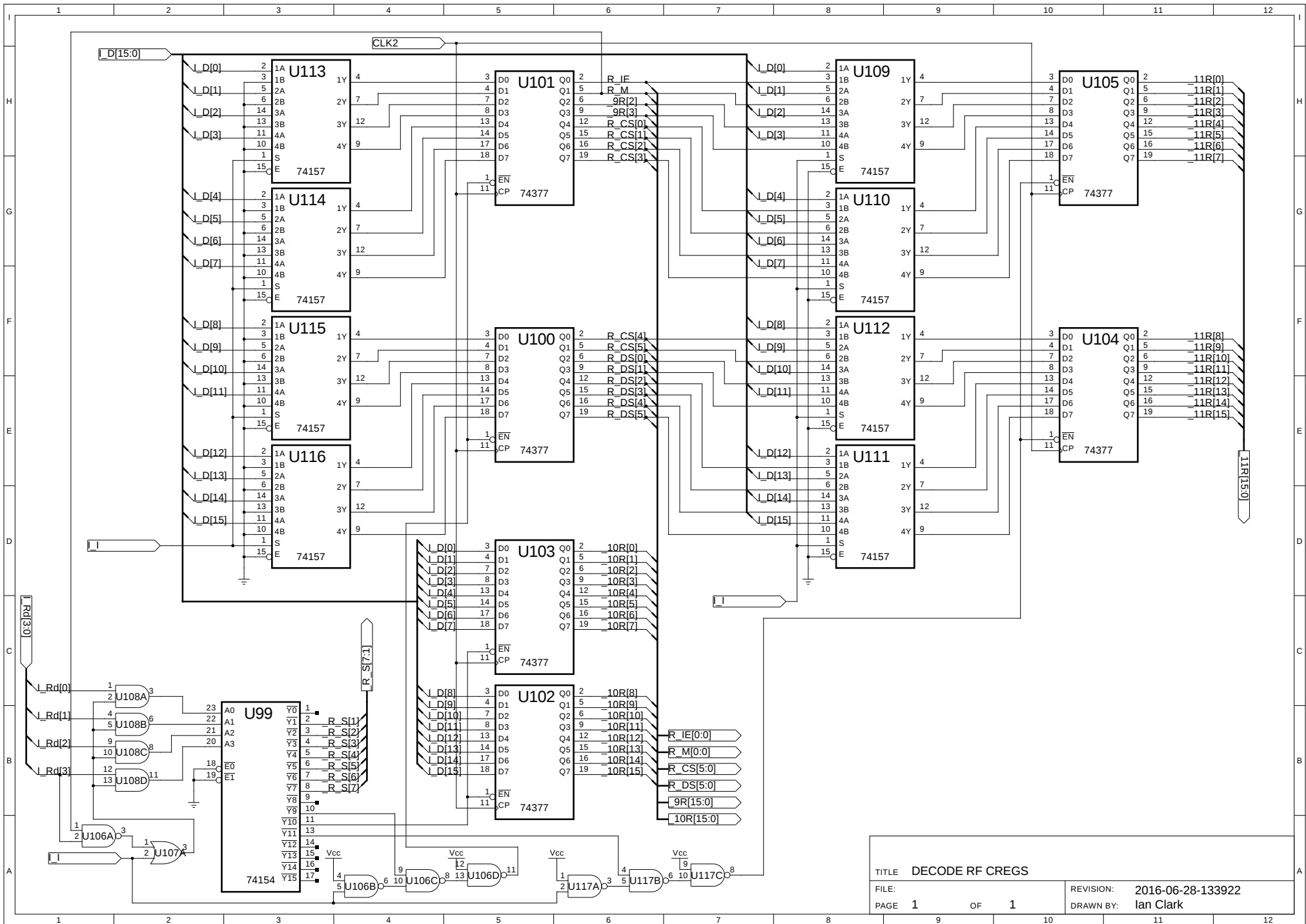
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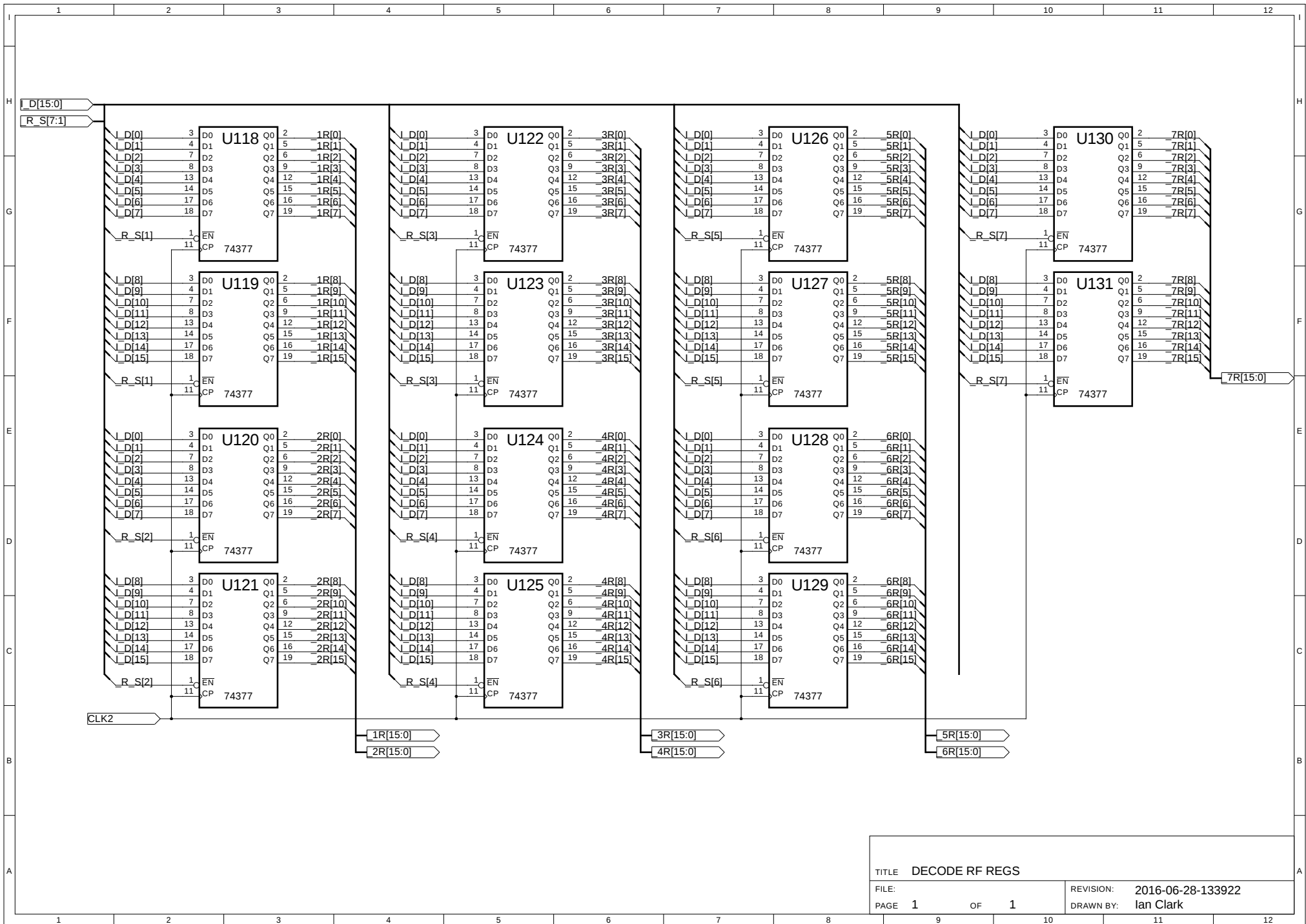
Ian Clark



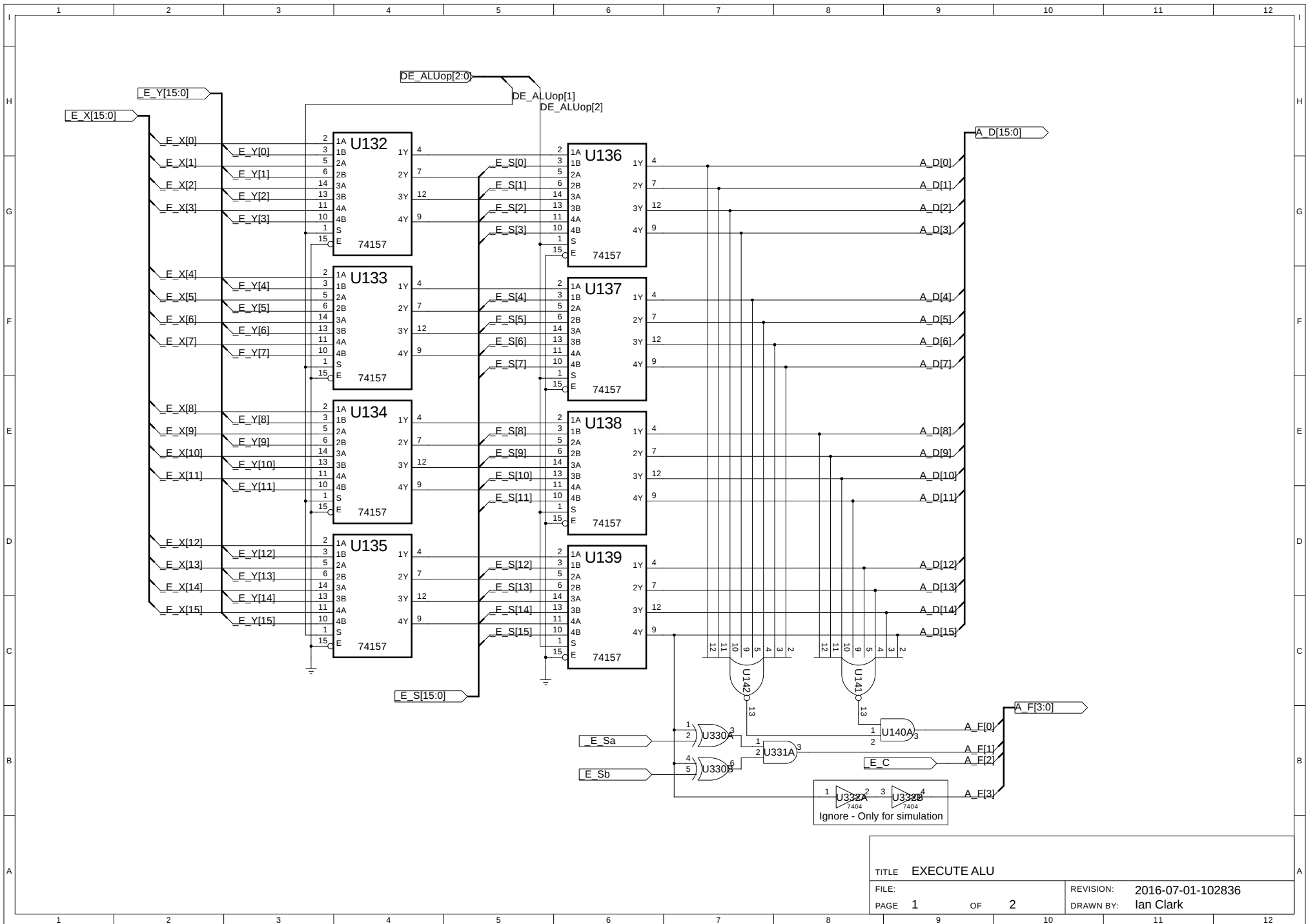
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PAGE	2	OF	2
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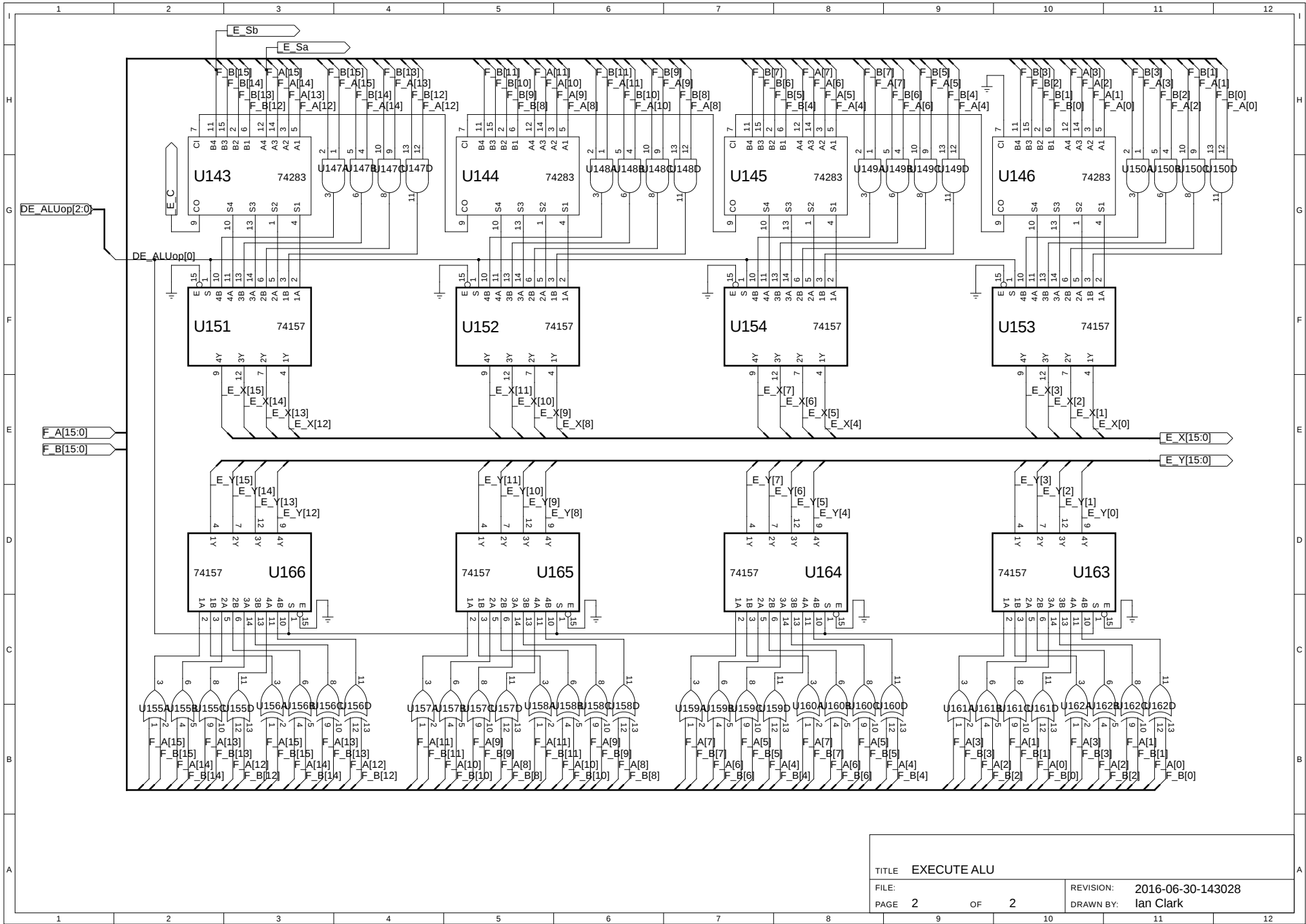


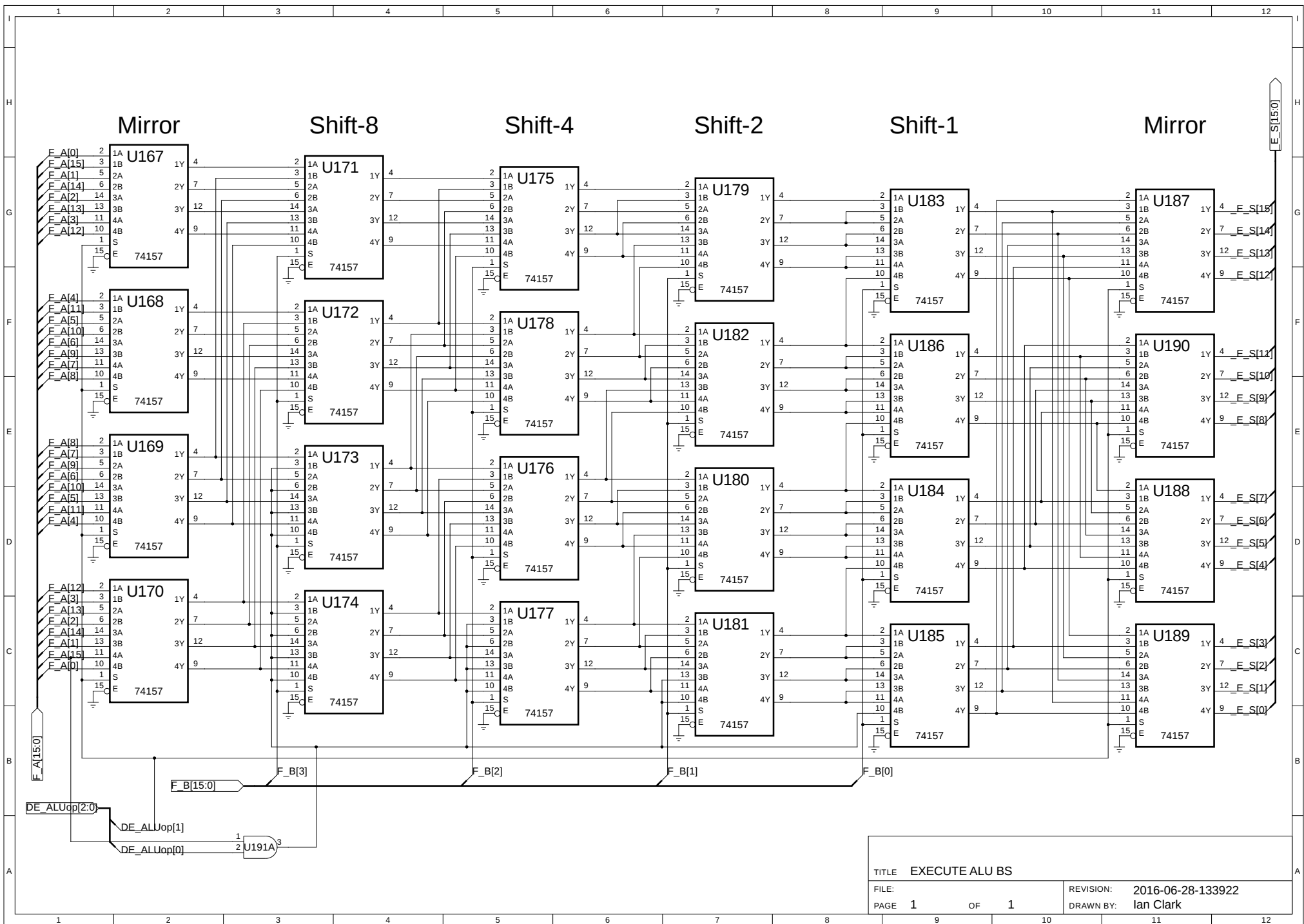
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FILE:			DRAWN BY: Ian Clark		
PAGE 1	OF	1			

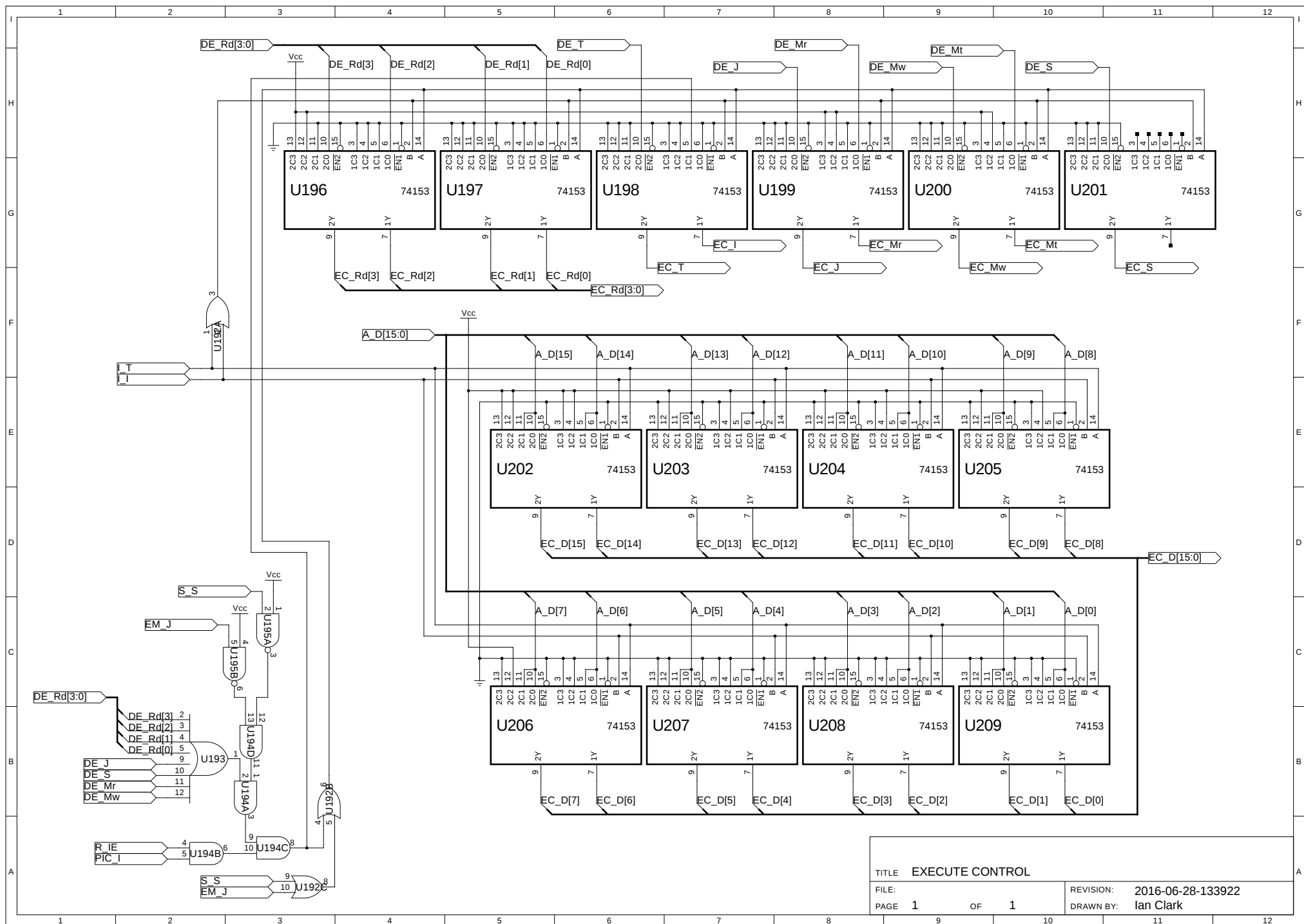


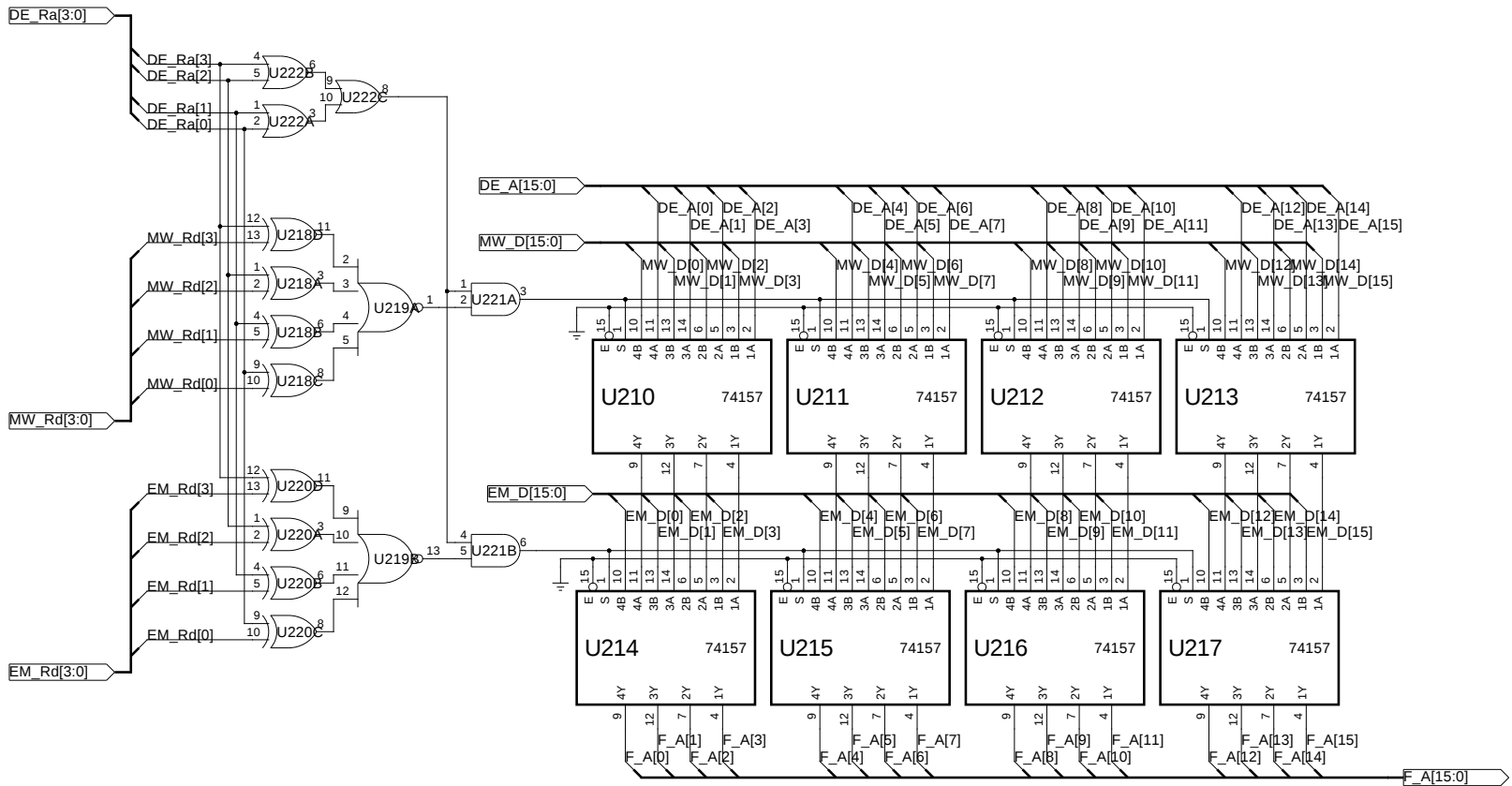
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FILE:	PAGE 1		OF 1	DRAWN BY: Ian Clark	



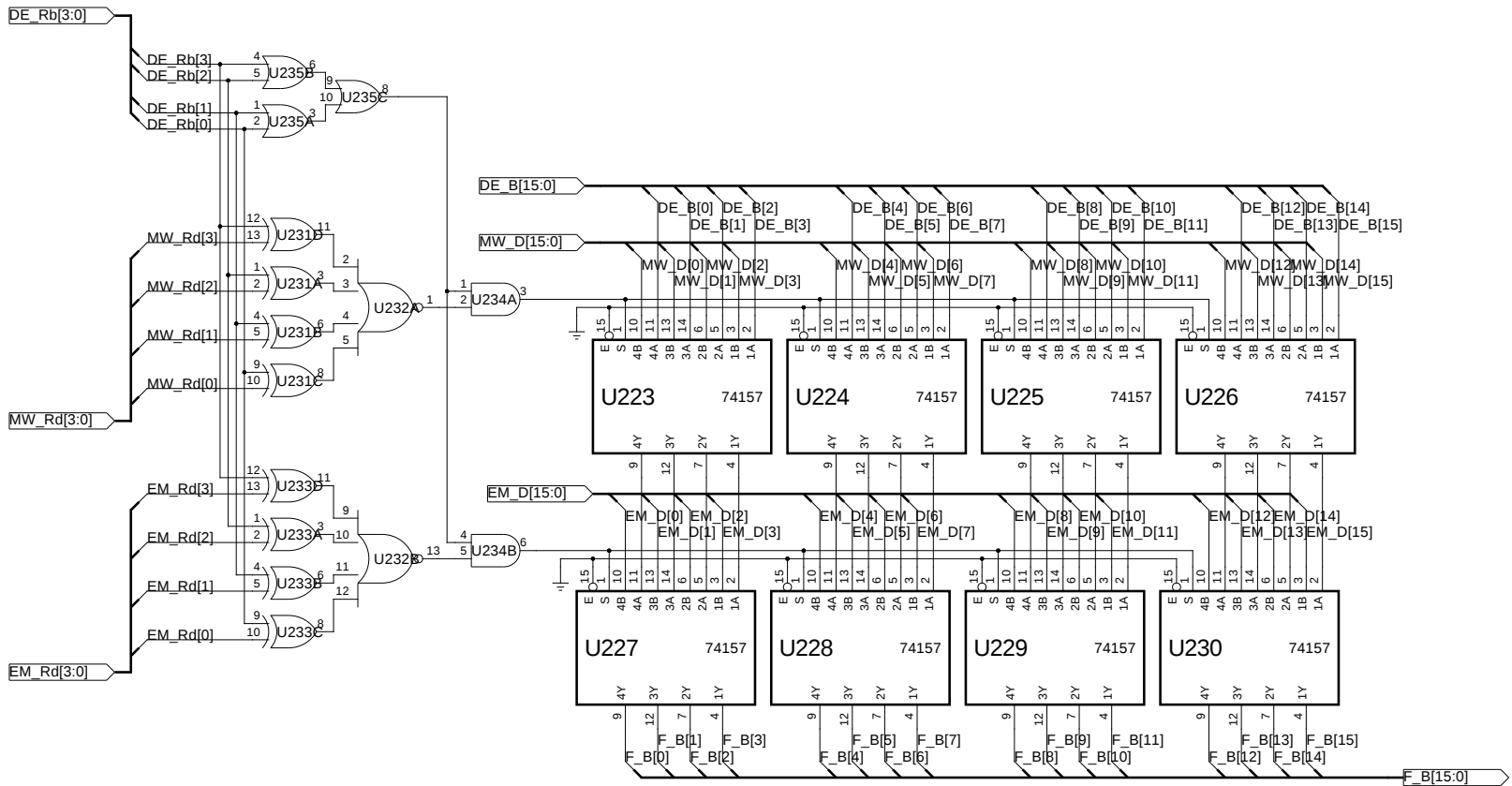




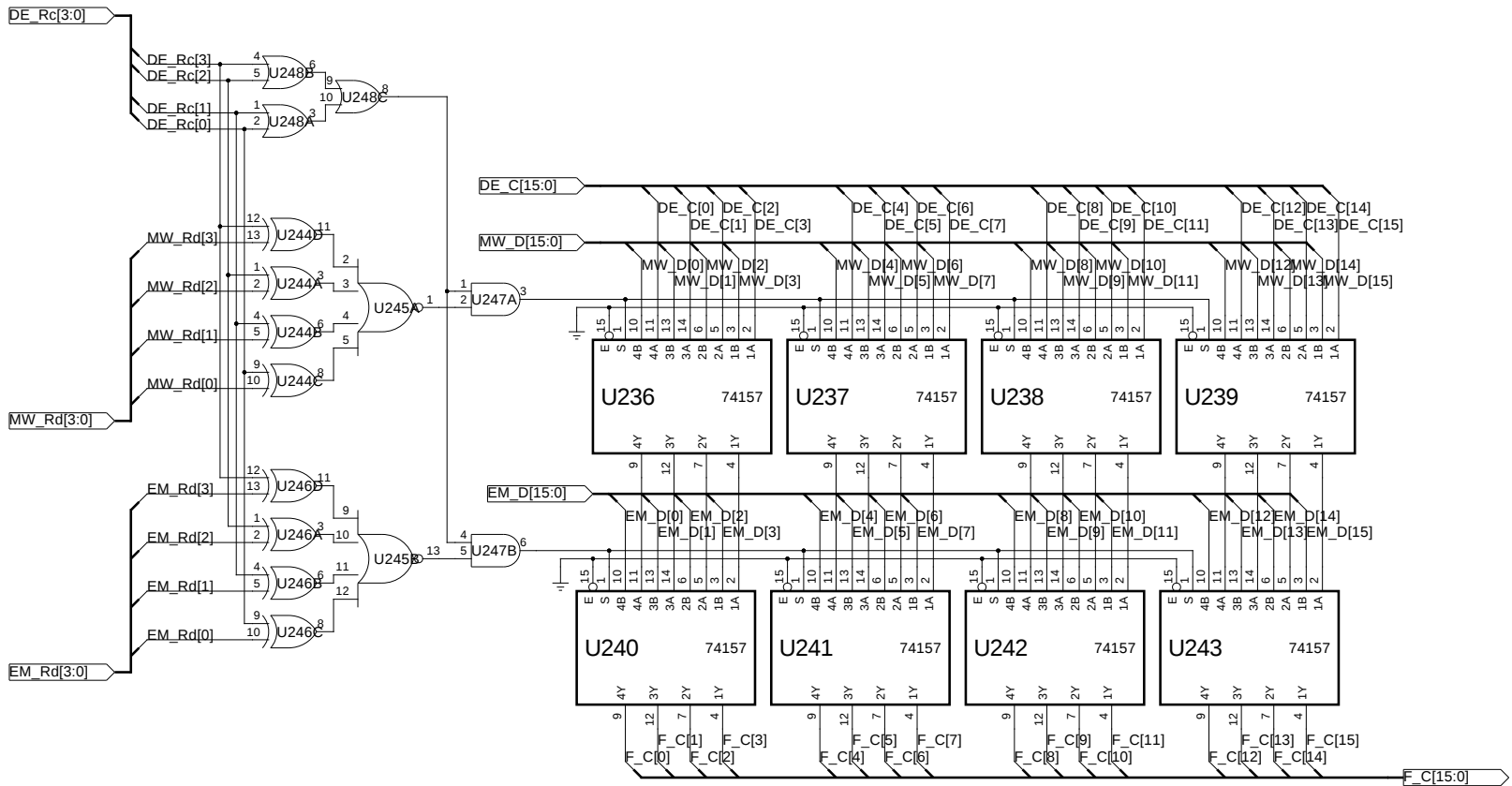


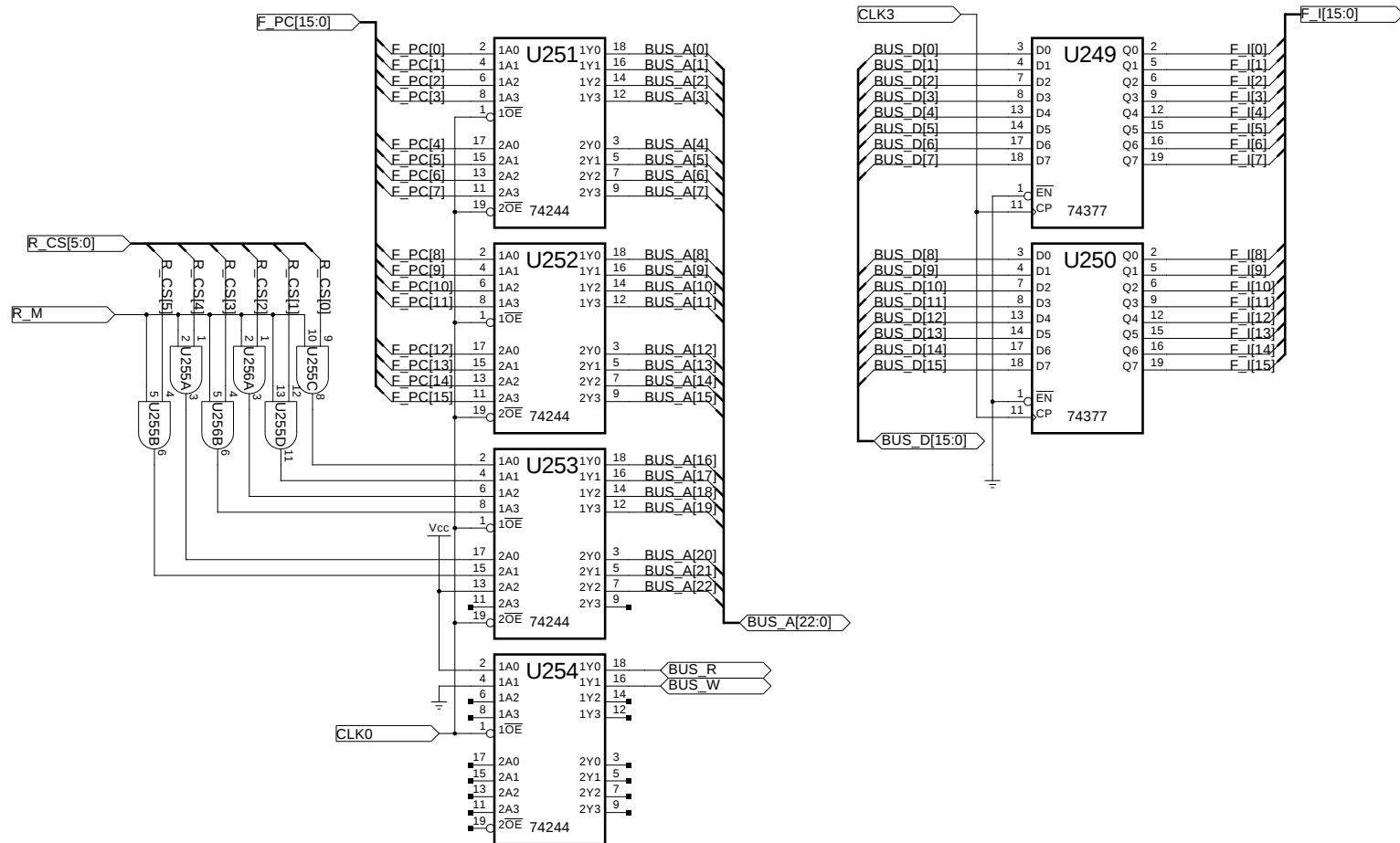


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FILE:			DRAWN BY: Ian Clark		
PAGE 1	OF 3				



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FILE:			REVISION: 2016-06-28-133922		
PAGE 2			DRAWN BY: Ian Clark		
OF 3					

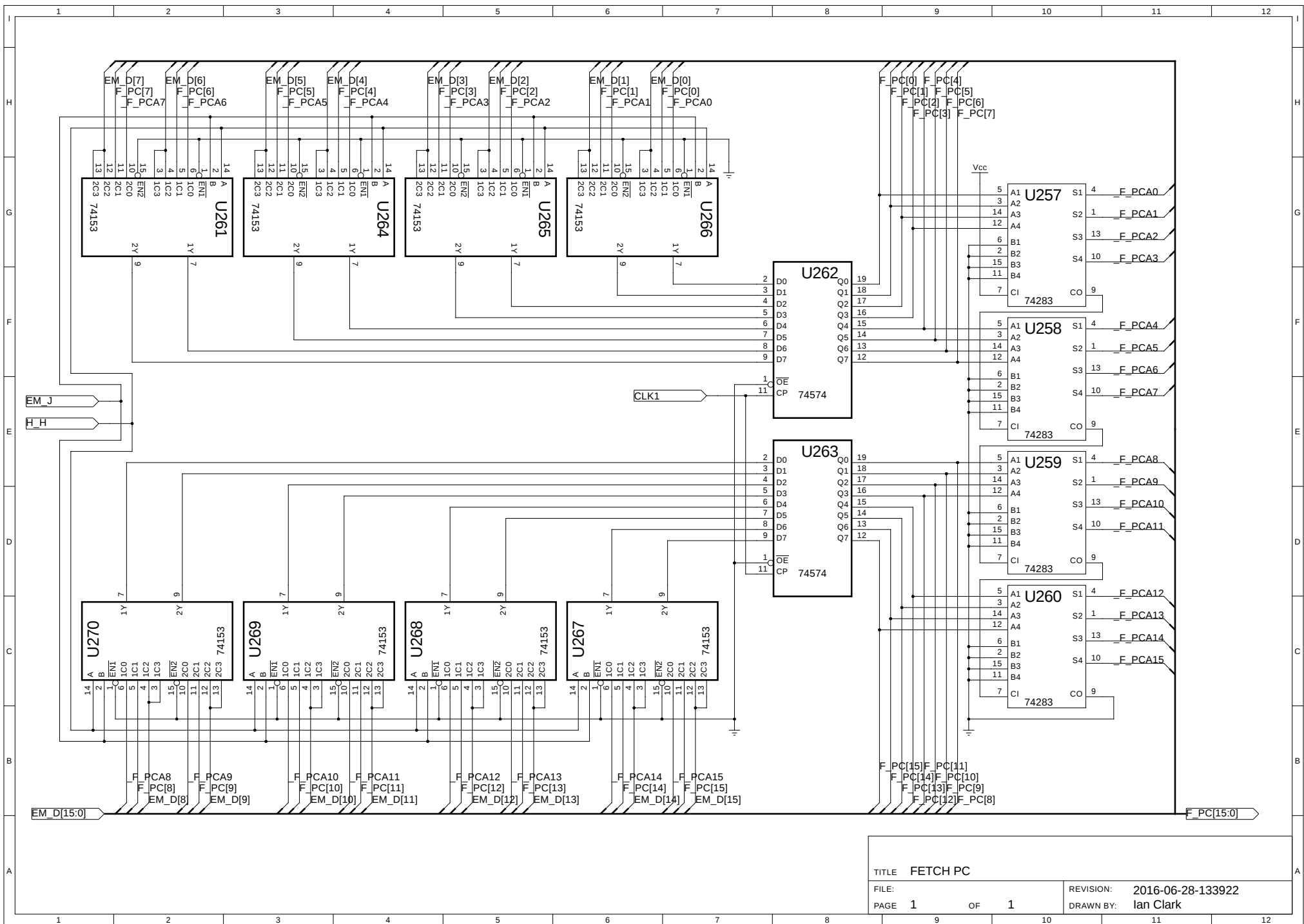




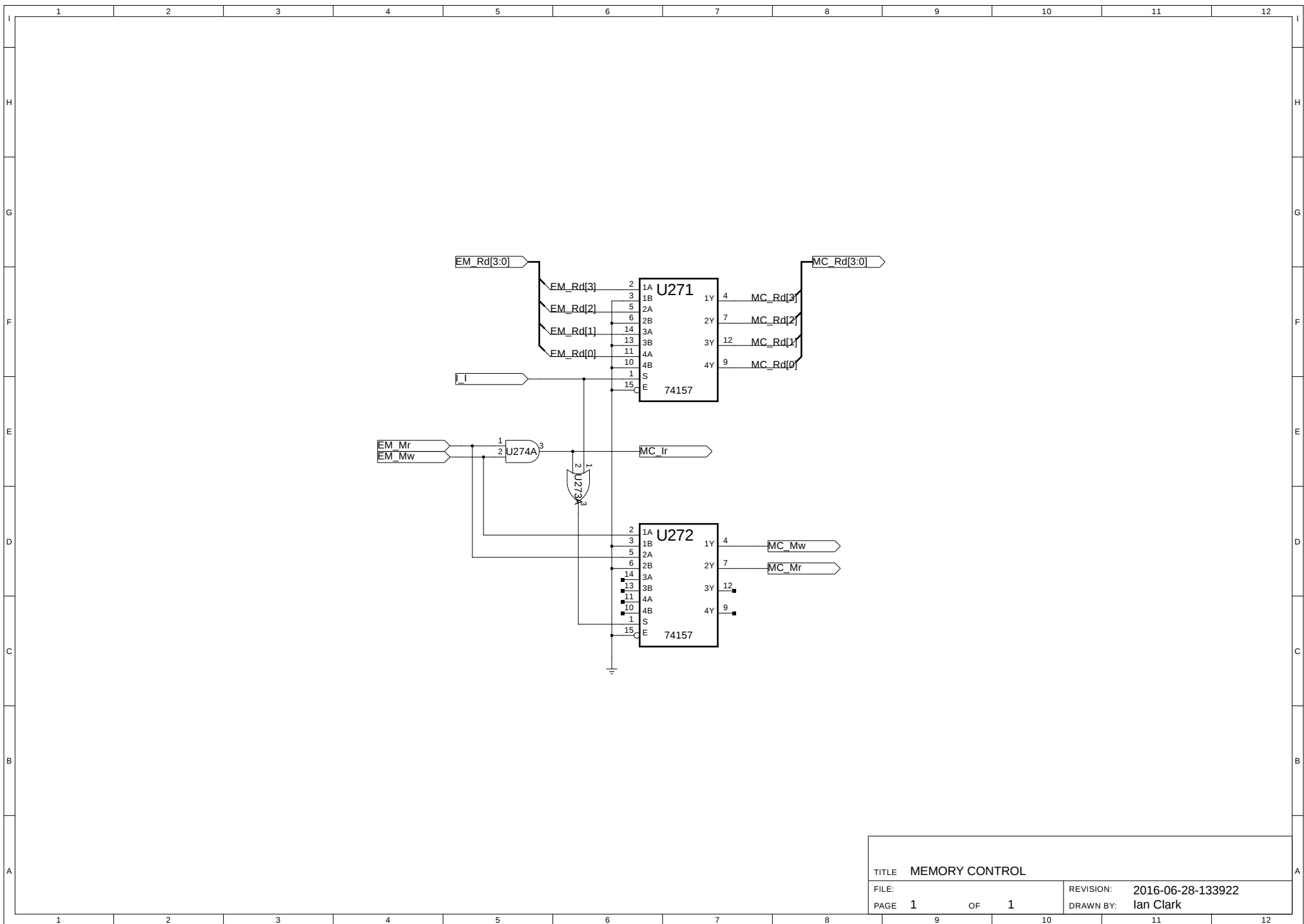
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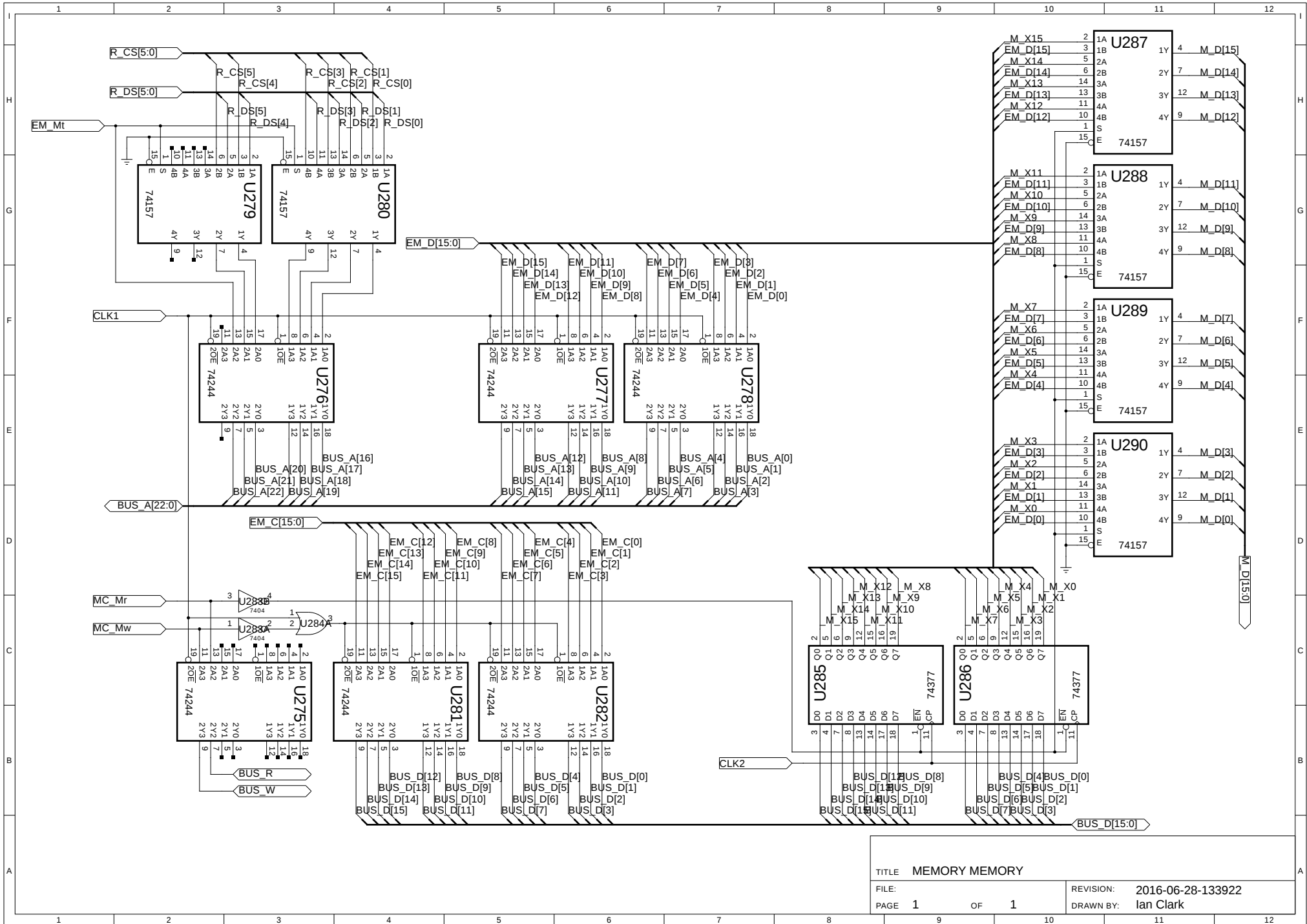
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PAGE 1 OF 1

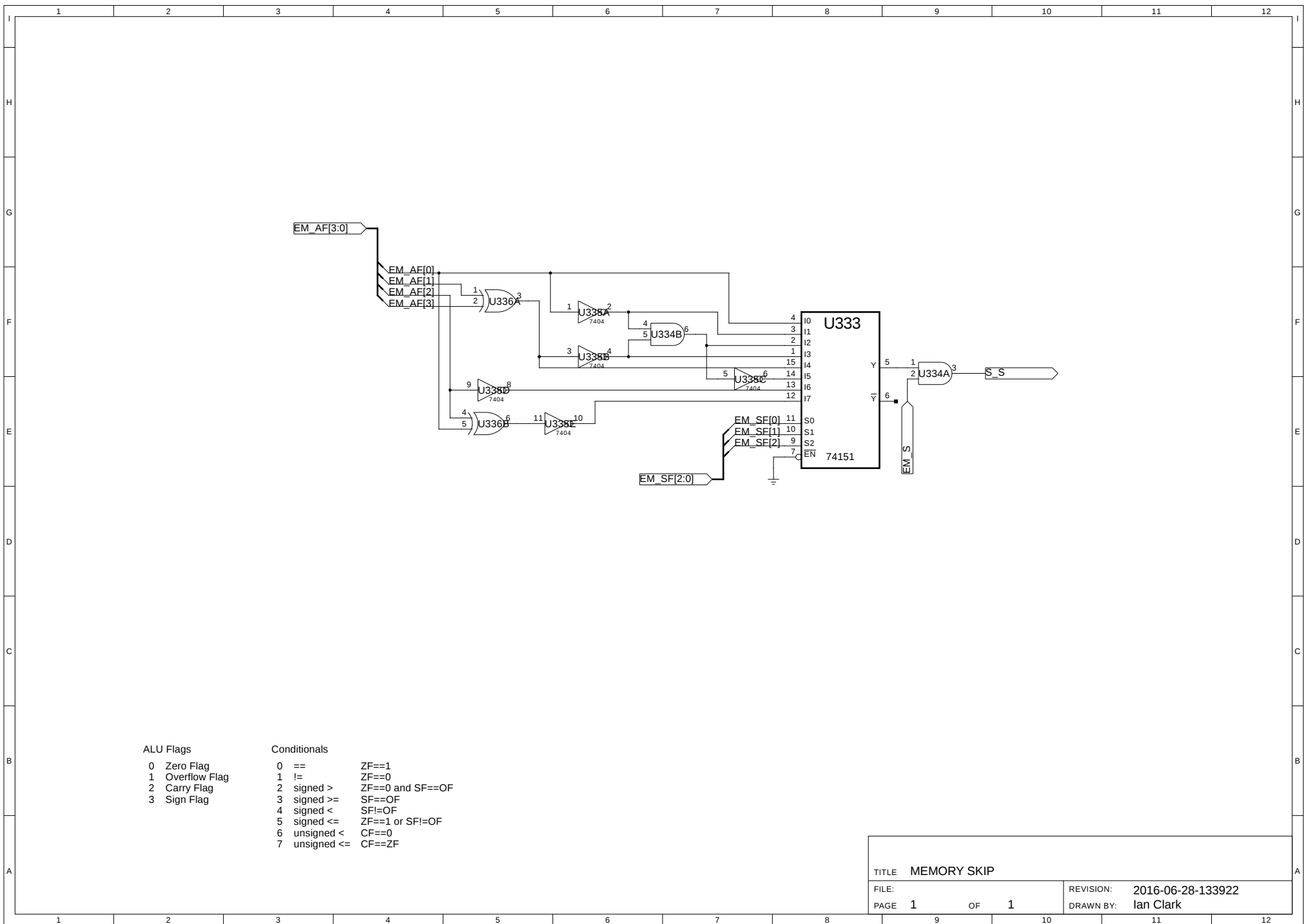
REVISION: 2016-06-28-133922
DRAWN BY: Ian Clark



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FILE:		DRAWN BY: Ian Clark		
PAGE 1	OF 1			







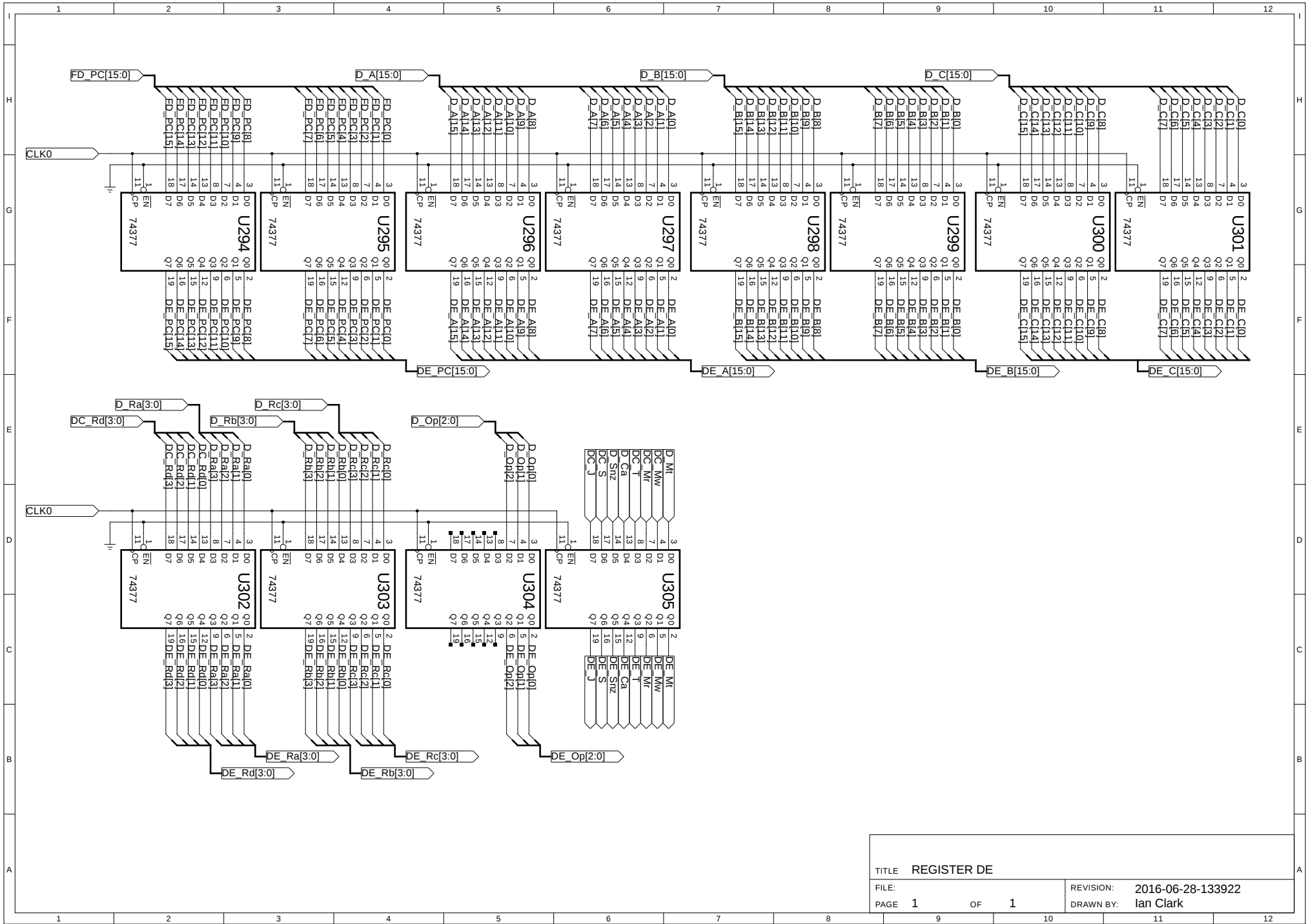
ALU Flags

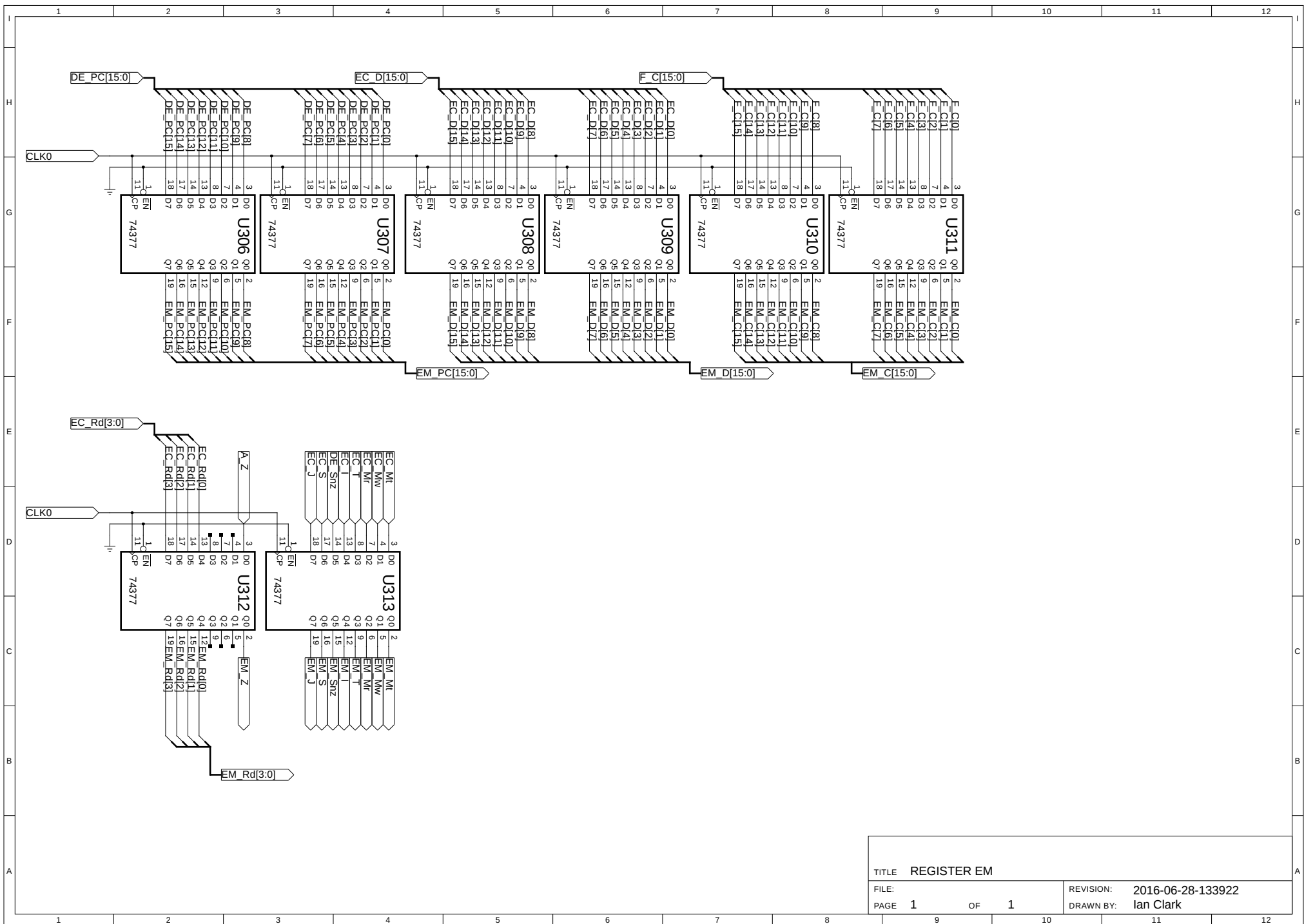
- 0 Zero Flag
- 1 Overflow Flag
- 2 Carry Flag
- 3 Sign Flag

Conditionals

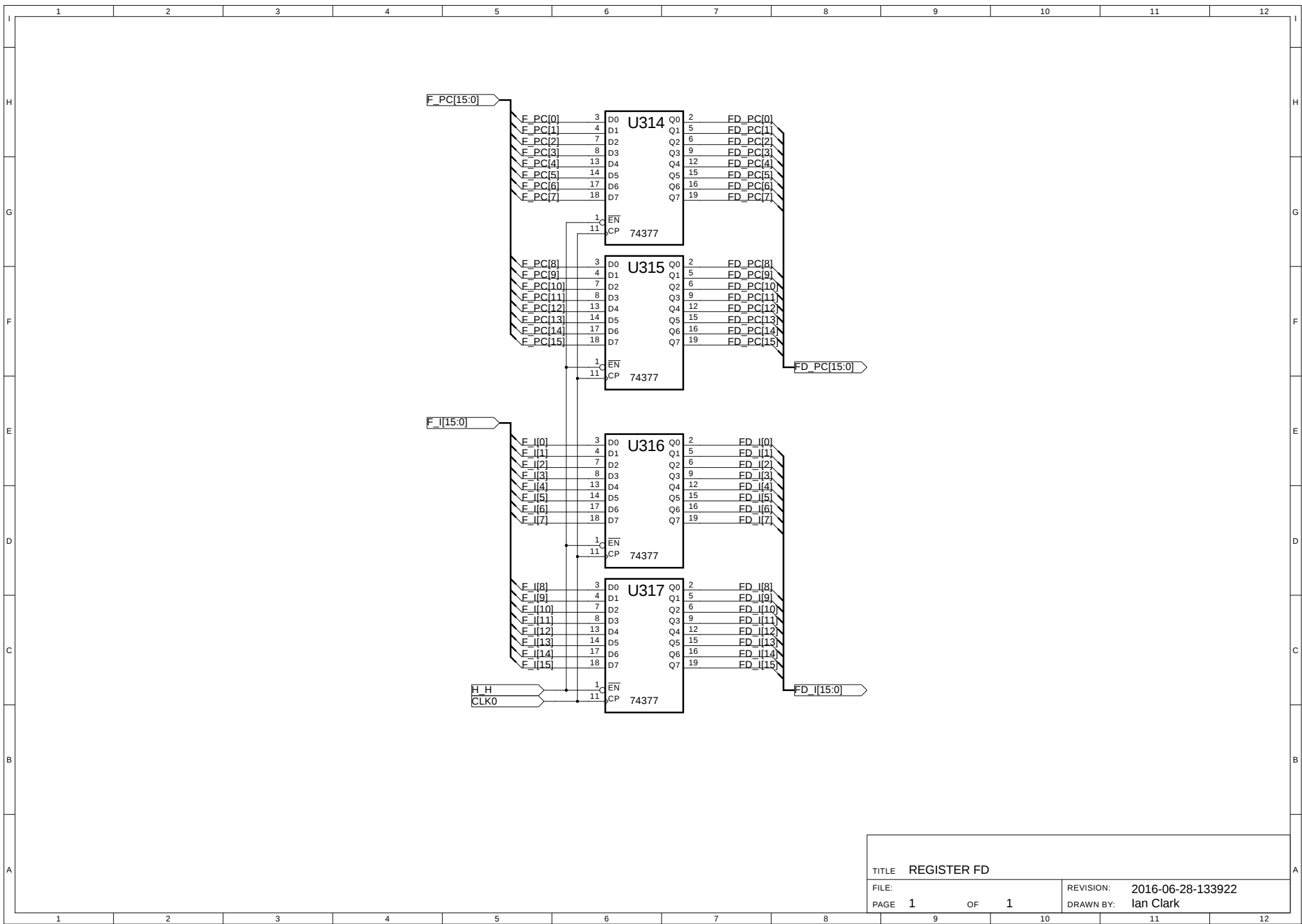
- 0 == ZF==1
- 1 != ZF==0
- 2 signed > ZF==0 and SF==OF
- 3 signed >= SF==OF
- 4 signed < SFI=OF
- 5 signed <= ZF==1 or SFI=OF
- 6 unsigned < CF==0
- 7 unsigned <= CF==ZF

TITLE MEMORY SKIP			REVISION: 2016-06-28-133922		
FILE:	PAGE 1	OF 1	DRAWN BY:	Ian Clark	





TITLE REGISTER EM			REVISION: 2016-06-28-133922		
PAGE 1	OF 1		DRAWN BY: Ian Clark		



TITLE REGISTER FD

FILE:

PAGE

1

OF

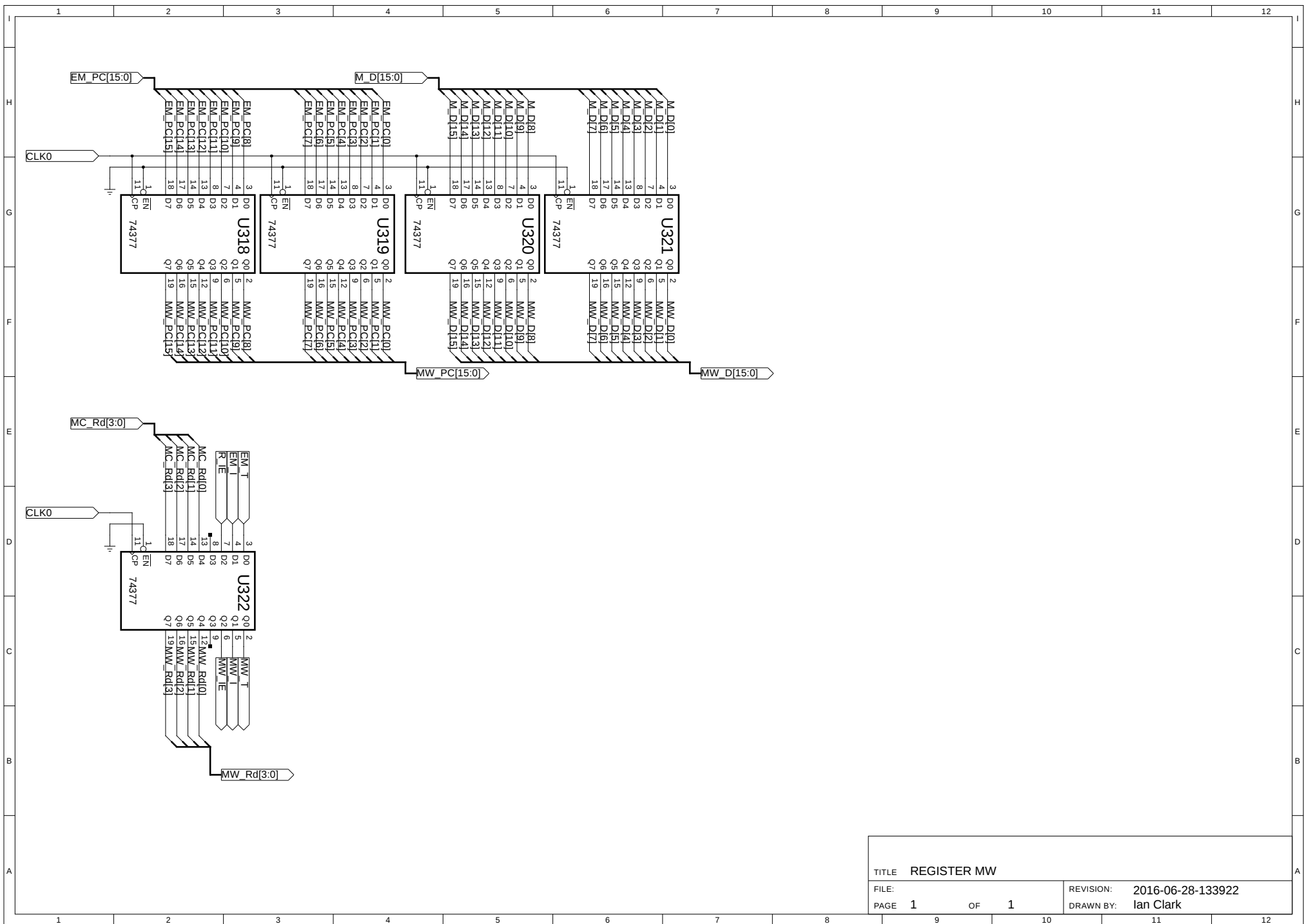
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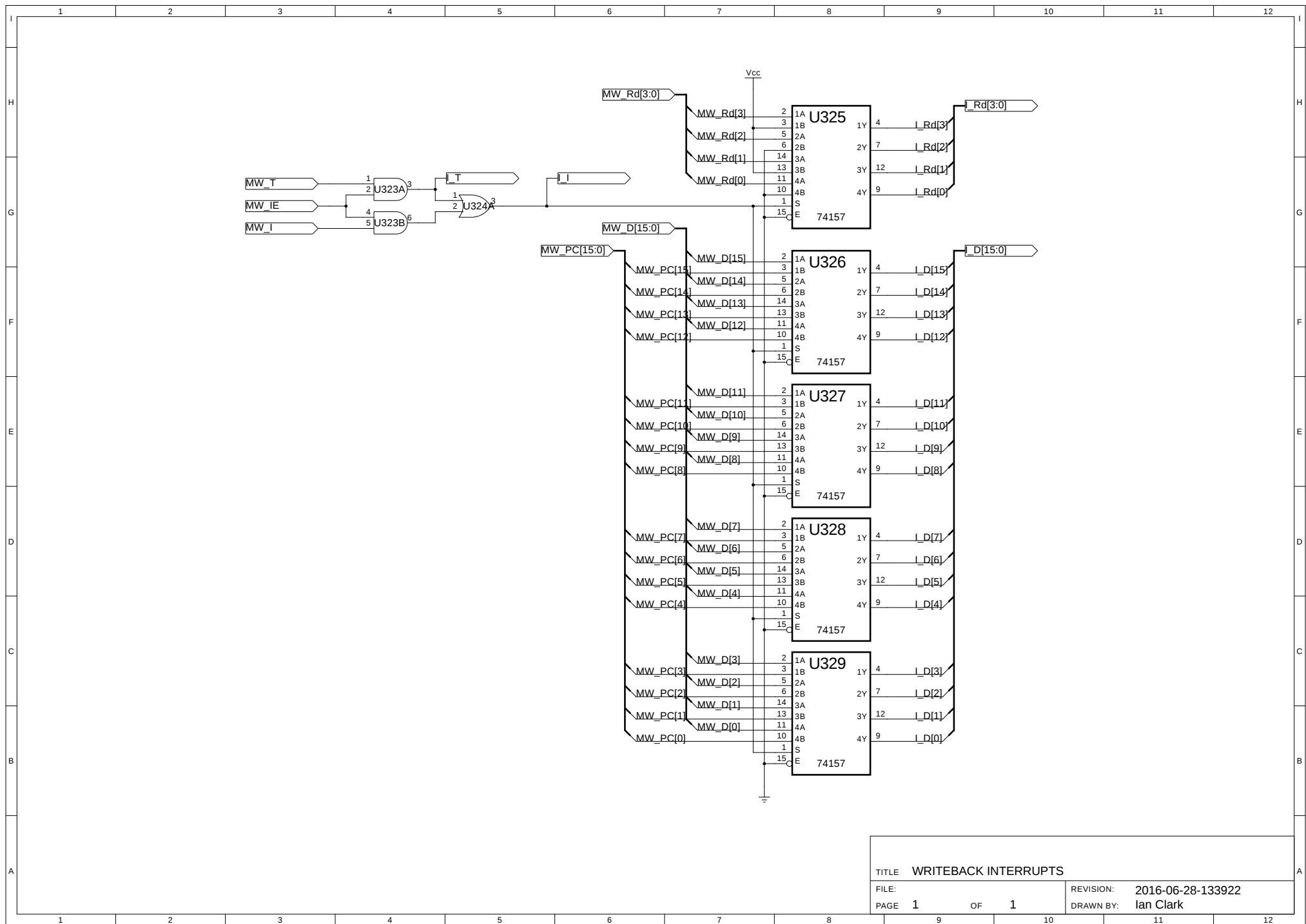
2016-06-28-133922

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Ian Clark



TITLE REGISTER MW			REVISION: 2016-06-28-133922		
FILE:	PAGE 1 OF 1		DRAWN BY: Ian Clark		



TITLE WRITEBACK INTERRUPTS			
FILE:	2016-06-28-133922	REVISION:	2016-06-28-133922
PAGE 1	OF 1	DRAWN BY:	Ian Clark