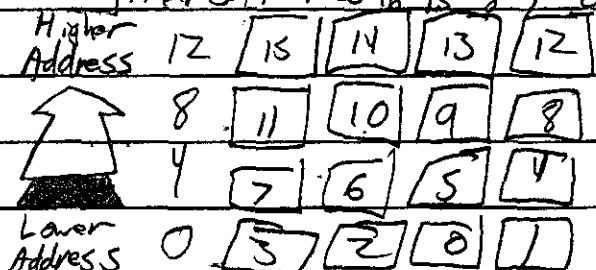
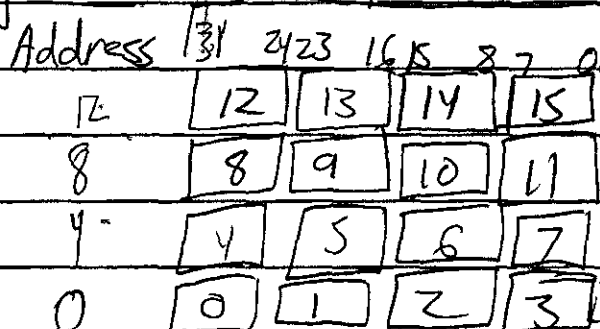
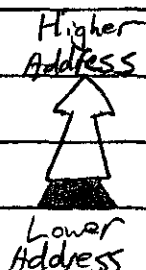


Big Endian / Little Endian

Big Endian: Word  $\rightarrow$  Bit#

7 Little-Endian: Word BTF#



1 word = 4 bytes


$$VETU \text{ time} = \text{Instruction Count} * CPI * \text{Clock cycle time}$$

Improvement =  $\frac{\sum \text{expu time}}{\text{expu time total}}$  divide by the improvement speed test are 1 unless stated.

(500)  $\frac{1}{1} =$  direct result - by 1 for improvement.

32, 1 11 11 5 11

2.471

When finding average CPU time the percentage is equal to IC cycles were equal to CPI and CCT was all 1. Found the CPU time and added together

2.47.2

Improvement 2 Average PV time, unless stated otherwise.

Performance A  $\rightarrow$  Execution time B  $B|1.25, 1/A$

Performance B	Execution time A	A	1.00	X	B
---------------	------------------	---	------	---	---

TEEE

bias = 127

Sig	8 digits	23 digits
0	exponent	fraction