

# 1. Description

# 1.1. Project

Project Name	VCU_Son
Board Name	custom
Generated with:	STM32CubeMX 6.15.0
Date	08/07/2025

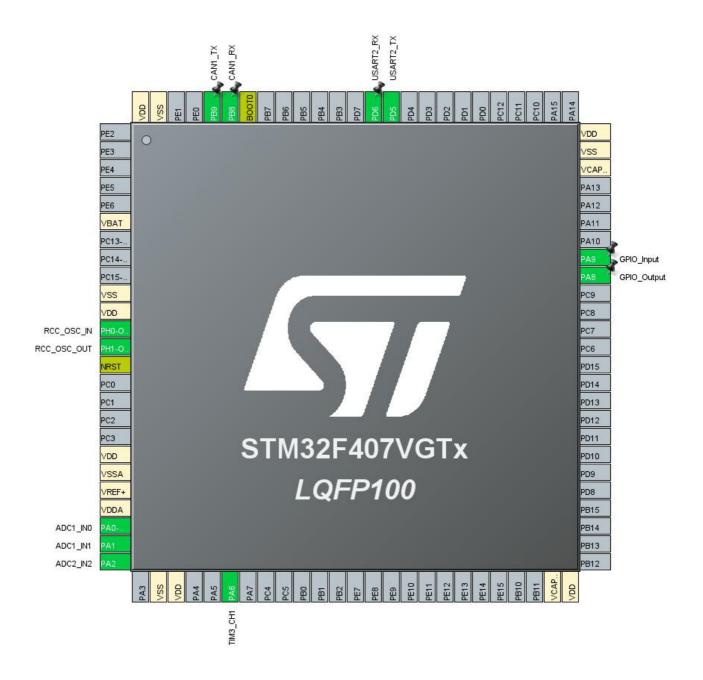
### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F407/417
MCU name	STM32F407VGTx
MCU Package	LQFP100
MCU Pin number	100

# 1.3. Core(s) information

Core(s)	Arm Cortex-M4

# 2. Pinout Configuration

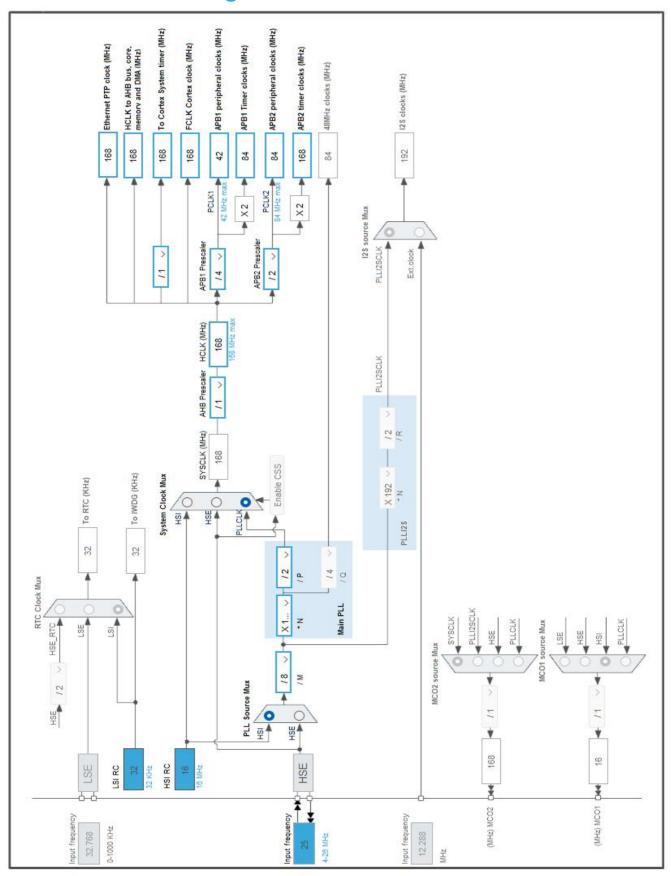


# 3. Pins Configuration

Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
6	VBAT	Power		
10	VSS	Power		
11	VDD	Power		
12	PH0-OSC_IN	I/O	RCC_OSC_IN	
13	PH1-OSC_OUT	I/O	RCC_OSC_OUT	
14	NRST	Reset		
19	VDD	Power		
20	VSSA	Power		
21	VREF+	Power		
22	VDDA	Power		
23	PA0-WKUP	I/O	ADC1_IN0	
24	PA1	I/O	ADC1_IN1	
25	PA2	I/O	ADC2_IN2	
27	VSS	Power		
28	VDD	Power		
31	PA6	I/O	TIM3_CH1	
49	VCAP_1	Power		
50	VDD	Power		
67	PA8 *	I/O	GPIO_Output	
68	PA9 *	I/O	GPIO_Input	
73	VCAP_2	Power		
74	VSS	Power		
75	VDD	Power		
86	PD5	I/O	USART2_TX	
87	PD6	I/O	USART2_RX	
94	ВООТ0	Boot		
95	PB8	I/O	CAN1_RX	
96	PB9	I/O	CAN1_TX	
99	VSS	Power		
100	VDD	Power		

<sup>\*</sup> The pin is affected with an I/O function

# 4. Clock Tree Configuration



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# 1. Power Consumption Calculator report

#### 1.1. Microcontroller Selection

Series	STM32F4
Line	STM32F407/417
мси	STM32F407VGTx
Datasheet	DS8626_Rev8

#### 1.2. Parameter Selection

Temperature	25
Vdd	3.3

### 1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

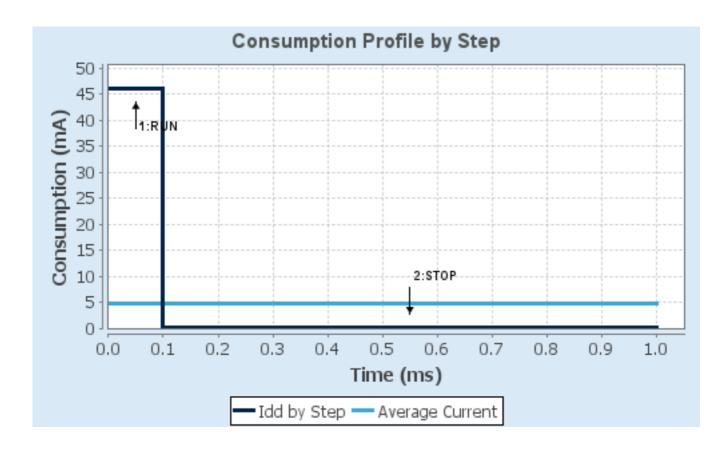
# 1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	Scale1-High	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	168 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator LP Flash-PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	46 mA	280 μA
Duration	0.1 ms	0.9 ms
DMIPS	210.0	0.0
Ta Max	98.47	104.96
Category	In DS Table	In DS Table

### 1.5. Results

Sequence Time	1 ms	Average Current	4.85 mA
Battery Life	29 days, 4 hours	Average DMIPS	210.0 DMIPS

### 1.6. Chart



# 2. Software Project

## 2.1. Project Settings

Name	Value
Project Name	VCU_Son
Project Folder	C:\Users\turjay\Desktop\KOU Racing\KOURacingVCU_Release\vss_video
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F4 V1.28.2
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

## 2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

### 2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_DMA_Init	DMA
3	MX_GPIO_Init	GPIO
4	MX_ADC1_Init	ADC1
5	MX_TIM3_Init	TIM3
6	MX_TIM4_Init	TIM4
7	MX_ADC2_Init	ADC2
8	MX_CAN1_Init	CAN1
9	MX_USART2_UART_Init	USART2

VCU_Son Project
Configuration Report

# 3. Peripherals and Middlewares Configuration

3.1. ADC1 mode: IN0 mode: IN1

3.1.1. Parameter Settings:

ADCs\_Common\_Settings:

Mode Independent mode

ADC\_Settings:

Clock Prescaler PCLK2 divided by 4

Resolution 12 bits (15 ADC Clock cycles)

Data Alignment

Scan Conversion Mode

Continuous Conversion Mode

Discontinuous Conversion Mode

Disabled

DMA Continuous Requests

Right alignment

Enabled

Enabled

\*

Disabled

Enabled \*

End Of Conversion Selection EOC flag at the end of all conversions \*

ADC\_Regular\_ConversionMode:

Number Of Conversion 2 \*

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None Rank 1

Channel 1 \*
Sampling Time 56 Cycles \*

Rank 2 \*

Channel 0
Sampling Time 56 Cycles \*

ADC\_Injected\_ConversionMode:

Number Of Conversions 0

WatchDog:

Enable Analog WatchDog Mode false

3.2. ADC2 mode: IN2

3.2.1. Parameter Settings:

ADCs\_Common\_Settings:

Mode Independent mode

ADC\_Settings:

Clock Prescaler PCLK2 divided by 4

Resolution 12 bits (15 ADC Clock cycles)

Data Alignment Right alignment

Scan Conversion Mode Disabled
Continuous Conversion Mode Disabled
Discontinuous Conversion Mode Disabled
DMA Continuous Requests Disabled

End Of Conversion Selection EOC flag at the end of single channel conversion

ADC\_Regular\_ConversionMode:

Number Of Conversion 1

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None Rank 1

Channel 2

Sampling Time 480 Cycles \*

ADC\_Injected\_ConversionMode:

Number Of Conversions 0

WatchDog:

Enable Analog WatchDog Mode false

3.3. CAN1

mode: Activated

3.3.1. Parameter Settings:

**Bit Timings Parameters:** 

Prescaler (for Time Quantum) 12 \*

Time Quantum 285.7142857142857 \*

Time Quanta in Bit Segment 1 11 Times \*

Time Quanta in Bit Segment 2 2 Times \*

Time for one Bit 4000 \*

Baud Rate 250000 \*

ReSynchronization Jump Width 1 Time

**Basic Parameters:** 

Time Triggered Communication Mode Disable

Automatic Bus-Off Management

Automatic Wake-Up Mode

Automatic Retransmission

Enable \*

Receive Fifo Locked Mode

Transmit Fifo Priority

Disable

**Advanced Parameters:** 

Operating Mode Normal

#### 3.4. RCC

### High Speed Clock (HSE): Crystal/Ceramic Resonator

#### 3.4.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 5 WS (6 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

#### 3.5. SYS

**Timebase Source: SysTick** 

3.6. TIM3

Clock Source: Internal Clock
Channel1: PWM Generation CH1

3.6.1. Parameter Settings:

#### **Counter Settings:**

Prescaler (PSC - 16 bits value) 83 \*
Counter Mode Up

Counter Period (AutoReload Register - 16 bits value ) 49 \*

Internal Clock Division (CKD)

auto-reload preload

Disable

**Trigger Output (TRGO) Parameters:** 

Master/Slave Mode (MSM bit)

Disable (Trigger input effect not delayed)

High

Trigger Event Selection Reset (UG bit from TIMx\_EGR)

**PWM Generation Channel 1:** 

Mode PWM mode 1

Pulse (16 bits value) 0
Output compare preload Enable
Fast Mode Disable

3.7. TIM4

**CH** Polarity

Clock Source : Internal Clock

3.7.1. Parameter Settings:

**Counter Settings:** 

Prescaler (PSC - 16 bits value) 16799 \*

Counter Mode Up
Counter Period (AutoReload Register - 16 bits value ) 0xffff
Internal Clock Division (CKD) No Division
auto-reload preload Disable

**Trigger Output (TRGO) Parameters:** 

Master/Slave Mode (MSM bit) Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx\_EGR)

3.8. **USART2** 

**Mode: Asynchronous** 

3.8.1. Parameter Settings:

**Basic Parameters:** 

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction	Receive and Transmit
Over Sampling	16 Samples

\* User modified value

# 4. System Configuration

# 4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC1	PA0-WKUP	ADC1_IN0	Analog mode	No pull-up and no pull-down	n/a	
	PA1	ADC1_IN1	Analog mode	No pull-up and no pull-down	n/a	
ADC2	PA2	ADC2_IN2	Analog mode	No pull-up and no pull-down	n/a	
CAN1	PB8	CAN1_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PB9	CAN1_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
RCC	PH0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
TIM3	PA6 TIM3_CH1 Alternate Function Push Pull		No pull-up and no pull-down	Low		
USART2 PD5 USART2_TX /		Alternate Function Push Pull	No pull-up and no pull-down	Very High		
	PD6	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
GPIO	PA8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA9	GPIO_Input	Input mode	Pull-up *	n/a	

## 4.2. DMA configuration

DMA request	Stream	Direction	Priority
ADC1	DMA2_Stream4	Peripheral To Memory	Very High *

### ADC1: DMA2\_Stream4 DMA request Settings:

Mode: Circular \*
Use fifo: Disable
Peripheral Increment: Disable
Memory Increment: Enable \*
Peripheral Data Width: Word \*

# 4.3. NVIC configuration

# 4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Pre-fetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	15	0	
CAN1 RX0 interrupts	true	0	0	
DMA2 stream4 global interrupt	true	0	0	
PVD interrupt through EXTI line 16	unused			
Flash global interrupt	unused			
RCC global interrupt	unused			
ADC1, ADC2 and ADC3 global interrupts		unused		
CAN1 TX interrupts		unused		
CAN1 RX1 interrupt				
CAN1 SCE interrupt	unused			
·	unused			
TIM3 global interrupt	unused			
TIM4 global interrupt	unused			
USART2 global interrupt	unused			
FPU global interrupt	unused			

# 4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
CAN1 RX0 interrupts	false	true	true
DMA2 stream4 global interrupt	false	true	true

	VCU_	_Son	Pro	ject
Со	nfigur	ation	Re	port

\* User modified value

# 5. System Views

5.1. Category view

5.1.1. Current

			Middleware			
System Core	Analog	Timers	Connectivity	Multimedia	Security	Computing
DMA 🤡	ADC1 ⊘	тімз 🔗	CAN1 ⊘			
GPIO ⊘	ADC2 🛇	TIM4 ⊘	USART2 🛇			
NVIC ⊗						
RCC ⊘						
sys 🛇						

### 6. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32f405-415\_407-

417\_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis\_model/stm32f405-415\_407-

417\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4-svd.zip

Description

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32\_eval-

tools\_portfolio.pdf

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onal-safety-packages.pdf

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stm8\_software\_development\_tools.pdf

Presentations https://www.st.com/resource/en/product\_presentation/microcontrollers-

stm32-family-overview.pdf

Brochures https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-

and-smart-i-os.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32nucleo.pdf

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Certifications ion\_can.pdf

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tn1489stpsirt-physical-attacks-on-stm32-and-stm32cube-firmware-

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microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

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- Application Notes https://www.st.com/resource/en/application\_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3371-using-the-hardware-realtime-clock-rtc-in-stm32-f0-f2-f3-f4-and-l1-series-of-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3997-audio-playback-and-recording-using-the-stm32f4discovery-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application\_note/an4821-migrating-from-stm32f405415-line-and-stm32f407417-line-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
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