

Tushar Kataria

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EDUCATION

University Of Utah

PhD in Computing

Utah, USA

Current -Jan 2021

Interests: Deep Learning, Medical Image Processing | Advisor: Prof. Shireen Elhabian

Indian Institute of Technology, Bombay

M.Tech. in Communication Engineering

Maharashtra, India

July 2017

Thesis: Image Fusion Using Multi-resolution Algorithms | Advisor: Prof. VM. Gadre, Electrical Engineering

AIACTR, GGSIPU

Bachelor of Technology in Electronics and Communication Engineering, Advisor: Prof. Rashmi Gupta, ECE

New Delhi, India

May 2013

PEER REVIEWED PUBLICATIONS

- **Ashish V. Vanmali, Tushar Kataria, Samrudha Kelkar, Prof. VM.Gadre:** Analysis of Ringing Artifact in Image Fusion Using Directional Wavelet Transforms, *IJERT* Nov 2020
- **Patel, Nikunj and Kataria, Tushar :** Image hallucination at different times of day using locally affine model and kNN template matching from time-lapse images. *Proceedings of the Tenth Indian Conference on Computer Vision, Graphics and Image Processing. ACM, 2016.*

JOURNAL PUBLICATIONS

- **Vanmali, Ashish V, Tushar Kataria, Samrudha G. Kelkar, and Vikram M. Gadre:** "Ringing artifacts in wavelet based image fusion: Analysis, measurement and remedies." *Information Fusion* 56 (2020): 39-69.

TEACHING EXPERIENCE

- TA/TM, CS6640 Fall 2021, Image Processing, University of Utah, Instructor :- Prof Ross Whitaker
- Lab Teaching Assistant, TIDSP lab, IIT Bombay
 - EE352(Digital Signal Processing Lab, undergraduate course)(Spring 2015,2016,2017), Instructors:- Prof. Rajbabu Velmurugan, Prof. Saravanan Vijayakumaran
 - EE750 (DSP System Design and Implementation, Graduate Course), (Autumn 2014,2015,2016), . Instructors:- Prof. VM Gadre, Prof Rajbabu Velmurugan

WORKSHOPS

- **MATLAB Workshop for Research,2015** A two-day workshop on Application of wavelets in MATLAB at the College of Engineering (Ambajogai) for more than 80 participants including faculties, PhD and M. Tech. students from colleges around Maharashtra.
- **DSP Workshop,2016** Conducted multiple workshops for personnels in Mumbai Police, BARC and other TEQIP institutes on the usage of TI DSP kit 5515 in their work.

JOB EXPERIENCE

Cadence Design Systems IP Group formerly Tensilica

Lead Design Engineer

Design Engineer II

Pune, Maharashtra

Dec 2020 -July 2019

July 2019 -July 2017

Inovas Tech

Website Developer

New Delhi

Oct 2013 - Jan 2013

VOLUNTEERING EXPERIENCE

- **Social Responsibility Group, Cadence Pune** : As part of social outreach program i have been an active participant in choosing causes to support like science education in rural villages, care home for mentally challenged adults, mentoring students from underprivileged backgrounds, flood relief etc. Responsibilities also include Volunteering in the social outreach activities, planning new activities around causes supported, encouraging employee participation etc. Dec 2017 - Dec 2020
- **Volunteering for Leadership Development with Tech4Team in Collaboration with W.I.T.U., Uganda** : Part of team of 10 Cadence employees who went to Uganda to work with a local non-profit called Women in Technology Uganda. We helped them build their first maker space, train their teachers on how to use 3D printing, virtual reality, electronics(BBC Microbit) and help in improving their entrepreneurship program. Aug 2019 - Oct 2019
- **Ed Support Volunteer, MAD** : Taught maths to students enrolled in Diploma programs at university. One of my students scored highest marks in his grade and other passed with distinction. Dec 2017 - Present
- **Volunteer Teacher, Abhyasika** : Taught Maths and English modules (once every week) designed to help underprivileged students with lack of good basics in these subjects. Aug 2014 - July 2017