

# Cheruku Sundeep *sundeepcheruku@gmail.com*

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## **Phone: +91 8298679030**

319, Aryabhatta Hall,  
Indian Institute of Technology Patna,  
Patna,  
Bihar, India 800013.

## **Permanent Address**

4-12-320, Bhagyalatha Colony,  
Vanasthalipuram,  
Hyderabad,  
Andhra Pradesh, India 500070

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## **Personal Details**

Date of Birth: 08<sup>th</sup> May, 1991.

Place of Birth: Hyderabad, Andhra Pradesh.

## **Education**

Educational Qualifications	Year of Completion	Board/University	CPI*/ Percentage
<i>B Tech</i>	<i>Still Pursuing</i>	<i>Indian Institute of Technology Patna</i>	<i>6.91</i>
<i>12th Standard</i>	<i>2008</i>	<i>Board of Intermediate Education, AP</i>	<i>93.3%</i>
<i>10<sup>th</sup> Standard</i>	<i>2006</i>	<i>Board of Secondary Education, AP</i>	<i>92.67%</i>

\* CPI-Cumulative Performance Index (scale of 10), for 6 semesters.

AIR: 5404 in General Category IIT-JEE, 2008.

## **Previous Experience as Intern**

1. 'Simulation and Physical realization of OFDM techniques', under the guidance Dr Preetam Kumar, Assistant Professor, Department of Electrical Engineering, IIT Patna.

Duration: May'10 – July'10.

2. 'Study, Design, Document & Automate UG Labs for Electronics & Communication Course using Tektronix Basic Instruments (Oscilloscopes, Function Generator, Power Supply & Digital Multimeter)', under the guidance of Mr Sunil Motwani, National Head Channels, Tektronix, Bangalore.

Duration: May'16 – July'08

## Projects Developed/Currently Working on

<i>Project Title</i>	<i>Period</i>	<i>Description</i>
<i>Automated Toll Booth System</i>	<i>October'09 - November'09</i>	<i>This system counts the no. of cars or trucks entering or leaving a point and charges depending on the amount of load.</i>
<i>Automated Temperature controller</i>	<i>March'10 –April'10</i>	<i>This system maintains the temperature of given substance</i>
<i>Thermocouple</i>	<i>October'10 - November'10</i>	<i>It measures the temperature of the medium with 85% accuracy made using Iron and Copper</i>
<i>Software based Speech Transcription System</i>	<i>January'11 – April'11</i>	<i>Performs user independent speech transcription for English numerals</i>
<i>Water shed algorithm</i>	<i>January'11 – April'11</i>	<i>Image processing technique which detects edges</i>
<i>Design and Implementation of Multirate CI/MC-CDMA Systems*</i>	<i>July'11 -</i>	<i>Final Year Project</i>

## Skills

- I have good knowledge of programming in MATLAB, I have used MATLAB for signal processing, simulating various communication, control and power systems etc.
- I have good knowledge of programming in C.
- I have done programming for DSK TMS320C6713.
- I have done programming in Verilog. I have used Xilinx Spartan 3E board for physical implementation of my Verilog codes. I can work on any other FPGA board also.
- I have done programming on various microcontrollers like Atmega32, ARM7, and ARM9 etc.
- I have basic knowledge of using and programming 8086 microprocessor in Mnemonics and programming 8085 microprocessor in Mnemonics and HEX codes.
- I have the knowledge of writing Ladder Diagram Programs for a Programmable Logic Controllers.
- I am a good speaker.

## Achievements

1. AIR 5404 in IIT JEE 2008
2. AIR 5528 in AIEEE 2008.
3. 298 marks in BITSAT 2008.
4. Rank of 1675 in EAMCET 2008.

## Interests

- I love solving puzzles, riddles, problems involving math.
- Reading novels is my favorite pass-time.
- I play foot-ball, cricket, table tennis, badminton occasionally.

## References

Dr Preetam Kumar,  
Assistant Professor,  
Department of Electrical Engineering,  
Indian Institute of Technology Patna, Patna.  
[pkumar@iitp.ac.in](mailto:pkumar@iitp.ac.in)

Dr Ahmad Ali,  
Assistant Professor,  
Department of Electrical Engineering,  
Indian Institute of Technology Patna, Patna.  
[ali@iitp.ac.in](mailto:ali@iitp.ac.in)