

Soni Rahul Jagdish Electrical Engineering Indian Institute of Technology, Bombay Specialization: Microelectronics 10D070005

**UG Third Year(Dual Degree)** 

Male

DOB: 10-08-1991

Examination	University	Institute	Year	CPI / %
Graduation	IIT Bombay	IIT Bombay	2013	6.47
Intermediate/+2	C.B.S.E	Modi Public School	2009	74.40
Matriculation	S.S.C	O.L.V High School	2007	83.23

### **SUMMER INTERNSHIP**

Public Relation Manager - Edfora " Education for all "

May'12 – July'12

- Contributed as an evangelist for spreading secondary education to poor by proposing strategies to stakeholders for drawing in the renowned teachers of Mumbai.
- Relayed mutual benefits and perspectives to HODs of many coaching classes and schools by empowering the vision of a Networking site for education community

### **PROJECTS**

• Efficiency improvement of LED lightning

Guide: Prof. Swaroop Ganguly

Dec'11 - Present

- Applying concepts of quantum mechanics in semiconductor devices to improve the efficiency of radiative re-combination by adding quantum well in between the junction.
- Stimulated new designs to decrease the absorption phenomena to increase luminescence.
- Implementation of hardware & software of Tetris game

Guide: Prof. M.B. Patil,

Feb'12 - March'12

- Built Tetris block game using a DEO Nano FPGA board, LED grid and some external digital circuitry
- Designed an efficient digital circuitry, and optimized Verilog based coding using Altera Quartus.
- Design of basic analog Operational Amplifier.

Guide: Prof. V.Ramogopal Rao

Jan'12 - April'12

- Designed Operational Amplifier for required gain, input and output resistance.
- Simulated the design using LT-Spice and practically verified the circuit in Lab using LN741.

Graph Plotter using C++

Guide: Prof. D.B.Phatak

Sept'10 - Nov'10

- Crafted Graph Plotter software using C++, where the Graphical Interference was developed using Ez-Window.
- It could plot various mathematical functions as well as their composites and perform operations like differentiation, integration, finding roots etc

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### **POSITION OF RESPONSIBILITY**

Internship Coordinator, Placement Cell I.I.T Bombay

July'12 - Present

- Procuring internship opportunities for the students of I.I.T Bombay.
- Coordinating with companies and university.
- Core Group Member, Alumina Student Mentorship Program (ASMP)

Aug'11 - April'12

- Introduced a new improved procedure for allotment of students and mentors which facilitated better interaction among students and their mentor.
- Successfully organized the Student Alumni Meet for the first time, as a platform for student and alumni networking.
- Improved the foundation of program by adding professionalism to it.
- Techfest Media & Marketing Coordinator

Aug'11 - Feb'12

- Headed a team of more than 20 organizers to reach through various forms of Media and coordinate with the sponsors.
- Contacted reporters of leading newspaper and news channel in India
- Helped in organizing "Ummed-e-Milaap" a social initiative to unite students of India and Pakistan.

## **COMPUTER SKILLS**

- Programming languages: C/C++, HTML, and Arduino (Microcontroller).
- Hardware Description Language : Verilog , Keil
- OS: Windows XP/7, Ubuntu
- Electronics: LTSpice, Eagle(PCB Design)
- Tools : Scilab , Matlab

# **EXTRA CURRICULAR ACTIVITIES**

- Made a remote controlled car with differential turning using a simple radio frequency circuit.
- Mentored sophomore team in ITSP (Institute Technical Summer Project).
- Table Tennis player certified by National Sports Organization in the year 2010-2011.
- Avidly interested in Trekking and Adventure Sports.
- Part of the team for the Performing Arts Festival (PAF), 2011, the largest Inter Hostel Cultural Festival of I.I.T Bombay.