DEEPAK KUMAR MEENA **10D070046**
Electrical Engineering **UG Third Year(Dual Degree)**
Indian Institute of Technology, Bombay **Male**
Specialization: Microelectronics **DOB: 29/09/1993**

Examination	University	Institute	Year	CPI / %
Graduation	IIT Bombay	IIT Bombay	2012	6.13
Intermediate/+2	VIDHYA MANDIR SR. SEC. SCHOOL	VIDHYA MANDIR SR. SEC. SCHOOL	2010	85.00
Matriculation	BSF SR. SEC. SCHOOL	BSF SR. SEC. SCHOOL	2008	85.80

SCHOLASTIC ACHIEVEMENTS:

- Cracked **IIT Joint Entrance Examination(2010),** one of the toughest pre-engineering examination for which **4,50,000** students appeared with **30 rank** in respective category.
- Participated in **National Mathematics Olympiad Contest: 2008** (B-LEVEL), Organised by **Delhi Association Of Mathematics Teachers** and got a **merit certificate** with **80% marks.**
- Participated in **State Level Environmental Awareness Examination** conducted by **ENVIRONMENTAL CARE SUSTAINABLE DEVELOPMENT SOCIETY, JABALPUR** and got a **Grade 'A'** certificate.

EXTRACURRICULAR ACTIVITIES:

- **SPORTS:**
  - Very much enthusiastic about cricket, badminton, tennis and athletics.
  - Was one of the two participants of **winning** Weight lifting GC'11 (under 75kg).
  - Purple cap holder (**best bowler**) of Cricket 2masha (an Cricket event of Hostel 2).
  - Member of the Tug Of War GC **runner up** team.
  - Participated in Crossy '11(a campus marathon competition).
  - Active member of Hostel athletics team.
- **CULTURAL:**
  - Was a part (Prod. Making) of winning **PAF'10** (an IITB event).
  - Was a part (both acting and Prod. making) of runner up **PAF'11.**
  - **P**articipated in freshman night(a very first senior interaction event) in my Ist Sem.
- **TECHNICAL EVENTS:**
  - Madea **Microcontroller car** in the event **Trackmania** organized by **TechniC, the Technical club of IIT Bombay** in my freshman year.

POSITION OF RESPONSIBILITY

- Worked as an organizer in Techfest(2011) and E-cell(2011)
- Worked as an coordinator in Techfect(2012)
- Currently volunteering test administration in AVANTI FELLOWS.

KEY PROJECTS UNDERTAKEN:

MORSE CODE ENCODER/DECODER **Guide:Prof S Patkar**

- To implement a Morse code encoder in Verilog using a CPLD/FPGA board, which takes input from a hex keypad.

- To also implement a Morse code decoder using a FPGA board, which identifies the sequence of 0's and 1's and recognize them as 'dits' and 'dahs'. The corresponding characters are displayed on the LCD Display.

PROGRAMMING ON A MICRO-CONTROLLER **Guide: Prof. Yardi Arti Dilip**

- Learnt how to programme codes in a microcontroller.
- Tried many codes in microcontroller for various experiments

OPAMP DESIGN **Guide:Anil Kottantharayi**

- Design the circuit for Op-amp in the breadboard using the bipolar junction transistors

MAZE SOLVER **Guide :Prof D.B. Phatak**

- Aim of the project was to make program using C++ with graphic user interface(GUI) which creates a random mazes (of size given by user) and finding shortest path to reach at center. We made a program for this of about 3000 lines.

TERM PAPER ON DEVICE OPENING **Guide: Prof J. Vashi**

- Aim of the project was to open a device (optical mouse) and understand its working and complications.
- Submitted term paper on the device opened.

TECHNICAL SKILLS:

- Programming languages:-C,C++,HTML,CSS
- Software Knowledge:- MS office, MS Word, MS Excel, Power Point, Photoshop, LT SPICE, HDL:Verilog
- Operating System : Windows, Linux(Ubuntu)
- Simulator: Familiar with Keil uvision, iverilog, Lab view and Microprocessor 8085 simulator.

COURSES UNDERTAKEN

Electrical Engineering	Other Departments
Introduction to Electrical Systems	Calculus
Introduction to Electronics	Chemistry
Network Theory	Computer Programming and Utilization
Electronic devices and circuits	Linear Algebra
Electrical Machines Power electronics	Differential Equations
Electronic devices lab	Electricity and Magnetism
Digital circuits and systems	Engineering graphics and drawing
Analog systems	Philosophy
Signals and systems	
Electromagnetic Waves	
Communication Systems	
Microprocessors	
CMOS Analog VLSI Design	

DECLARATION:

I hereby declare that the information given above is true to the best of my knowledge as of 15/08/2012

Deepak Kumar