TEJAS AJAY CHAUDHARI **10D070043**
Electrical Engineering **UG Third Year(Dual Degree)**
Indian Institute of Technology, Bombay **Male**
Specialization: Microelectronics **DOB: 18/04/1992**

Examination	University	Institute	Year	CPI / %
Graduation	IIT Bombay	IIT Bombay	2012	7.04
Intermediate/+2	CBSE	DAV PUBLIC SCHOOL	2010	85.00
Matriculation	MAHARASHTRA BOARD	ST JOSEPH'S CONVENT HIGH SCHOOL	2008	93.07

TECHNICAL RESUME

❖ **Scholastic achievements** :-

- Recipient of the NTSE(National Talent search examination) scholarship conducted in 10th standard
- Awarded MTSE(Maharashtra Talent search examination) scholarship with state rank 21 in 8th standard.
- Awarded MTSE(Maharashtra Talent search examination) scholarship in 9th standard.
- Awareded scholarships in 4th &7th standard scholarship examinations conducted by Maharashtra education board.
- Recipient of mathex scholarship (8th std) by mathematics department of maharshtra state board..

❖ **Projects** :-

- L.C.D display of characteristics of d.c motor : characteristics such as voltage, current, power& speed were obtained by a circuit which was designed & then displayed on l.c.d display.. using verilog HDL on terasic F.P.G.A on aletra quartus software.. [april 2012]
- Optical mouse in this project the optical mouse was deassembled and power point presentation was made describing working & structure of optical mouse.[march 2011]
- Maze solver game a maze solving game was coded in C++ using api for graphical user interface [October 2010]
- Wireless racing bot a wireless car robot was designed successfully with differential turning mechanism [august 2010].

❖ **Programming skills :-**

- Certified in C programming by recognized software institute .
- Knowledge of C++ coding with project experience..

- Knowledge of programming in java.
- Knowledge of useful application softwares such as matlab ,LT-spice, Adobe photoshop ,adobe illustrator.
- Knowledge of HTML(hypertext markup language).
- Knowledge of coding using arduino microcontroller.
- Knowledge of verilog hardware description language with project experience.
- Knowledge of programming on TERASIC based F.P.G.A (field programmable gate array) using altera quartus software .

❖ **Courses done** :-

COURSES UNDERTAKEN	
Electrical Engineering	**Other Departments**
Introduction to Electrical Systems Introduction to Electronics Network Theory Electronic devices and circuits Electronic devices lab Analog circuits Digital circuits Analog lab Digital lab Machines lab Power electronics Signals & systems	Calculus Chemistry Computer Programming and Utilisation Linear Algebra Differential Equations Electricity and Magnetism Data Analysis and Interpretation Complex Analysis Differential Equations II Introduction to biomedical engineering