



Amit Yadav
Electrical Engineering
Indian Institute of Technology, Bombay

09007018
UG Third Year (B.Tech.)
Male
DOB: 17 Jan 1990

Examination	University	Institute	Year	CPI / %
Graduation	IIT Bombay	IIT Bombay	2011	8.50
Intermediate/+2	CBSE	The Army Public School	2008	92.80
Matriculation	CBSE	The Army Public School	2006	95.00

KEY ACADEMIC PROJECTS

Designed Wireless Mesh Network using Zigbee modules [Jun 2011 - Jul 2011]
Guide : Prof. V.R.Rao

- Created a **wireless network** between **two Zigbee modules** using **API mode of operation**
- Tested the network using microcontrollers
- **Analysed, Designed and tested** various **meshes of sensors** to maximize **efficiency**

Designed a mini computer using Harvard Architecture [Mar 2011 - Apr 2011]
Guide : Prof. Sachin Patkar

- Studied **Harvard Architecture**
- Designed a **4-bit 'mini computer'** based on Harvard Architecture using the basic Logic gates
- Programmed it using '**Verilog Hardware Description Language**' and tested it on a **CPLD chip**

Robopolo – Water Racing Bot [Nov 2010 - Dec 2010]

- Built a **Wireless Bot**, using **RF communication**, that raced on water and could launch a ball into a goal multiple times
- Implemented **sensors** using **microcontrollers** to stabilize and turn the bot
- Built an **ingenious design** for launcher

Finger Print Matching Project [Oct 2009 - Nov 2009]
Guide : Prof. D.B.Phatak

- Designed **Input Interface in C++** to record various parameters about an individual
- **Database Management** – created the registration information database which was used in fingerprint matching project

COMPUTER SKILLS

- Programming languages - Visual C++, **MATLAB**, **HTML**, IVERILOG, LaTeX
- Simulation/Analysis - **SimuLink**, NGSPICE, MAGIC
- Other Software Packages - WinAVR, Adobe Photoshop, **Adobe AfterEffects**, MS Office

SCHOLASTIC ACHIEVEMENTS

- Achieved All India Rank of **557 in IIT- JEE 2009**
- Secured a State Rank **35** and **98.12 percentile** in the **7th National Science Olympiad (NSO)**
- Was among **top 0.2%** students in **All India Engineering Entrance Examination (AIEEE) 2009**
- Received **EESA Scholarship** for excellence in academic education during schooling

RELEVANT COURSES

<i>Courses</i>	<i>Labs</i>
<ul style="list-style-type: none">• VLSI Design• Communication Systems• Microprocessors (8085)• Digital Systems , Analog Circuits• Data Interpretation & analysis• Probability and Random Processes• Linear Algebra, Differential Equation	<ul style="list-style-type: none">• Communication Lab• Microprocessor Lab• Analog Circuits Lab• Digital Circuits Lab• Electronic Devices Lab• Power Electronics Lab• Experimental & Measurement Lab

POSITION OF RESPONSIBILITY

Joint Secretary, Electrical Engineering Students Association [Apr 2011 – Present]

- Coordinated in **planning and organising department level events** such as 'Trek', 'Picnic', 'Freshmen Welcome', 'Traditional Day' etc
- Involved in **crowd management** and **logistics supply**
- Assisting a **team of 12 members** to successfully **organize Department Fest – 'Aagomani'**

Coordinator, Techfest 2011 [Dec 2009 - Jan 2010 }

- Supervised the **conduct of the 'Robowars'** event in Techfest
- Widely **publicized** events of Techfest in various colleges of Pune and Mumbai

AWARDS

- TV program *Khel Khel Mein Badlo Duniya* [Jan 2005]
 - Won the **first prize** in the 'play cum quiz' show organized by **Petroleum Conservation Research Association**, Ministry of India

EXTRA CURRICULAR

- **Won 3rd position** in Intra-hostel **Creative Writing** competition
- **National Sports Organization** - Basketball [2009 – 2010]
- Highly **adventurous** – love **trekking** and visiting new places
- Active in all sports - love Table Tennis , **badminton** , Lawn Tennis, **Basketball**
- Other interests – **Inline Skating**, Video and Photo editing