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| **Professional Experience 24 Months** | | | | |
| **SYNOPSYS(IND) PVT. LTD. Applications Engineer June 08 - June 10**  Synopsys is the world leader in Electronic Design Automation and semiconductor IP market.   * **Understand customer requirement** and **provide a feasible solution** to integrate and debug Synopsys IP in their Chip designs - Worked with **global** semiconductor companies (clients/prospective), on protocols like USB, AMBA, PCIe etc. * **Recognize product improvement opportunities** while working with the clientsand work with Research and Development teams for their evaluation and **implementation** in the upcoming product releases – part of five major product releases * **Recognize potential Sales leads,** **competitor presence** or **License renewal opportunities** and coordinate with respective Sales or Accounts management teams to take them forward * Delivered jump start **training to clients** and created the new hire training and **‘best debugging techniques’** database * **Published technical articles** and **application notes** that help clients implement Synopsys solutions better | | | | |
| **Professional Recognition**   * Awarded the **‘Rising Star Award’** for outstanding overall performance during the year 2008-’09 , given once every year * **Promoted** to Applications Engineer II **within 17 months** of service at Synopsys expediting the norm of 30 – 36 months * Recognized as the ‘**Go-to person**’ and SPOC within the India & North America teams for any issues, enhancements or latest features on USB Products | | | | |
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| **INTERNSHIPS/LIVE PROJECTS** | | | | |
| *Summer Internship,*  **Capgemini** | **Go-To-Market Strategy for Capgemini Telecom Testing GSL May ’11 – June ’11**   * **Assessing** the global telecom testing market, **segmenting** it and analyzing current position of Capgemini * **Competitive landscaping** of the industry – **SWOT analysis** for Capgemini and its competitors in all segments * Recognizing upcoming trends, opportunities and **growth drivers** in various geographies/segments * Devised **Market Entry strategy** for APAC region and **Market penetration strategy** for Western Europe. | | | |
| **Qualcomm** | Internship Jan ’08 – June ’08   * Worked with the Chip-level verification team to **automate** a part of the Verification flow to **reduce cycle times** | | | |
| **CEERI, CSIR** | Internship June ’06 - July ’06   * **Evaluated the usefulness** of a new camera technology (Specim) for counterfeit currency detection | | | |
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| **ACADEMICS** | | | | |
| **Degree** | | **Institute / University / Board** | **Year** | **%/CGPA** |
| Master of Mgmt. | | SJMSOM, IIT Bombay | 2010-12 | 8.40/10 |
| B. E(Hons.), EEE. | | Birla Institute of Technology and Science(BITS), Pilani | 2004-08 | 8.88/10 |
| Class XII | | Aditya Junior College, (Board of Intermediate Education-AP) | 2003-04 | 97.36 % |
| Class X | | St. Francis de Sales School, (Board of Secondary Education – AP) | 2001-02 | 90.33 % |
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| **ACADEMIC ACHIEVEMENTS** | | | | |
| * Awarded **Chief Minister’s Pratibha Award** (Gold Medal) by the **Government of Andhra Pradesh** for excellence in SSC * Awarded **HUL scholarship** **twice** for academic excellence at SJMSOM, IIT Bombay * Authored a paper selected for presentation at a **National** Seminar sponsored by **UGC** and VLSI Society of India. * Secured **State 1st** position in 1999, **State 3rd** position in 1998 and **State 14th** position in 2002 in Sir C.V Raman Council Talent test conducted by Viswabharati Academy * Secured **State 3rd** position among all classes in 2000 in State Level Science Talent Search Exam conducted by Unified Council * Cleared APICS – CPIM Level1 certification in supply chain management | | | | |
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| **POSITIONS OF RESPONSIBILITY** | | | | |
| **Student Coordinator, Placement Committee, SJMSOM, IIT Bombay** 2010-12   * **Lead an 8 member placement committee -** successfully completed summer placements and conducting final placements * Managed the planning, scheduling, responsibility and task allocation to team, recruiter campus relations, business networking, student and public relations, recruitment event design & execution for 240 MBA students and over 200 Corporate Accounts | | | | |
| **Coordinator, Continuum 2011 –** Overall Coordinator of Annual Business Seminar series of SJMSOM, IIT Bombay | | | | |
| **Coordinator**, Venue Management Committee, Quark 2007, the **National level Technical Festival** at BITS Pilani, Goa | | | | |
| **Manager,** Literary Events, Waves’07,the **Annual Cultural Festival** of BITS Pilani, Goa Campus | | | | |
| **Maintenance Secretary** at BITS Pilani, Goa Campus - **Elected twice consecutively** | | | | |
| **Founding Member** of Electrical and Electronics Association at BITS Pilani, Goa Campus, responsible for all EEE student activities - Coordinated industry seminars by **National Instruments,** VI Microsystems and the set up of PSOC lab by **Cypress** | | | | |
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| **EXTRA CURRICULAR ACTIVITIES/ACHIEVEMENTS/INTERESTS** | | | | |
| * Member, Synopsys Hyd. **Toastmasters**, part of Toastmasters International, a body that nurtures leadership & public speaking * **Organized** regional **alumni meet** for SJMSOM alumni Bangalore chapter * Won **1ST Prize** in **‘General Proficiency’** competition at School level from among a total of 550 students | | | | |