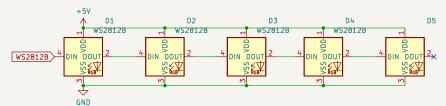
VL53L0X **XIAO** +5V +3٧3 XSHUT1 1 D0/A0
XSHUT2 2 D1/A1
XSHUT3 3 D2/A2
XSHUT4 4 D3/A3
SDA 6 D4/SDA
SCL 6 D5/SCL
TX 7 D6/TX 5V 14 +3V3
GND 13 GND 12
D10 11 U1 SCL SXSHUT 5 XSHUT1 ∑ XSHUT 5 XSHUT4 SCL SCL G GPIO 6× GPIO 6× D9 9 WS2812B 9 XSHUT5 8 RX/D7 RX 2 XIAO_RP2040 GND GND +3V3 +3V3 $\begin{array}{c|c} \hline SCL & 3 \\ \hline SDA & 4 \\ \hline SDA & 2 \\ \hline SDA & 3 \\ \hline SDA & 4 \\ \hline SDA & 5 \\ \hline SDA & 6 \\ \hline SDA &$ SCL SCL XSHUT 5 XSHUT2 CONN GPIO 6× 2 O H1 MountingHole GND GND +3V3 OpenMV Conn O H2 MountingHole 10k +3V3 R2 +3V3 SCL SCL XSHUT 5 XSHUT3 PWR_FLAG SDA 4 SDA S GPIO 6× 7 GND

WS2812B



Sheet: / File: 03-FRONT.kicad sch Title: Size: A4 Date: Rev: KiCad E.D.A. eeschema 7.0.9 ld: 1/1