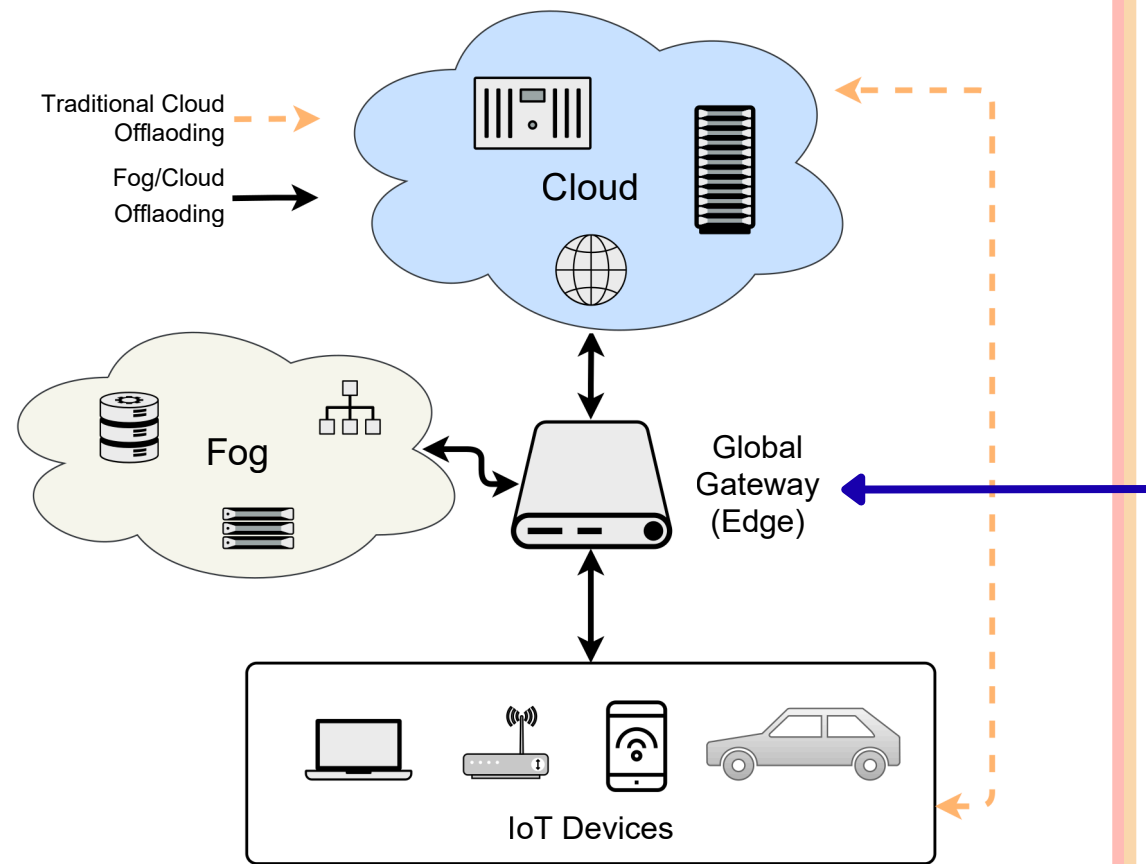
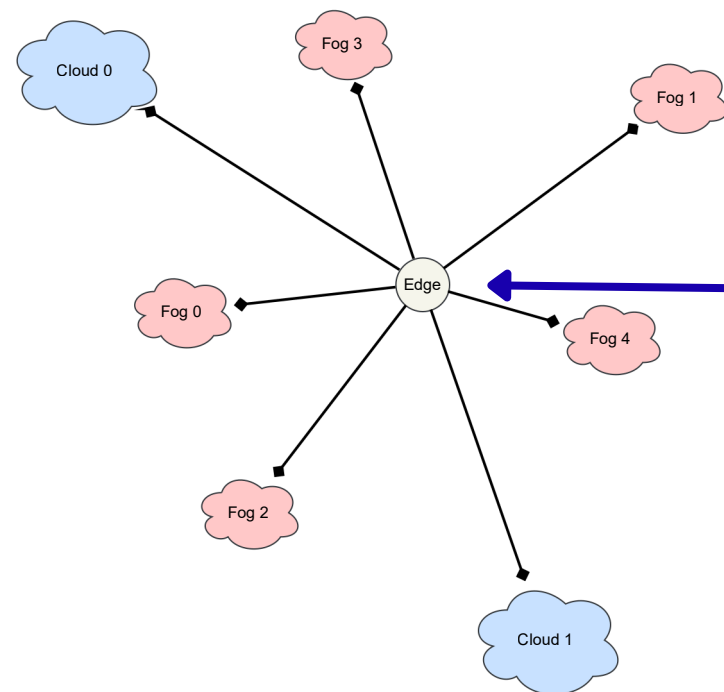


# Modelisation

## System Architecture

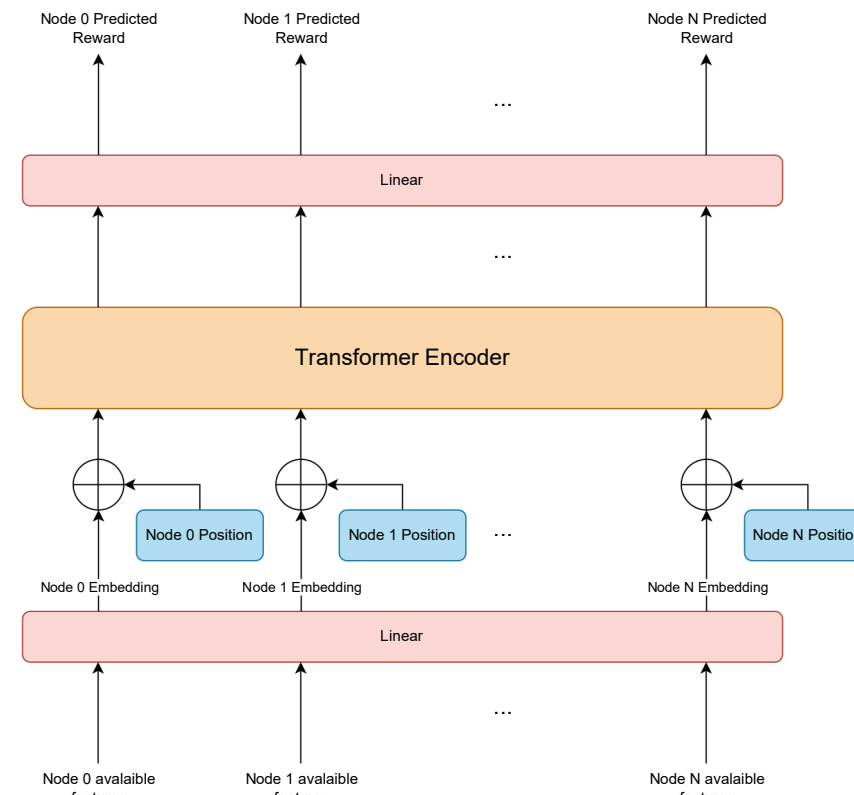


## Nodes Infrastructure

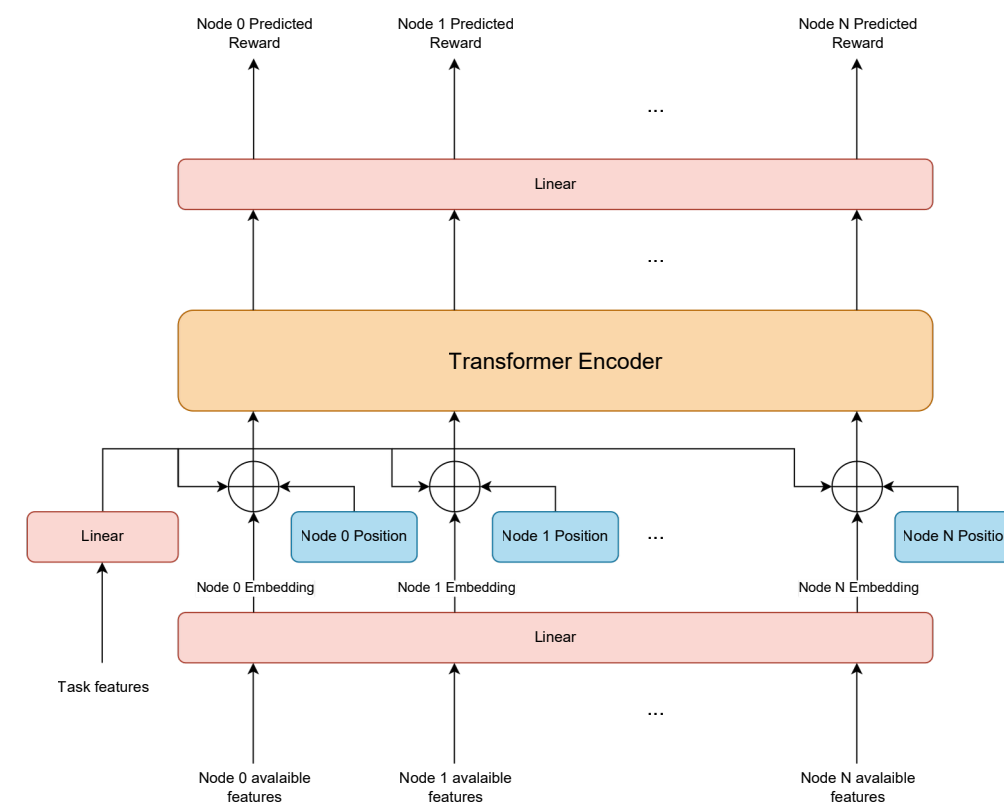


# Models

## NOTE

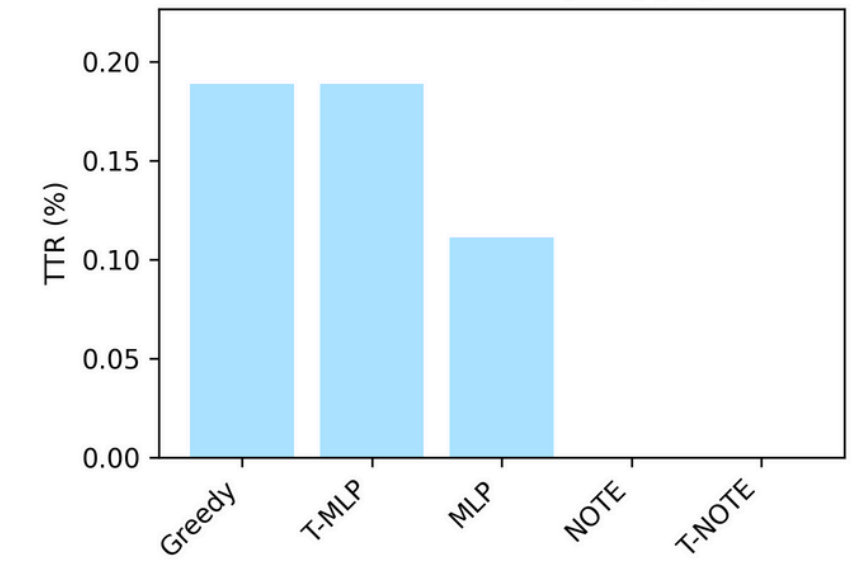


## T-NOTE

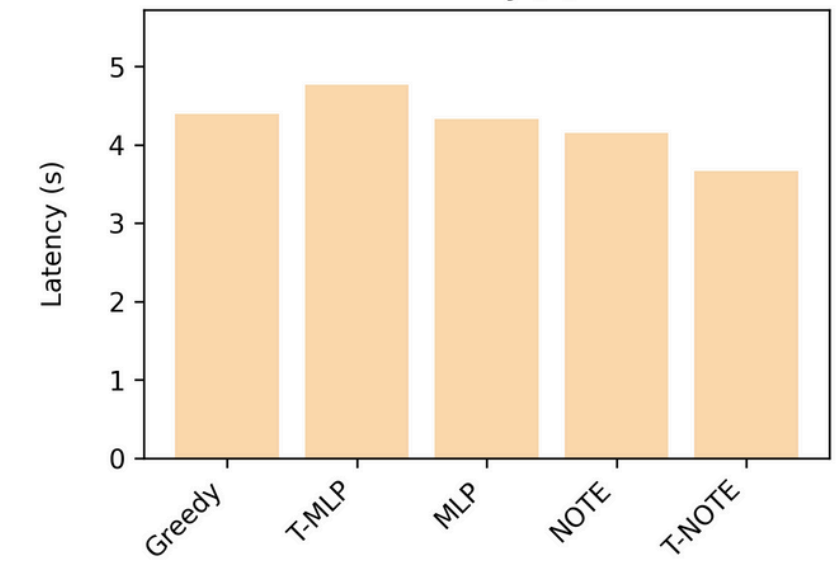


# Results

## Task Throw Rate (TTR) [%]



## Latency [s]



## Energy [W]

