

Circuit	N	States	Verification Time (sec)								
			IND_TOOL			MPSAT			ESSET		
			Deadlock	Compliance	Persistency	Deadlock	Compliance	Persistency	Deadlock	Compliance	Persistency
Async Counter	8	3570	11.4	25	141	8.7	8.8	10.1	0.68	0.69	0.68
	9	7154	12	27	272	30	30	37	1.9	1.9	1.9
	10	14322	27	56	672	133	133	159	5.6	5.6	5.6
C-element	8	512	19	63	28	0	0	0	0.2	0.2	0.2
	9	1024	46	73	72.8	0	0	0	0.26	0.26	0.26
	10	2048	204	245	239	0	0	0	0.48	0.48	0.48
Ring Oscillator	21	42	5.7	9.3	4.66	0	0	0	0	0	0
	31	62	6	25.8	11.2	0	0	0	0	0	0
	41	82	6.75	55.7	17.3	0	0	0	0	0	0
	51	102	7	108	25.6	0	0	0	0	0	0