Master Thesis

An LLVM Backend for Accelerate



Programming Languages and Compiler Construction Department of Computer Science Christian-Albrechts-University of Kiel

student: Timo von Holtz

Priv.-Doz. Dr. Frank Huch

advised by:
Assoc. Prof. Dr. Manuel M. T. Chakravarty

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Selbstständigkeitserklärung

Ich erkläre hiermit, dass ich die vorliegende Arbeit selbstständig und nur unter Verv	wen-
dung der angegebenen Literatur und Hilfsmittel angefertigt habe.	
April 17, 2014	

Timo von Holtz

Todo list

Contents

1.	Introduction	1
2.	Technologies	3
	2.1. LLVM	3
	2.1.1. LLVM IR	3
	2.1.2. Optimization	4
	2.1.3. Vectorization	4
	2.2. Accelerate	4
3.	Contributions	5
	3.1. llvm-general-quote	5
4.	Conclusion	9
	4.1. Related Work	9
Α.	Listings	11
Bil	bliography	15

List of Figures

2.1.	sum as a C function	3
2.2.	sum as a LLVM	4
3.1.	Monadic generation of for loop	6
3.2.	For Loop using llvm-general-quote	6
3.3.	Expanded For Loop	7

List of Tables

1. Introduction

Write Introduc-

2. Technologies

2.1. LLVM

LLVM[Lat02] is a compiler infrastructure written in C++. In contrast to GCC it is designed to be used as a library by compilers. Originally implemented for C and C++, the language-agnostic design (and the success) of LLVM has since spawned a wide variety of front ends: languages with compilers that use LLVM include ActionScript, Ada, D, Fortran, OpenGL Shading Language, Haskell, Java bytecode, Julia, Objective-C, Python, Ruby, Rust, Scala and C.

2.1.1. LLVM IR

LLVM defines it's own language to represent programs. It uses Static Single Assignment (SSA) form.[AWZ88; RWZ88] A program is said to be in SSA form if each of its variables is defined exactly once, and each use of a variable is dominated by that variable's definition. SSA form greatly simplifies many dataflow optimizations because only a single definition can reach a particular use of a value, and finding that definition is trivial.

To get idea of how this looks like in practice, let's look at an example. Figure 2.1 shows a simple c function to sum up the elements of an array. The corresponding LLVM code is shown in figure 2.2.

```
double dotp(double* a, double* b, int length) {
  double x = 0;
  for (int i=0;i<length;i++) {
    x += a[i]*b[i];
  }
  return x;
}</pre>
```

Figure 2.1.: sum as a C function

```
define double @sum(double* %a, i32 %length) {
  %1 = icmp sgt i32 %length, 0
  br i1 %1, label %.lr.ph, label %._crit_edge
                                                  ; preds = %0, %.lr.ph
.lr.ph:
  %indvars.iv = phi i64 [ %indvars.iv.next, %.lr.ph ], [ 0, %0 ]
  %x.01 = phi double [ %4, %.lr.ph ], [ 0.000000e+00, %0 ]
  %2 = getelementptr double* %a, i64 %indvars.iv
  %3 = load double* %2
  %4 = fadd double %x.01, %3
  %indvars.iv.next = add i64 %indvars.iv, 1
  %lftr.wideiv = trunc i64 %indvars.iv.next to i32
  %exitcond = icmp eq i32 %lftr.wideiv, %length
  br i1 %exitcond, label %._crit_edge, label %.lr.ph
                                                  ; preds = %.lr.ph, %0
._crit_edge:
 %x.0.lcssa = phi double [ 0.000000e+00, %0 ], [ %4, %.lr.ph ]
 ret double %x.0.lcssa
```

Figure 2.2.: sum as a LLVM

- 2.1.2. Optimization
- 2.1.3. Vectorization
- 2.2. Accelerate

3. Contributions

3.1. Ilvm-general-quote

When writing a companyiler using LLVM in Haskell there is a good tutorial on how to do it at http://www.stephendiehl.com/llvm/. It uses *llvm-general* to interface with LLVM. The general idea is to use a monadic generator to produce the AST on the fly.

Figure 3.1 shows how to implement a simple for loop using monadic generators. As you can tell this is much boilerplate code. We have to define the basic blocks manually and add the instructions one by one. This has some obvious drawbacks, as the code can get unreadable pretty quickly.

A solution is to use quasiquotation [Mai07] instead. The idea behind quasiquotation is, that you can define a DSL with arbitrary syntax, which you can then directly transform into Haskell data structures. This is done at compile-time, so you get the same type safety as writing the AST by hand.

I implemented *llvm-general-quote*, a quasiquotation library for LLVM. Figure 3.2 shows a for loop using my library.

Figure 3.3 shows the resulting LLVM IR. This is clearly more readable. Furthermore, one can see much more clearly what the produced code will be.

Another advantage of quasiquotation is antiquotation. This means you can still reference arbitrary Haskell variables from within the quotation. Using this the following are equivalent:

- [llinstr| add i64 %x, 1 |]
- let y = 1 in [llinstr| add i64 %x, \$opr:(y) |]

The design of *llvm-general-quote* is inspired by *language-c-quote*, which is also used in the cuda implementation of Accelerate. I use "Happy" and *Alex*.

```
for :: Type
                                                    -- type of the index
    \rightarrow Operand
                                                    -- starting index
   \rightarrow (Operand \rightarrow CodeGen\ Operand)
                                                   -- loop test to keep going
   \rightarrow (Operand \rightarrow CodeGen\ Operand) -- increment the index
    \rightarrow (Operand \rightarrow CodeGen())
                                                    -- body of the loop
   \rightarrow CodeGen()
for ti start test incr body = do
   loop \leftarrow newBlock "for.top"
   exit \leftarrow newBlock "for.exit"
     -- entry test
   c \leftarrow test \ start
   top \leftarrow cbr \ c \ loop \ exit
     -- Main loop
   setBlock\ loop
   c \quad i \leftarrow freshName
   let i = local \ c_i
   body i
   i' \leftarrow incr i
   c' \leftarrow test \ i'
   bot \leftarrow cbr \ c' \ loop \ exit
   \_\leftarrow phi\ loop\ c\_i\ ti\ [(i',bot),(start,top)]
   setBlock\ exit
```

Figure 3.1.: Monadic generation of for loop

```
[11g|
define i64 @foo(i64 %start, i64 %end) {
  entry:
    br label %for

for:
    for i64 %i in %start to %end with i64 [0,%entry] as %x {
        %y = add i64 %i, %x
        ret i64 %y
    }
}
```

Figure 3.2.: For Loop using llvm-general-quote

```
define i64 @foo(i64 %start, i64 %end) {
entry:
  br label %for
for:
                                       ; preds = %for.body, %entry
  %i = phi i64 [ %i.new, %for.body ], [ %start, %entry ]
  %x = phi i64 [ %y, %for.body ], [ 0, %entry ]
  %for.cond = icmp ule i64 %i, %end
  \%i.new = add nuw nsw i64 \%i, 1
  br i1 %for.cond, label %for.body, label %for.end
for.end:
                                        ; preds = %for
  ret i64 %x
for.body:
                                        ; preds = %for
  %y = add i64 %i, %x
  br label %for
}
```

Figure 3.3.: Expanded For Loop

4. Conclusion

4.1. Related Work

A. Listings

Listings

```
; Function Attrs: nounwind readonly
    \mathbf{define} \ \mathbf{double} \ @sum(\mathbf{double*} \ \mathbf{nocapture} \ \mathbf{readonly} \ \% \mathbf{a} \,, \ \mathbf{i32} \ \% l \, \mathbf{ength} \,) \ \#0 \ \{
2
      \%1 = icmp \ sgt \ i32 \ \%length, 0
3
      br i1 %1, label %.lr.ph.preheader, label %. crit edge
4
5
    .lr.ph.preheader:
                                                                  ; preds = \%0
6
      \%2 = \mathbf{add} \ \mathbf{i32} \ \% length , \ -1
7
      \%3 = \mathbf{zext} \ \mathbf{i32} \ \%2 \ \mathbf{to} \ \mathbf{i64}
8
      \%4 = add i64 \%3, 1
10
      \%end.idx = add i64 %3, 1
      %n.vec = and i64 %4, 8589934576
11
      \%cmp.zero = icmp eq i64 \%n.vec, 0
12
      br i1 %cmp.zero, label %middle.block, label %vector.body
13
14
                                                                 ; preds = \%
    vector.body:
15
        . lr. ph. preheader \ , \ \% vector. body
      %index = phi i64 [ %index.next, %vector.body ], [ 0, %.lr.ph.preheader ]
16
      \%vec.phi = phi <4 x double> [ %13, %vector.body ],
17
                                         [ zeroinitializer, %.lr.ph.preheader ]
18
      %vec.phi6 = phi <4 x double> [ %14, %vector.body ],
19
                                            zeroinitializer, %.lr.ph.preheader
20
21
      \%vec.phi7 = phi <4 x double>
                                            %15, %vector.body ],
                                            zeroinitializer, %.lr.ph.preheader ]
22
      \% vec.phi8 = phi < 4 x double> [
                                            %16, %vector.body ],
23
                                          [\ \ zeroinitializer\ ,\ \%.lr.ph.preheader\ ]
24
      \%5 = \mathbf{getelementptr} \ \mathbf{double}* \ \%a\,, \ \mathbf{i64} \ \% \mathbf{index}
25
      \%6 = bitcast double* \%5 to <4 x double>*
26
      \%wide.load = load <4 x double>* \%6, align 8
27
      \%.sum22 = or i64 \%index, 4
28
      %7 = getelementptr double* %a, i64 %.sum22
29
      \%8 = bitcast double* \%7 to <4 x double>*
      \%wide.load9 = load <4 x double>* \%8, align 8
31
      \%.sum23 = or i64 \%index, 8
32
      %9 = getelementptr double* %a, i64 %.sum23
33
      \%10 = bitcast double* \%9 to <4 x double>*
34
      \%wide.load10 = load <4 x double>* \%10, align 8
35
      \%.sum24 = or i64 \%index, 12
36
      %11 = getelementptr double* %a, i64 %.sum24
37
      \%12 = bitcast double* \%11 to <4 x double>*
38
      \%wide.load11 = load <4 x double>* \%12, align 8
39
      %13 = fadd <4 x double> %vec.phi, %wide.load
      \%14 = fadd < 4 x double > \%vec.phi6, \%wide.load9
41
      \%15 = \mathbf{fadd} < 4 \times \mathbf{double} > \% vec.phi7, \% wide.load10
42
      \%16 = fadd < 4 x double > \%vec.phi8, \%wide.load11
```

```
\%index.next = add i64 \%index, 16
44
     \%17 = icmp eq i64 \%index.next, \%n.vec
45
     br i1 %17, label %middle.block, label %vector.body, !llvm.loop !0
46
47
                                                         ; preds = \%vector.body, %
   middle.block:
48
       .lr.ph.preheader
     %resume.val = phi i64 [ 0, %.lr.ph.preheader ], [ %n.vec, %vector.body ]
49
     %rdx.vec.exit.phi = phi <4 x double> [ zeroinitializer, %.lr.ph.preheader
50
          ],
                                             [ %13, %vector.body ]
51
     %rdx.vec.exit.phi14 = phi <4 x double> [ zeroinitializer, %
52
         .lr.ph.preheader ],
                                                [ %14, %vector.body ]
53
     %rdx.vec.exit.phi15 = phi < 4 x double > [ zeroinitializer, %]
         .lr.ph.preheader ],
                                                [ %15, %vector.body ]
55
     % rdx.vec.exit.phi16 = phi < 4 x double > [ zeroinitializer, %]
56
         .lr.ph.preheader ],
                                               [ %16, %vector.body ]
57
     %bin.rdx = fadd <4 x double> %rdx.vec.exit.phi14, %rdx.vec.exit.phi
58
     %bin.rdx17 = fadd <4 x double> %rdx.vec.exit.phi15, %bin.rdx
59
     %bin.rdx18 = fadd <4 x double> %rdx.vec.exit.phi16, %bin.rdx17
60
     %rdx.shuf = shufflevector <4 x double> %bin.rdx18, <4 x double> undef, <4
61
          x i32> <i32 2, i32 3, i32 undef, i32 undef>
     \%bin.rdx19 = fadd <4 x double> \%bin.rdx18, \%rdx.shuf
62
     %rdx.shuf20 = shufflevector <4 x double> %bin.rdx19, <4 x double> undef,
63
         <4 x i32><i32 1, i32 undef, i32 undef, i32 undef>
     \%bin.rdx21 = fadd <4 x double> \%bin.rdx19, \%rdx.shuf20
64
     \%18 = \mathbf{extractelement} < 4 \ x \ \mathbf{double} > \% \mathrm{bin.rdx} 21 \ , \ \mathbf{i32} \ 0
65
     %cmp.n = icmp eq i64 %end.idx, %resume.val
66
     br i1 %cmp.n, label %. crit edge, label %.lr.ph
67
68
                                                         ; preds = \%middle.block,
69
   .lr.ph:
       \%. lr.ph
     \%indvars.iv = phi i64 [ \%indvars.iv.next , \%.lr.ph ] , [ \%resume.val , \%
         middle.block
     %x.01 = phi double [ %21, %.lr.ph ], [ %18, %middle.block ]
71
     %19 = getelementptr double* %a, i64 %indvars.iv
72
     \%20 = load double* \%19, align 8
73
     \%21 = fadd fast double \%x.01, \%20
74
     %indvars.iv.next = add i64 %indvars.iv, 1
75
     %lftr.wideiv1 = trunc i64 %indvars.iv.next to i32
76
     %exitcond2 = icmp eq i32 %lftr.wideiv1, %length
77
     br i1 %exitcond2, label %. crit edge, label %.lr.ph, !llvm.loop !3
78
                                                          ; preds = \%.lr.ph, %
   . crit edge:
80
       middle.block, %0
     %x.0.1cssa = phi double [ 0.0000000e+00, %0 ], [ %21, %.1r.ph ], [ %18, % ]
81
         middle.block ]
     ret double %x.0.lcssa
82
   }
83
84
   attributes #0 = { nounwind readonly }
85
86
```

```
87 | !0 = metadata !{metadata !0, metadata !1, metadata !2}

88 | !1 = metadata !{metadata !"llvm.vectorizer.width", i32 1}

89 | !2 = metadata !{metadata !"llvm.vectorizer.unroll", i32 1}

90 | !3 = metadata !{metadata !3, metadata !1, metadata !2}
```

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