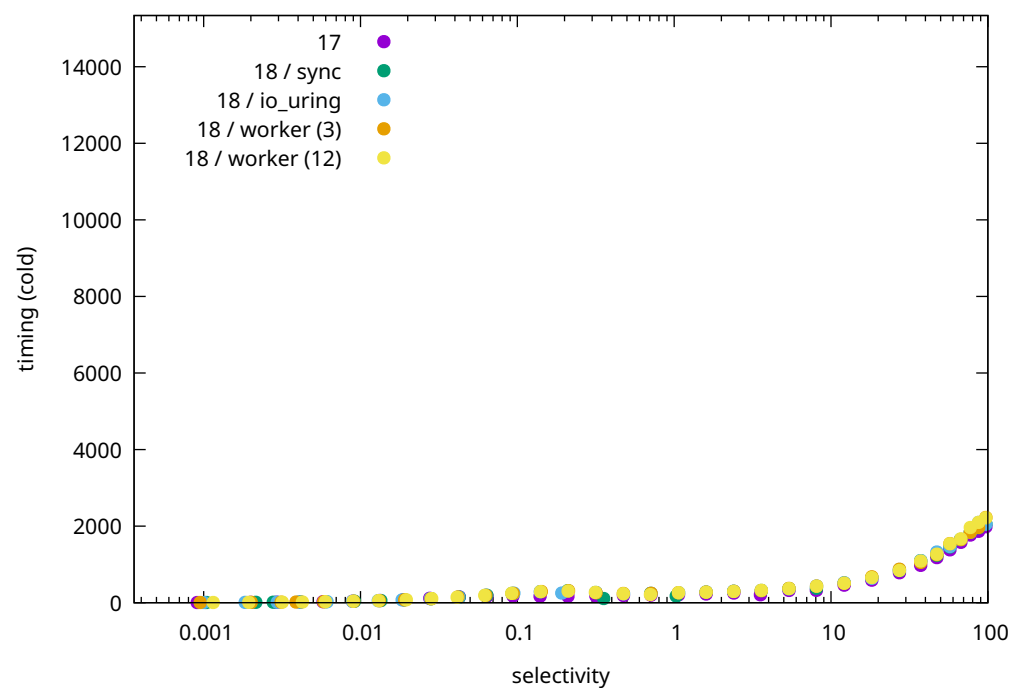
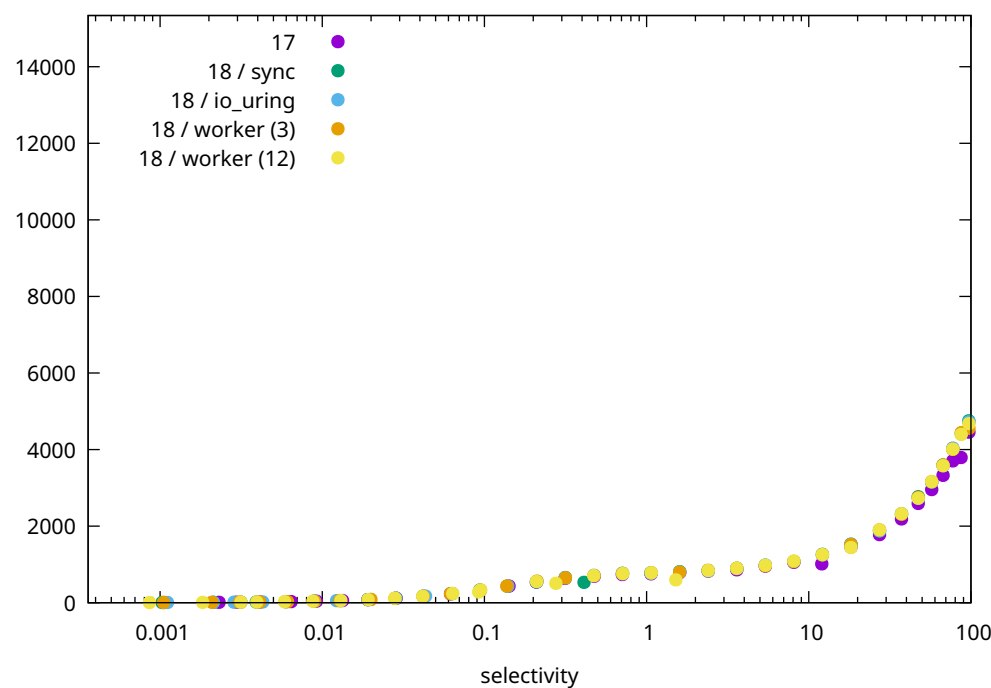


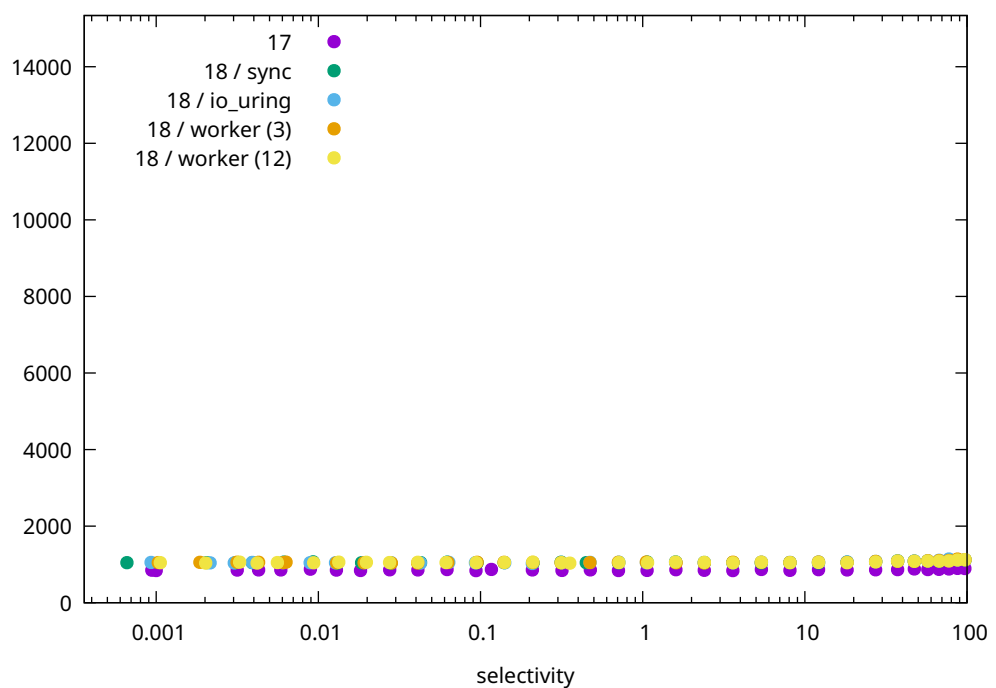
**cyclic\_1 / 0 / bitmaps**



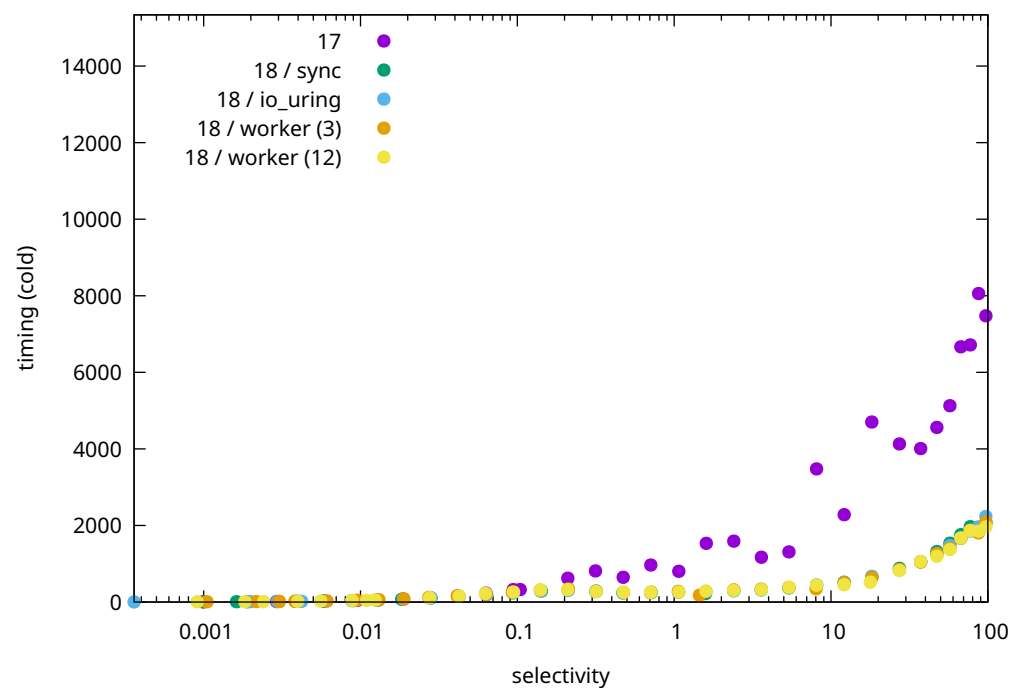
**cyclic\_1 / indexscan / eic=0**



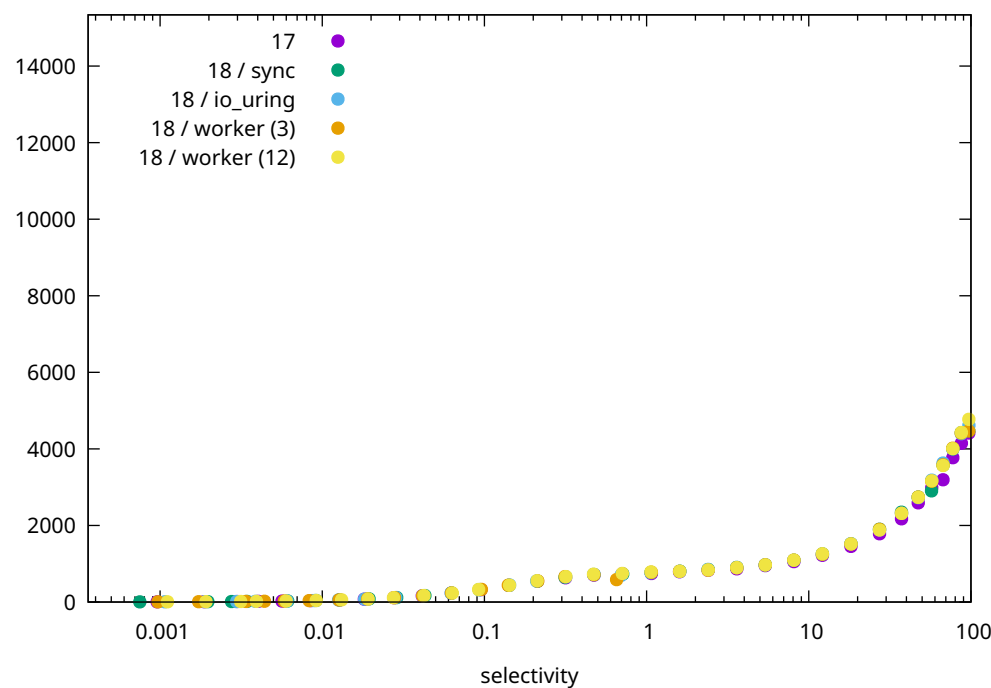
**cyclic\_1 / seqscan / eic=0**



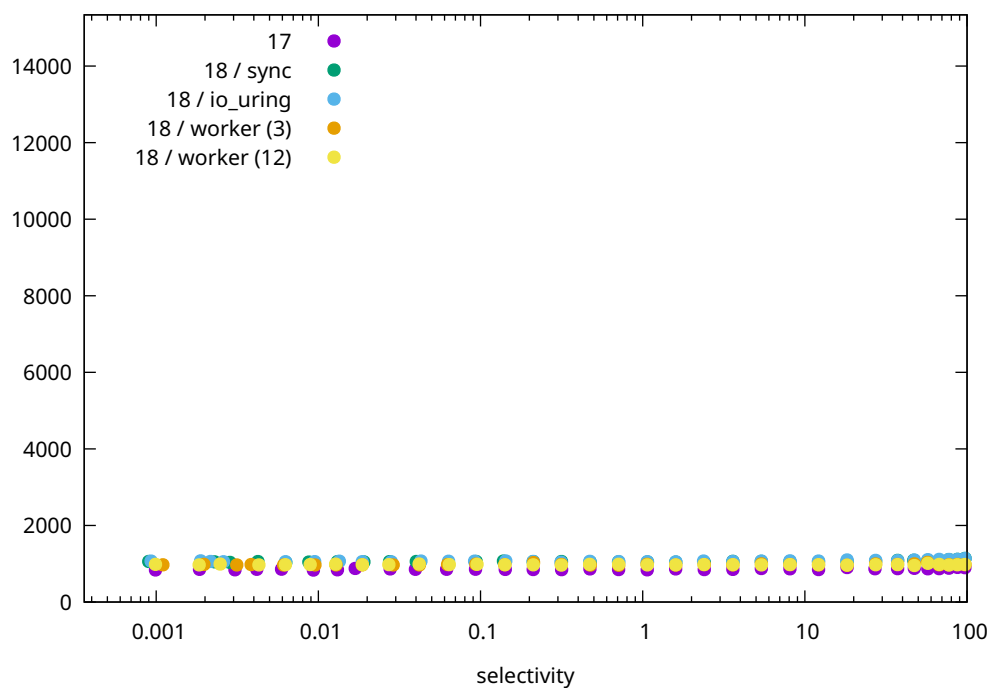
**cyclic\_1 / 1 / bitmapscan**



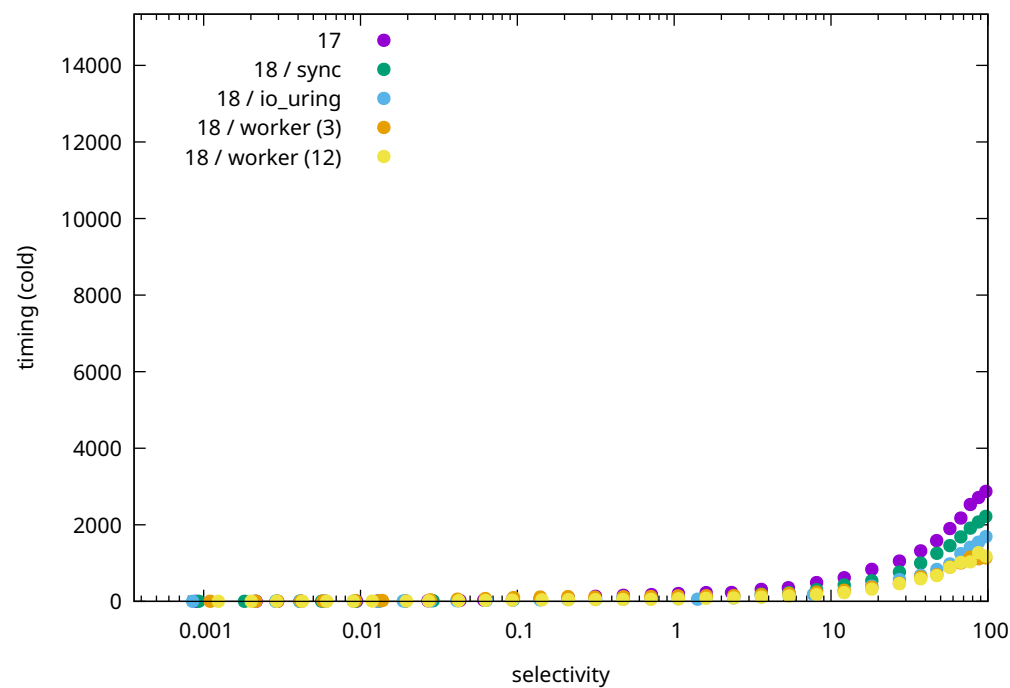
**cyclic\_1 / indexscan / eic=1**



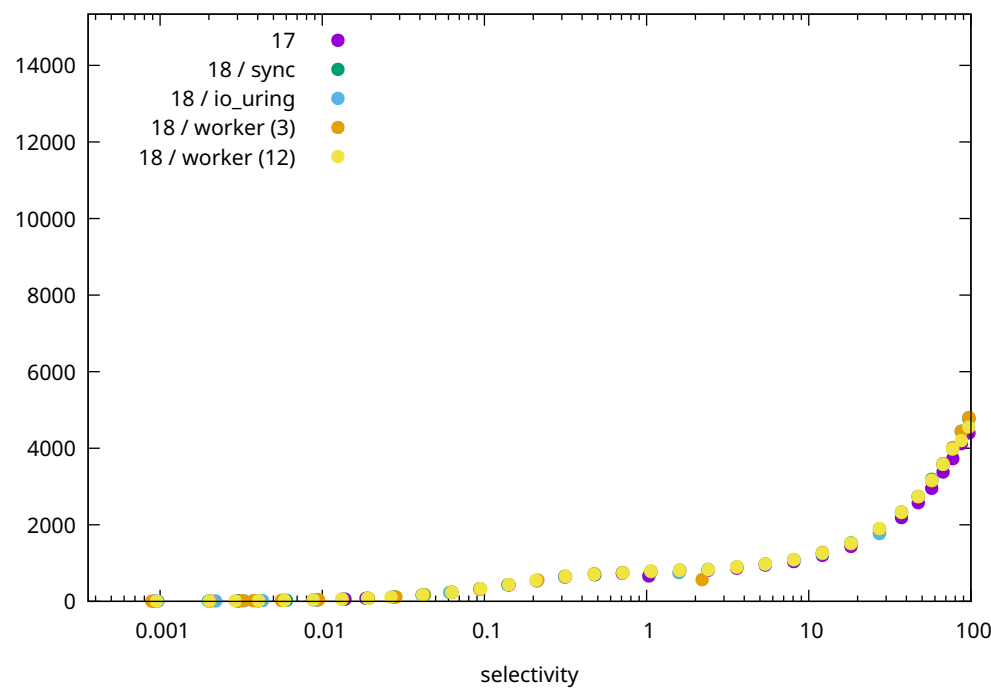
**cyclic\_1 / seqscan / eic=1**



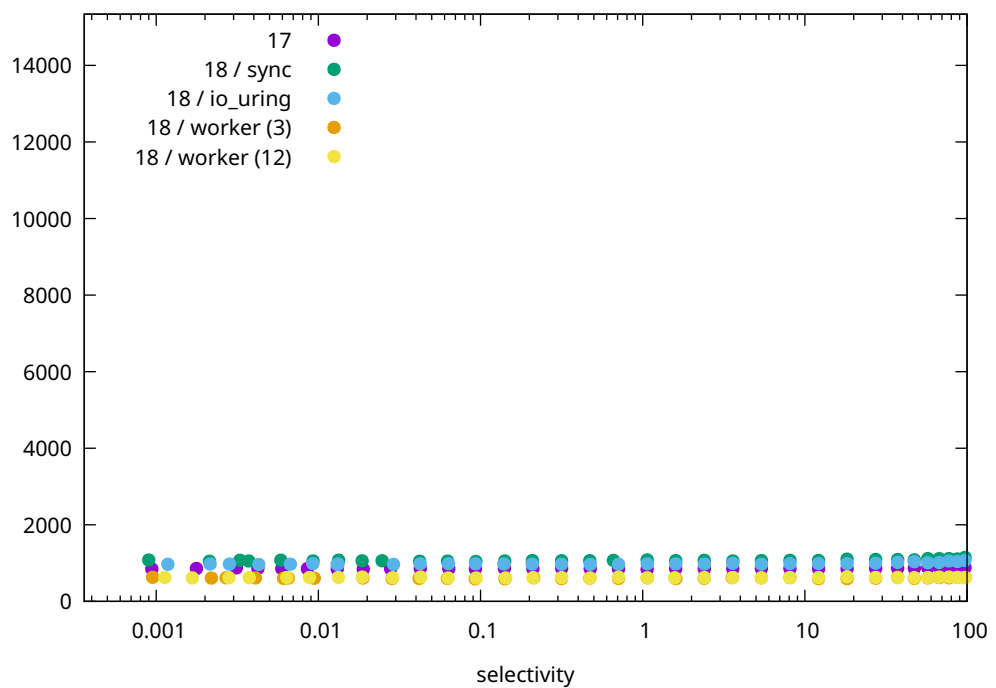
**cyclic\_1 / 16 / bitmapscan**



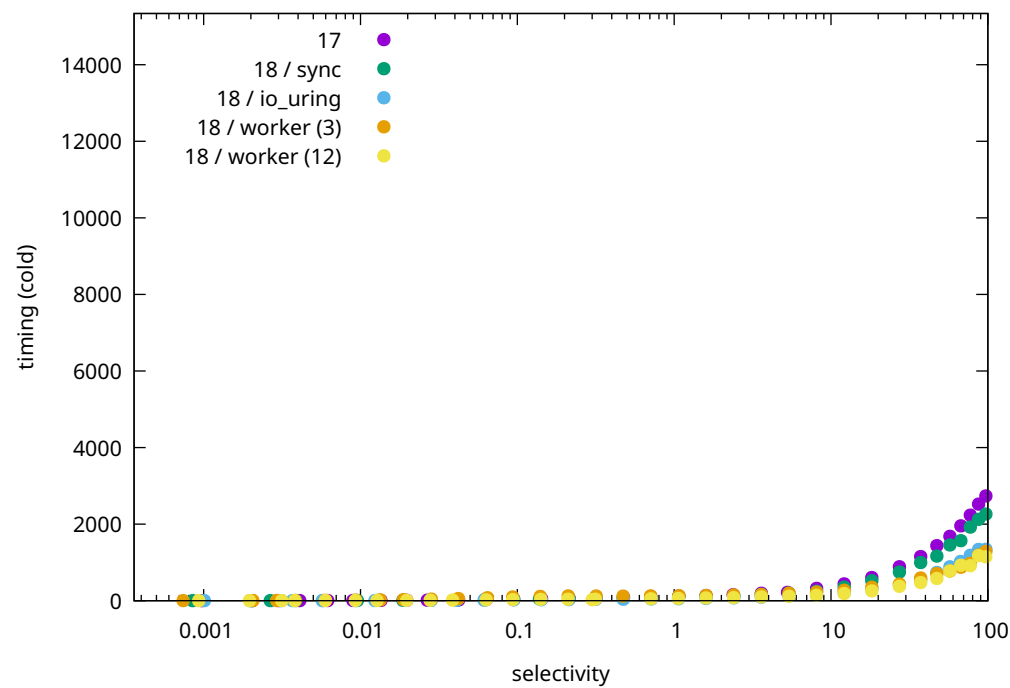
cyclic\_1 / indexscan / eic=16



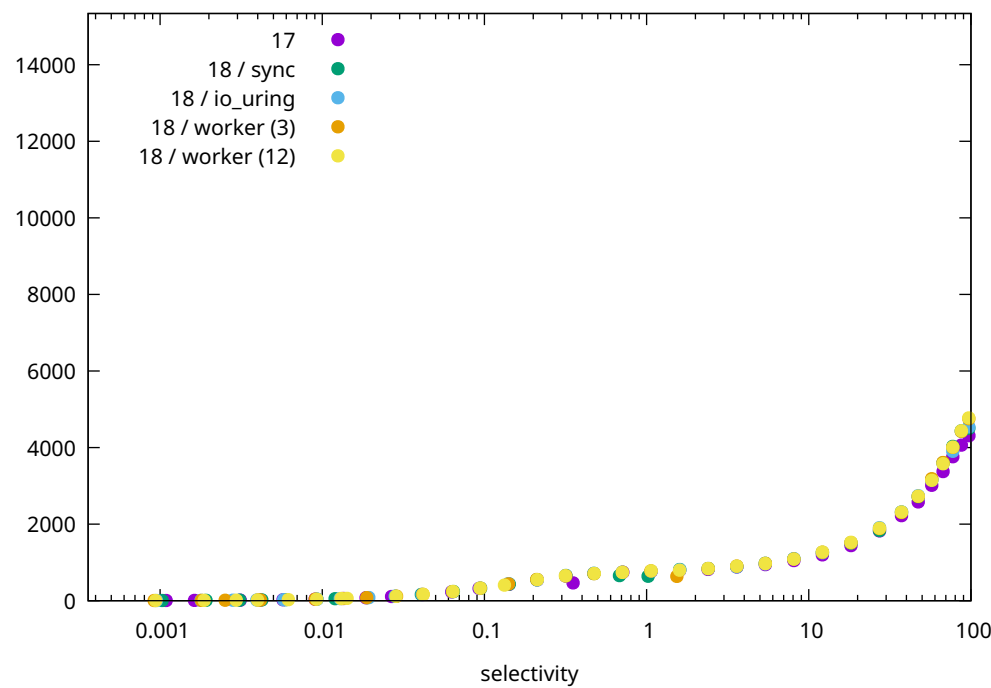
**cyclic\_1 / seqscan / eic=16**



**cyclic\_1 / 64 / bitmaps**



**cyclic\_1 / indexscan / eic=64**



**cyclic\_1 / seqscan / eic=64**

