

JSS Mahavidyapeetha
Sri Jayachamarajendra College of Engineering, Mysore 570 006
An Autonomous Institution Affiliated to
Visvesvaraya Technological University, Belgaum



Minimum Register Instruction Sequencing

Report submitted in partial fulfillment of the curriculum prescribed for
the award of the degree of

Bachelor of Engineering in Computer Science
by

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Under the Guidance of
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Department of Computer Science & Engineering
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CERTIFICATE

This is to certify that the work entitled "**Minimum Register Instruction Sequencing using LLVM Compiler**" is a bonafide work carried out by **Rajkiran Anthapur, Shashidhar G and Vikram T.V** in partial fulfillment of the award of the degree of **Bachelor of Engineering in Computer Science & Engineering of Visvesvaraya Technological University, Belgaum**, during the year **2010-2011**. It is certified that all corrections/suggestions indicated during CIE have been incorporated in the report. The project report has been approved as it satisfies the academic requirements in respect of the project work prescribed for the Bachelor of Engineering Degree.

Guide

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