

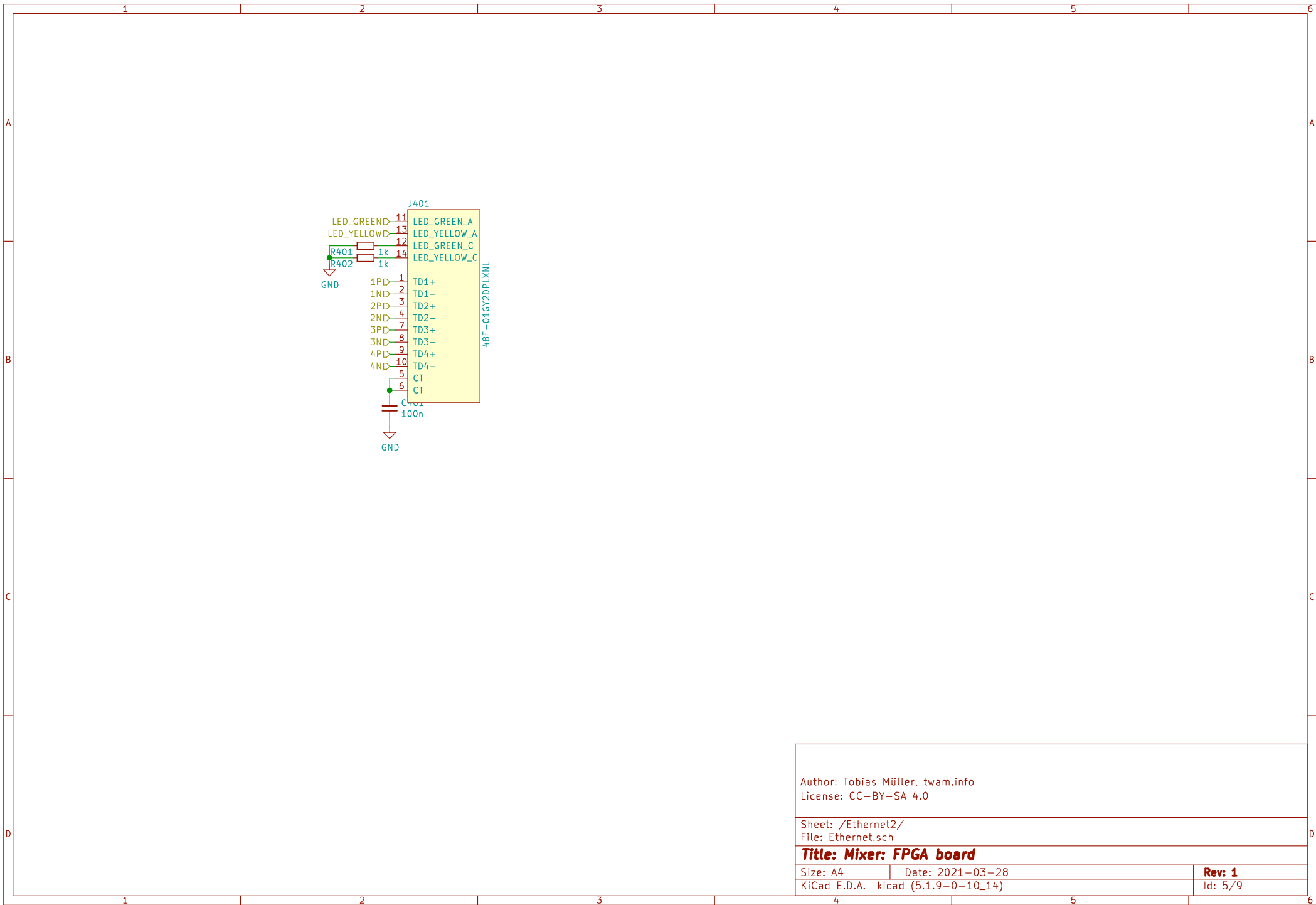
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Sheet: /Ethernet1/  
File: Ethernet.sch

**Title: Mixer: FPGA board**

Size: A4 Date: 2021-03-28  
KiCad E.D.A. kicad (5.1.9-0-10\_14)

**Rev: 1**  
Id: 4/9

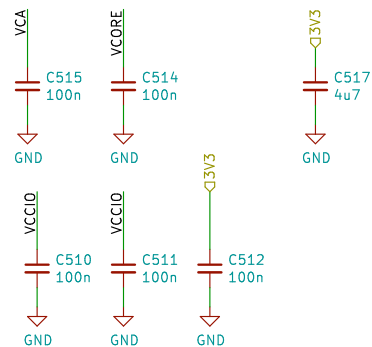
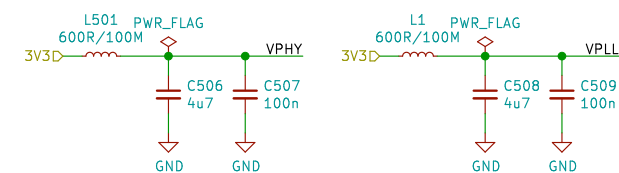


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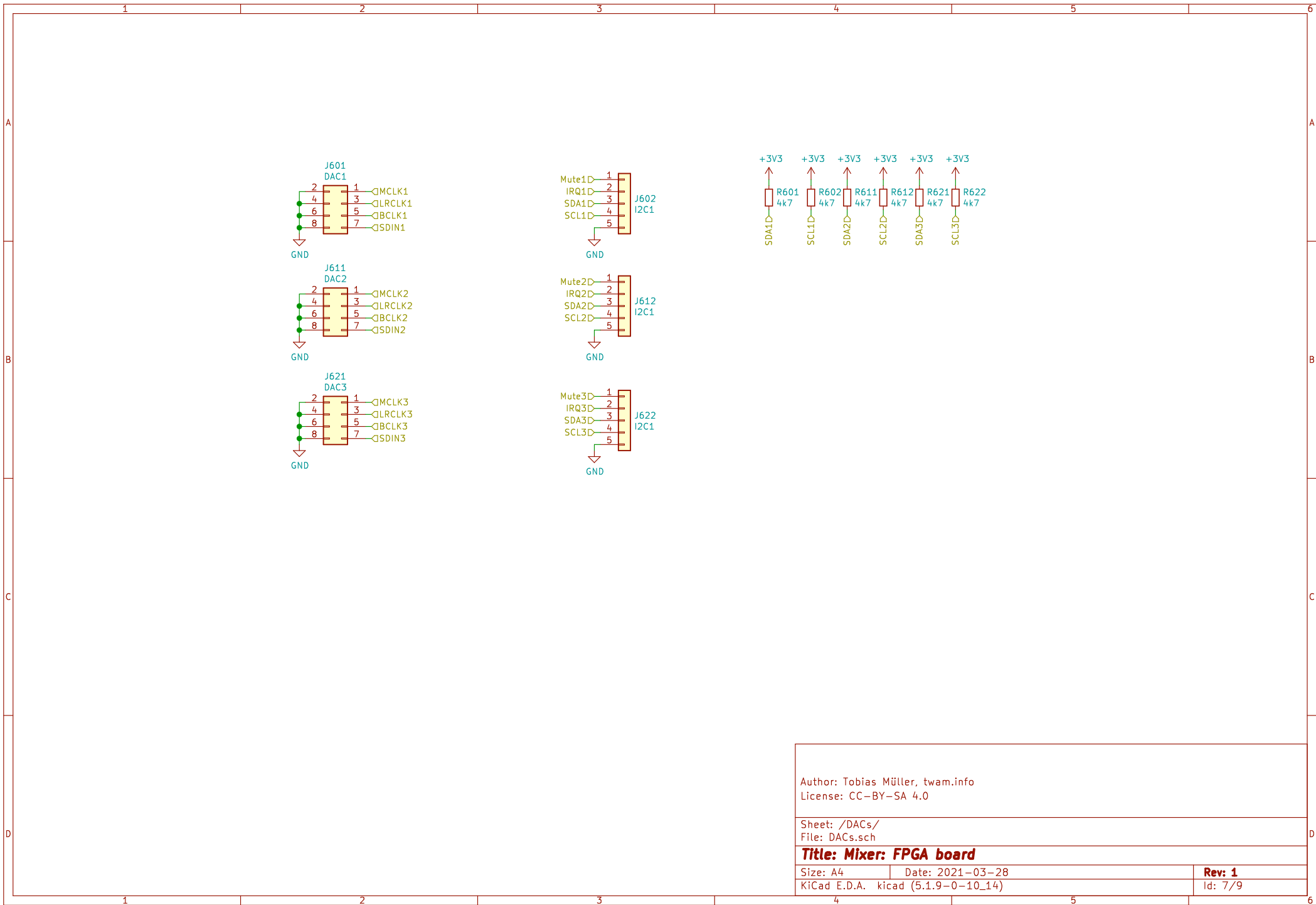
Sheet: /Ethernet2/  
File: Ethernet.sch

**Title: Mixer: FPGA board**

Size: A4	Date: 2021-03-28	Rev: 1
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Sheet: /DACs/  
File: DACs.sch

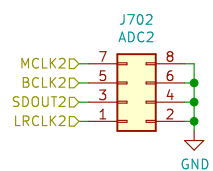
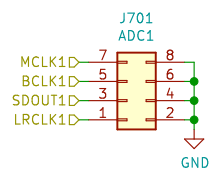
**Title: Mixer: FPGA board**

Size: A4 Date: 2021-03-28

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Sheet: /ADCs/  
File: ADCs.sch

**Title: Mixer: FPGA board**

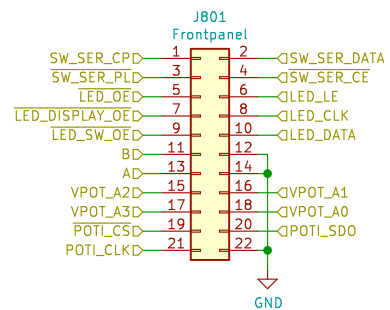
Size: A4 Date: 2021-03-28

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**Rev: 1**

Id: 8/9





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Sheet: /Frontpanel/  
File: Frontpanel.sch

**Title: Mixer: FPGA board**

Size: A4 Date: 2021-03-28

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**Rev: 1**

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