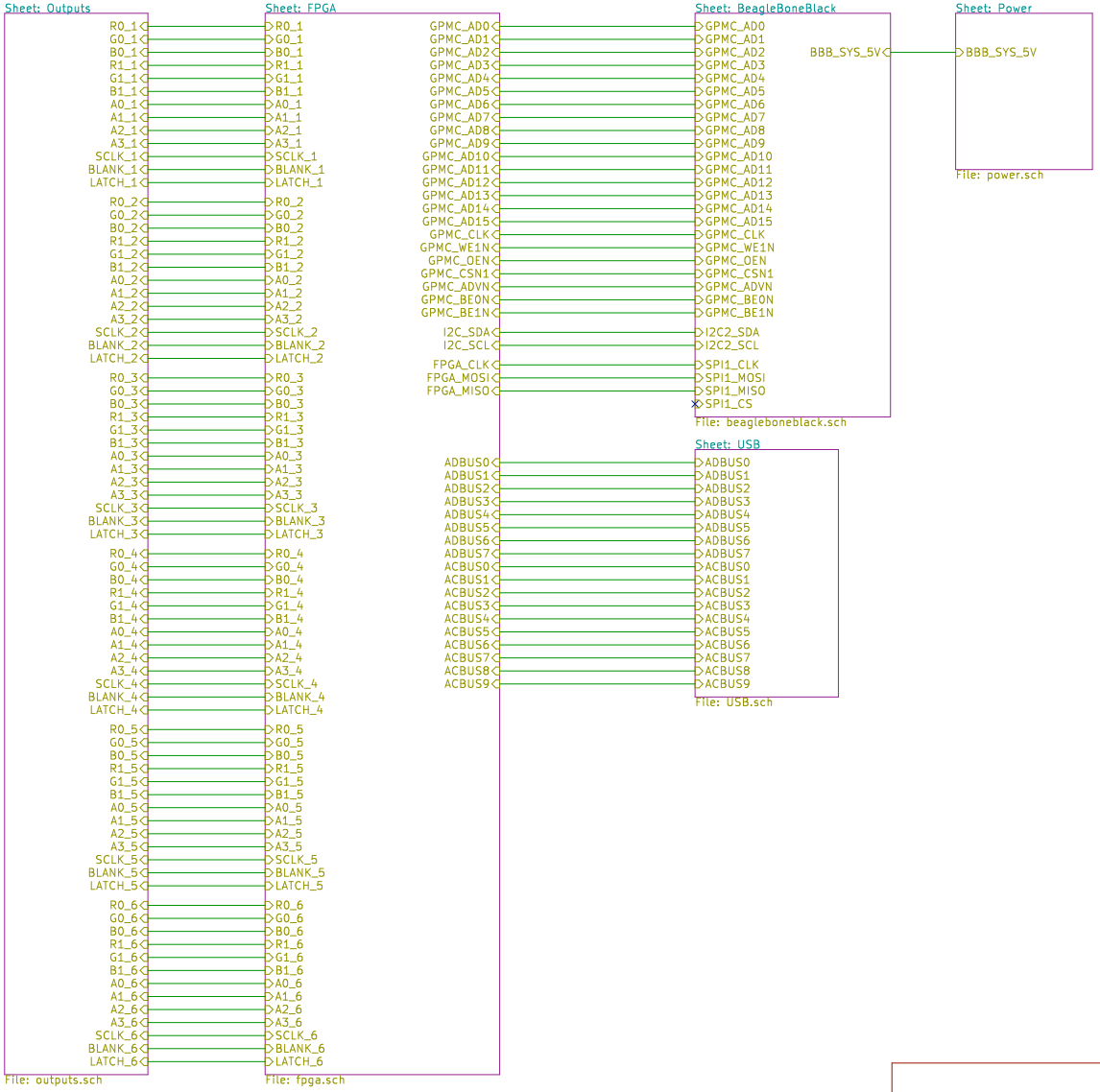

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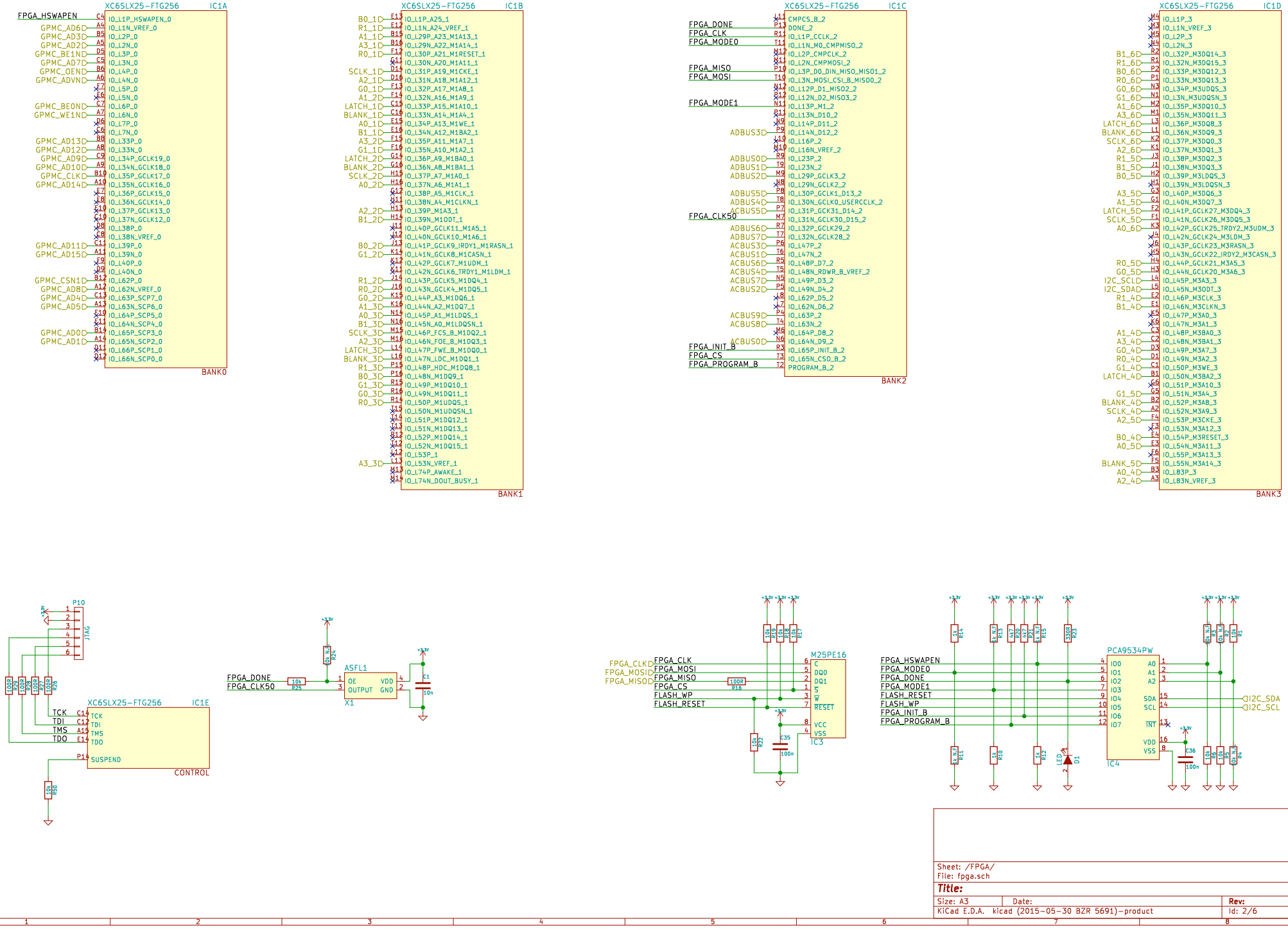
XC6SLX25-FTG256
IC1A
FPGA_HSWAPEN
GPMC_AD6
GPMC_AD3
GPMC_AD2
GPMC_BE1N
GPMC_AD7
GPMC_OEN
GPMC_ADVN
GPMC_BE0N
GPMC_WE1N
GPMC_AD13
GPMC_AD12
GPMC_AD9
GPMC_AD10
GPMC_CLK
GPMC_AD14
GPMC_AD11
GPMC_AD15
GPMC_CSN1
GPMC_AD8
GPMC_AD4
GPMC_AD5
GPMC_AD0
GPMC_AD1
BANK0
XC6SLX25-FTG256
IC1B
BANK1
XC6SLX25-FTG256
IC1C
FPGA_DONE
FPGA_CLK
FPGA_MODE0
FPGA_MISO
FPGA_MOSI
FPGA_MODE1
ADBUS3
ADBUS0
ADBUS1
ADBUS2
ADBUS5
ADBUS4
ACBUS5
FPGA_CLK50
ADBUS6
ADBUS7
ACBUS3
ACBUS1
ACBUS6
ACBUS4
ACBUS7
ACBUS2
ACBUS9
ACBUS8
ACBUS0
FPGA_INIT_B
FPGA_CS
FPGA_PROGRAM_B
BANK2
XC6SLX25-FTG256
IC1D
BANK3
P10
JTAG
XC6SLX25-FTG256
IC1E
TCK
TDI
TMS
TDO
SUSPEND
CONTROL
FPGA_DONE
FPGA_CLK50
ASFL1
OE
OUTPUT
VDD
GND
X1
M25PE16
FPGA_CLK
FPGA_MOSI
FPGA_MISO
FPGA_CS
FLASH_WP
FLASH_RESET
C
DQ0
DQ1
S
W
RESET
VCC
VSS
IC3
FPGA_HSWAPEN
FPGA_MODE0
FPGA_DONE
FPGA_MODE1
FLASH_RESET
FLASH_WP
FPGA_INIT_B
FPGA_PROGRAM_B
PCA9534PW
IO0
IO1
IO2
IO3
IO4
IO5
IO6
IO7
SDA
SCL
INT
VDD
VSS
IC4
I2C_SDA
I2C_SCL
LED
D1
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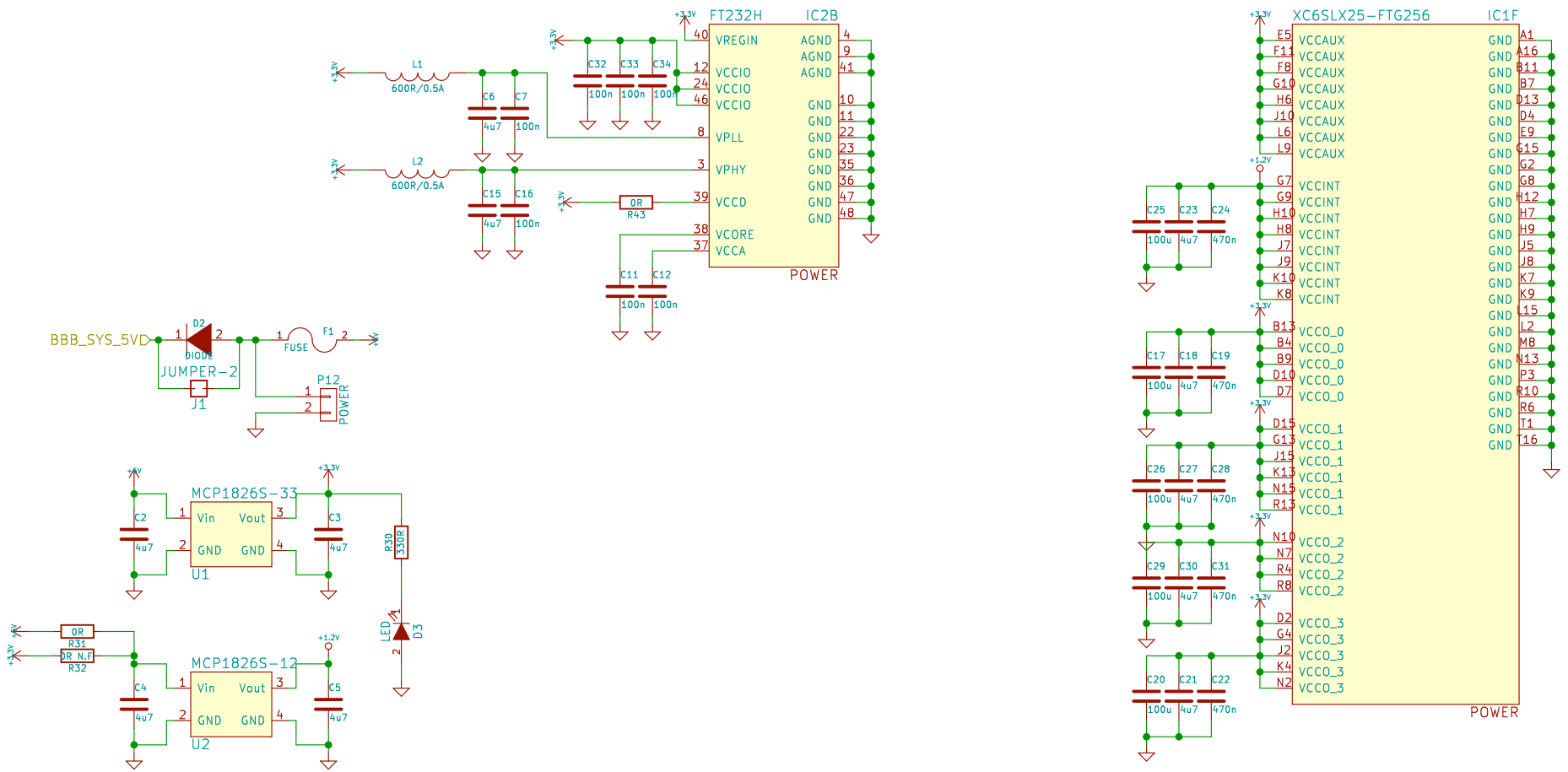

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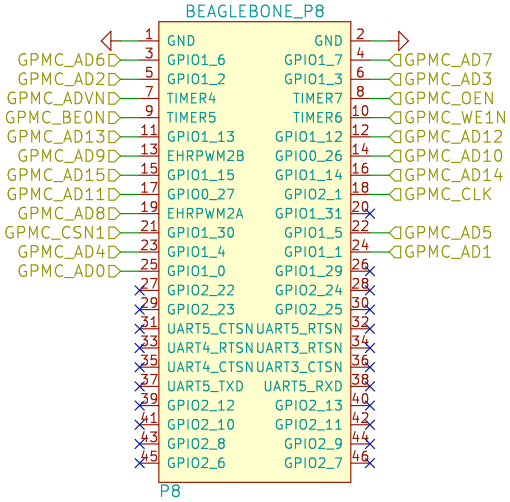

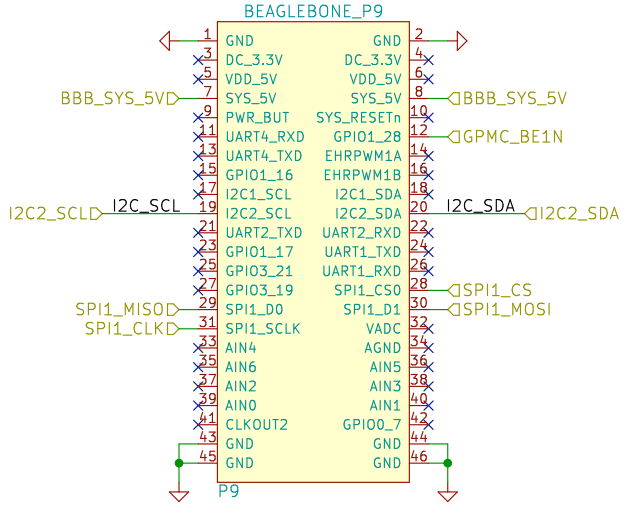

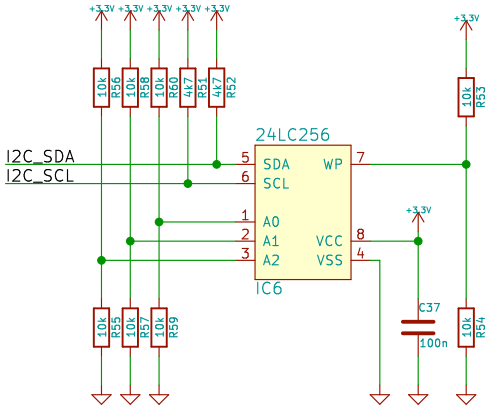

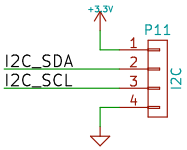

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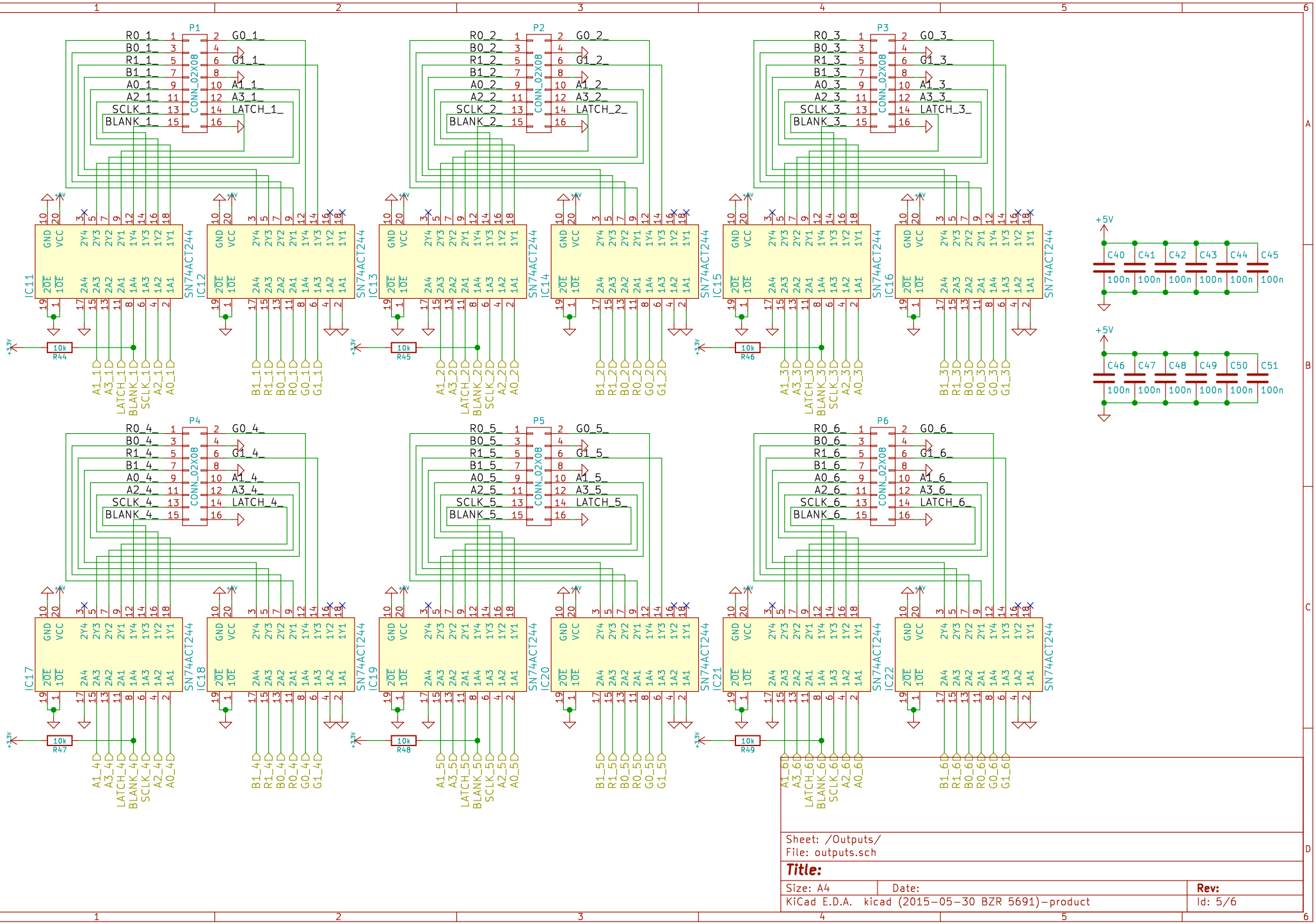
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B0_1_
R1_1_
B1_1_
A0_1_
A2_1_
SCLK_1_
BLANK_1_
G0_1_
G1_1_
A1_1_
A3_1_
LATCH_1_
CONN_02X08
P2
R0_2_
B0_2_
R1_2_
B1_2_
A0_2_
A2_2_
SCLK_2_
BLANK_2_
G0_2_
G1_2_
A1_2_
A3_2_
LATCH_2_
P3
R0_3_
B0_3_
R1_3_
B1_3_
A0_3_
A2_3_
SCLK_3_
BLANK_3_
G0_3_
G1_3_
A1_3_
A3_3_
LATCH_3_
P4
R0_4_
B0_4_
R1_4_
B1_4_
A0_4_
A2_4_
SCLK_4_
BLANK_4_
G0_4_
G1_4_
A1_4_
A3_4_
LATCH_4_
P5
R0_5_
B0_5_
R1_5_
B1_5_
A0_5_
A2_5_
SCLK_5_
BLANK_5_
G0_5_
G1_5_
A1_5_
A3_5_
LATCH_5_
P6
R0_6_
B0_6_
R1_6_
B1_6_
A0_6_
A2_6_
SCLK_6_
BLANK_6_
G0_6_
G1_6_
A1_6_
A3_6_
LATCH_6_
IC11
IC12
IC13
IC14
IC15
IC16
IC17
IC18
IC19
IC20
IC21
IC22
SN74ACT244
GND
VCC
2OE
1OE
2Y4
2Y3
2Y2
2Y1
1Y4
1Y3
1Y2
1Y1
2A4
2A3
2A2
2A1
1A4
1A3
1A2
1A1
+5V
+3V
10k
R44
R45
R46
R47
R48
R49
C40
C41
C42
C43
C44
C45
C46
C47
C48
C49
C50
C51
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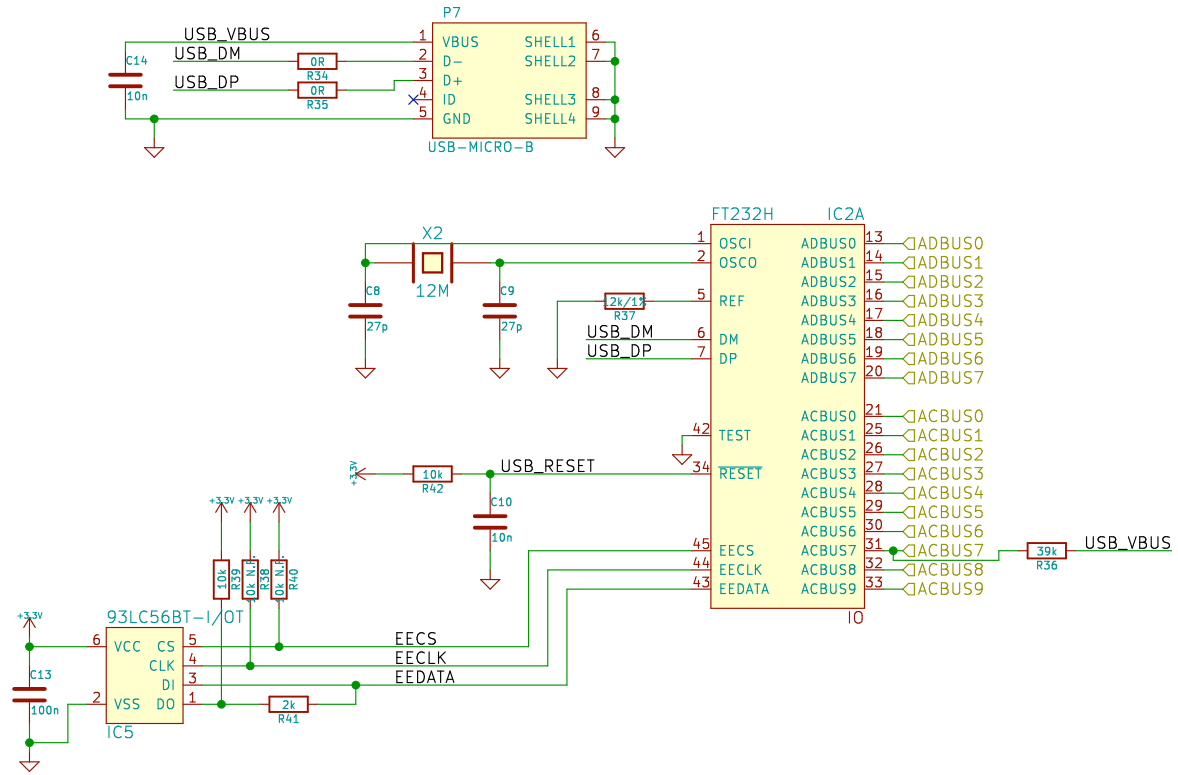

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