

Sheet: Outputs

R0_1_C	▷R0_1
G0_1_C	▷G0_1
B0_1_C	▷B0_1
R1_1_C	▷R1_1
G1_1_C	▷G1_1
B1_1_C	▷B1_1
A0_1_C	▷A0_1
A1_1_C	▷A1_1
A2_1_C	▷A2_1
A3_1_C	▷A3_1
SCLK_1_C	▷SCLK_1
BLANK_1_C	▷BLANK_1
LATCH_1_C	▷LATCH_1
R0_2_C	▷R0_2
G0_2_C	▷G0_2
B0_2_C	▷B0_2
R1_2_C	▷R1_2
G1_2_C	▷G1_2
B1_2_C	▷B1_2
A0_2_C	▷A0_2
A1_2_C	▷A1_2
A2_2_C	▷A2_2
A3_2_C	▷A3_2
SCLK_2_C	▷SCLK_2
BLANK_2_C	▷BLANK_2
LATCH_2_C	▷LATCH_2
R0_3_C	▷R0_3
G0_3_C	▷G0_3
B0_3_C	▷B0_3
R1_3_C	▷R1_3
G1_3_C	▷G1_3
B1_3_C	▷B1_3
A0_3_C	▷A0_3
A1_3_C	▷A1_3
A2_3_C	▷A2_3
A3_3_C	▷A3_3
SCLK_3_C	▷SCLK_3
BLANK_3_C	▷BLANK_3
LATCH_3_C	▷LATCH_3
R0_4_C	▷R0_4
G0_4_C	▷G0_4
B0_4_C	▷B0_4
R1_4_C	▷R1_4
G1_4_C	▷G1_4
B1_4_C	▷B1_4
A0_4_C	▷A0_4
A1_4_C	▷A1_4
A2_4_C	▷A2_4
A3_4_C	▷A3_4
SCLK_4_C	▷SCLK_4
BLANK_4_C	▷BLANK_4
LATCH_4_C	▷LATCH_4
R0_5_C	▷R0_5
G0_5_C	▷G0_5
B0_5_C	▷B0_5
R1_5_C	▷R1_5
G1_5_C	▷G1_5
B1_5_C	▷B1_5
A0_5_C	▷A0_5
A1_5_C	▷A1_5
A2_5_C	▷A2_5
A3_5_C	▷A3_5
SCLK_5_C	▷SCLK_5
BLANK_5_C	▷BLANK_5
LATCH_5_C	▷LATCH_5
R0_6_C	▷R0_6
G0_6_C	▷G0_6
B0_6_C	▷B0_6
R1_6_C	▷R1_6
G1_6_C	▷G1_6
B1_6_C	▷B1_6
A0_6_C	▷A0_6
A1_6_C	▷A1_6
A2_6_C	▷A2_6
A3_6_C	▷A3_6
SCLK_6_C	▷SCLK_6
BLANK_6_C	▷BLANK_6
LATCH_6_C	▷LATCH_6

File: outputs.sch

Sheet: FPGA

▷R0_1	▷G0_1	▷B0_1	▷R1_1	▷G1_1	▷B1_1	▷A0_1	▷A1_1	▷A2_1	▷A3_1	▷SCLK_1	▷BLANK_1	▷LATCH_1
▷R0_2	▷G0_2	▷B0_2	▷R1_2	▷G1_2	▷B1_2	▷A0_2	▷A1_2	▷A2_2	▷A3_2	▷SCLK_2	▷BLANK_2	▷LATCH_2
▷R0_3	▷G0_3	▷B0_3	▷R1_3	▷G1_3	▷B1_3	▷A0_3	▷A1_3	▷A2_3	▷A3_3	▷SCLK_3	▷BLANK_3	▷LATCH_3
▷R0_4	▷G0_4	▷B0_4	▷R1_4	▷G1_4	▷B1_4	▷A0_4	▷A1_4	▷A2_4	▷A3_4	▷SCLK_4	▷BLANK_4	▷LATCH_4
▷R0_5	▷G0_5	▷B0_5	▷R1_5	▷G1_5	▷B1_5	▷A0_5	▷A1_5	▷A2_5	▷A3_5	▷SCLK_5	▷BLANK_5	▷LATCH_5
▷R0_6	▷G0_6	▷B0_6	▷R1_6	▷G1_6	▷B1_6	▷A0_6	▷A1_6	▷A2_6	▷A3_6	▷SCLK_6	▷BLANK_6	▷LATCH_6

File: fpga.sch

GPMC_AD0_C	▷GPMC_AD0
GPMC_AD1_C	▷GPMC_AD1
GPMC_AD2_C	▷GPMC_AD2
GPMC_AD3_C	▷GPMC_AD3
GPMC_AD4_C	▷GPMC_AD4
GPMC_AD5_C	▷GPMC_AD5
GPMC_AD6_C	▷GPMC_AD6
GPMC_AD7_C	▷GPMC_AD7
GPMC_AD8_C	▷GPMC_AD8
GPMC_AD9_C	▷GPMC_AD9
GPMC_AD10_C	▷GPMC_AD10
GPMC_AD11_C	▷GPMC_AD11
GPMC_AD12_C	▷GPMC_AD12
GPMC_AD13_C	▷GPMC_AD13
GPMC_AD14_C	▷GPMC_AD14
GPMC_AD15_C	▷GPMC_AD15
GPMC_CLK_C	▷GPMC_CLK
GPMC_WE1N_C	▷GPMC_WE1N
GPMC_OEN_C	▷GPMC_OEN
GPMC_CSN1_C	▷GPMC_CSN1
GPMC_ADVN_C	▷GPMC_ADVN
GPMC_BE0N_C	▷GPMC_BE0N
GPMC_BE1N_C	▷GPMC_BE1N
I2C_SDA_C	▷I2C2_SDA
I2C_SCL_C	▷I2C2_SCL
FPGA_CLK_C	▷SPI1_CLK
FPGA_MOSI_C	▷SPI1_MOSI
FPGA_MISO_C	▷SPI1_MISO

ADBUS0_C	▷ADBUS0
ADBUS1_C	▷ADBUS1
ADBUS2_C	▷ADBUS2
ADBUS3_C	▷ADBUS3
ADBUS4_C	▷ADBUS4
ADBUS5_C	▷ADBUS5
ADBUS6_C	▷ADBUS6
ADBUS7_C	▷ADBUS7
ACBUS0_C	▷ACBUS0
ACBUS1_C	▷ACBUS1
ACBUS2_C	▷ACBUS2
ACBUS3_C	▷ACBUS3
ACBUS4_C	▷ACBUS4
ACBUS5_C	▷ACBUS5
ACBUS6_C	▷ACBUS6
ACBUS7_C	▷ACBUS7
ACBUS8_C	▷ACBUS8
ACBUS9_C	▷ACBUS9

Sheet: BeagleBoneBlack

▷GPMC_AD0	▷GPMC_AD1	▷GPMC_AD2	▷GPMC_AD3	▷GPMC_AD4	▷GPMC_AD5	▷GPMC_AD6	▷GPMC_AD7	▷GPMC_AD8	▷GPMC_AD9	▷GPMC_AD10	▷GPMC_AD11	▷GPMC_AD12	▷GPMC_AD13	▷GPMC_AD14	▷GPMC_AD15	▷GPMC_CLK	▷GPMC_WE1N	▷GPMC_OEN	▷GPMC_CSN1	▷GPMC_ADVN	▷GPMC_BE0N	▷GPMC_BE1N	▷I2C2_SDA	▷I2C2_SCL	▷SPI1_CLK	▷SPI1_MOSI	▷SPI1_MISO	✗SPI1_CS
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File: beagleboneblack.sch

Sheet: USB

▷ADBUS0	▷ADBUS1	▷ADBUS2	▷ADBUS3	▷ADBUS4	▷ADBUS5	▷ADBUS6	▷ADBUS7	▷ACBUS0	▷ACBUS1	▷ACBUS2	▷ACBUS3	▷ACBUS4	▷ACBUS5	▷ACBUS6	▷ACBUS7	▷ACBUS8	▷ACBUS9
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File: USB.sch

Sheet: Power

▷BBB_SYS_5V

File: power.sch

BBB_SYS_5V_C

▷BBB_SYS_5V

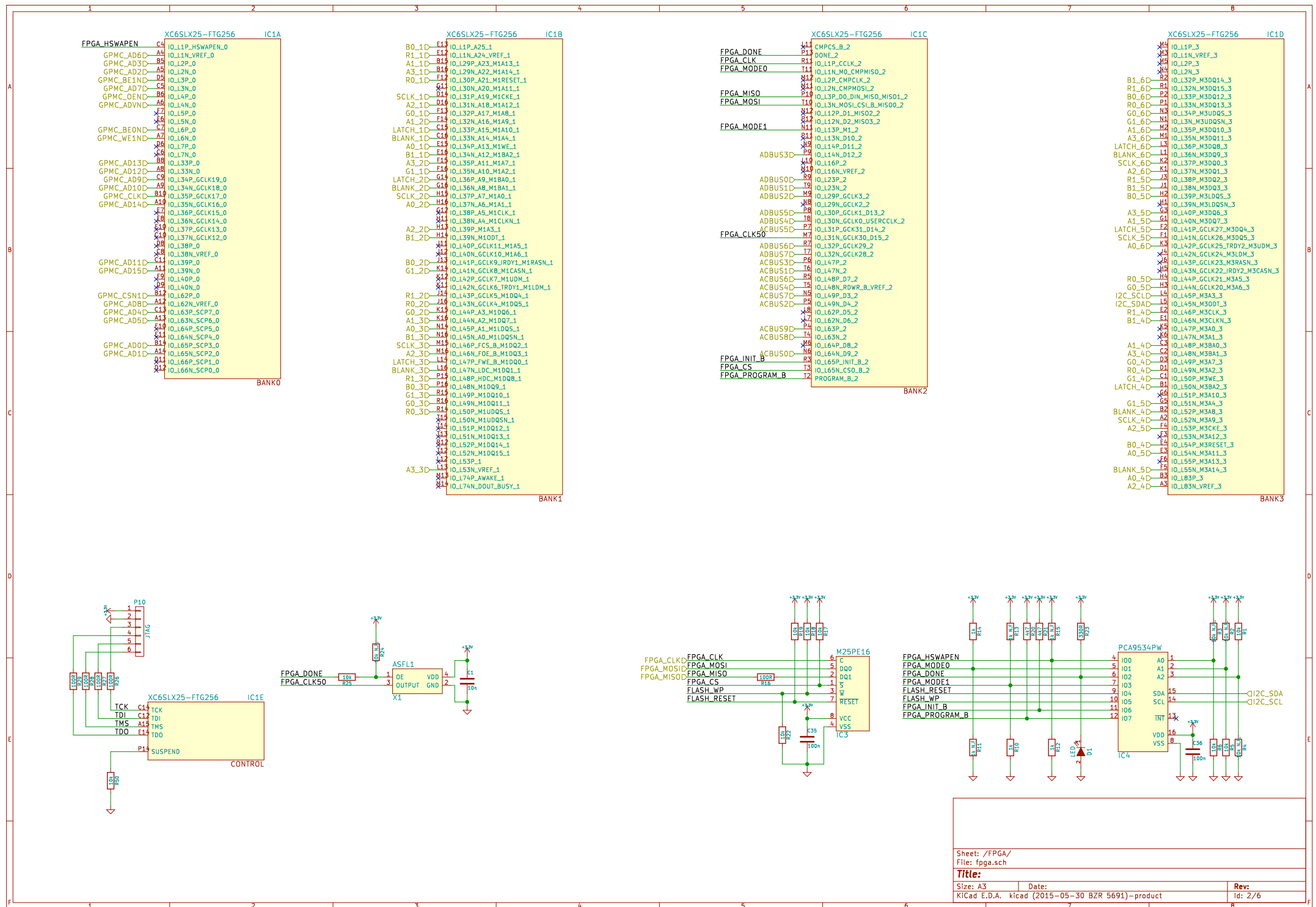
(c) 2015 Tobias Müller, twam.info

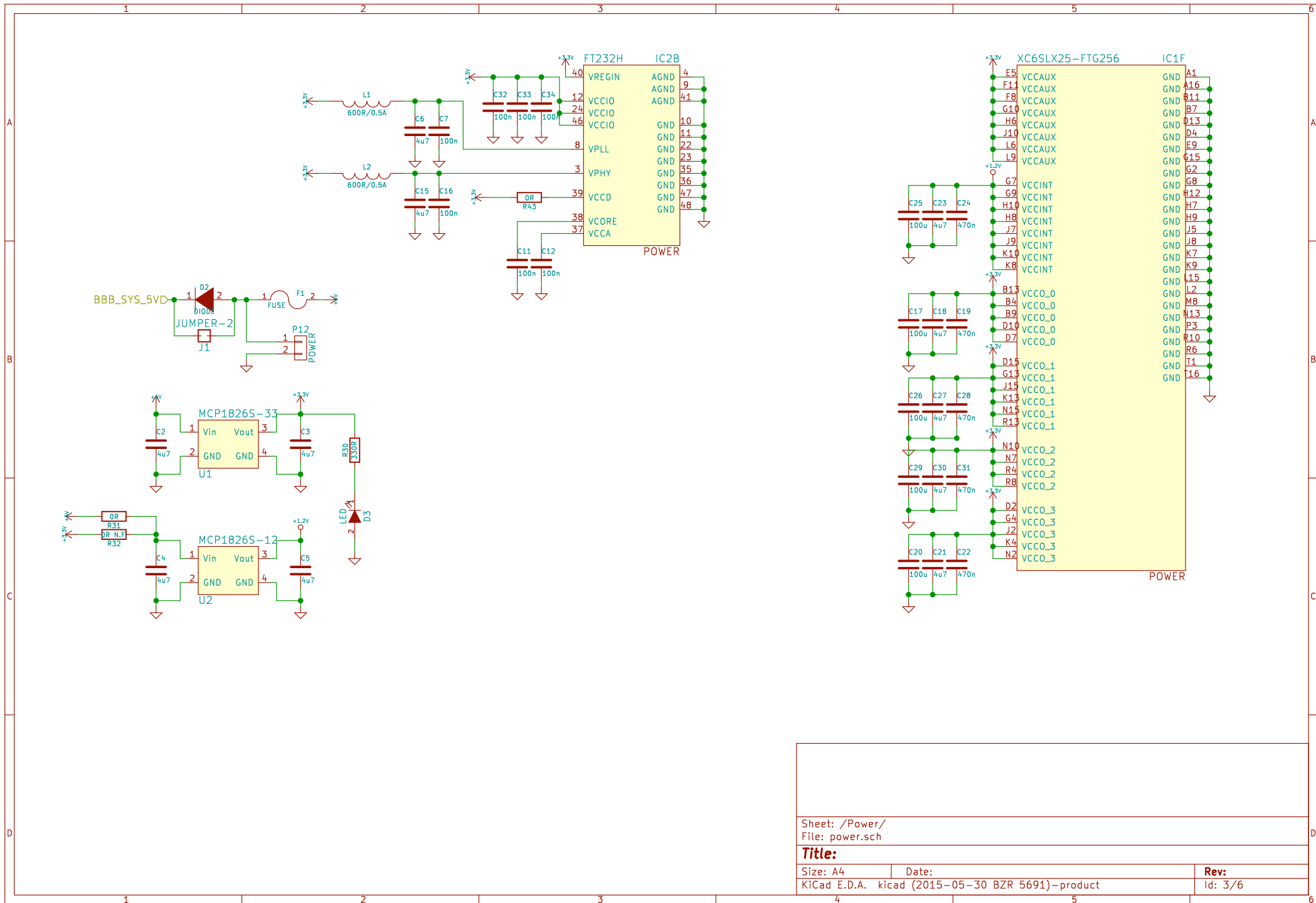
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Title: LED Display

Size: A3 | Date: 2015-09-06
KiCad E.D.A. kicad (2015-05-30 BZR 5691)-product

Rev: 2.0
Id: 1/6





Sheet: /Power/ File: power.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (2015-05-30 BZR 5691)-product		Id: 3/6

