# **BC847** series

45 V, 100 mA NPN general-purpose transistors

Rev. 12 — 24 October 2019

**Product data sheet** 

### 1. Product profile

### 1.1. General description

NPN general-purpose transistors in a small SOT23 (TO-236AB), very small SOT323 (SC-70) or ultra small SOT883 (DFN1006-3) Surface-Mounted Device (SMD) plastic package.

**Table 1. Product overview** 

Type number[1]	Package			umber[1] Package		PNP complement
	Nexperia	JEITA	JEDEC			
BC847	SOT23	-	TO-236AB	BC857		
BC847A				BC857A		
BC847B				BC857B		
BC847C				BC857C		
BC847W	SOT323	SC-70	-	BC857W		
BC847AW				BC857AW		
BC847BW				BC857BW		
BC847CW				BC857CW		
BC847AM	SOT883	T883 SC-101	-	BC857AM		
BC847BM	]			BC857BM		
BC847CM				BC857CM		

<sup>[1]</sup> Valid for all available selection groups.

### 1.2. Features and benefits

- · General-purpose transistors
- SMD plastic packages
- Three different gain selections
- AEC-Q101 qualified

### 1.3. Applications

General-purpose switching and amplification



### 45 V, 100 mA NPN general-purpose transistors

### 1.4. Quick reference data

### Table 2. Quick reference data

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	45	V
I <sub>C</sub>	collector current		-	-	100	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V;	110	-	800	
	h <sub>FE</sub> group A	I <sub>C</sub> = 2 mA	110	180	220	
	h <sub>FE</sub> group B		200	290	450	
	h <sub>FE</sub> group C		420	520	800	

## 2. Pinning information

**Table 3. Pinning information** 

Pin	Symbol	Descrition	Simlified outline	Graphic symbol
SOT23; SOT323	•			
1	В	base	3	С
2	Е	emitter		
3	С	collector		B — E sym123
SOT883	•	·		
1	В	base	1 🔲	С
2	Е	emitter	2 3	
3	С	collector	Transparent top view	B—————————————————————————————————————
				sym123

2/17

### 45 V, 100 mA NPN general-purpose transistors

## 3. Ordering information

**Table 4. Ordering information** 

Type number	Package					
	Name	Description	Version			
BC847	TO-236AB	plastic surface-mounted package; 3	SOT23			
BC847A		leads				
BC847B						
BC847C						
BC847W	SC-70		SOT323			
BC847AW						
BC847BW						
BC847CW						
BC847AM	SC-101	lesdless ultra small plastic package;	SOT 883			
BC847BM		3 solder lands; body 1.0 x 0.6 x 0.5 mm				
BC847CM	1					

## 4. Marking

Table 5. Marking codes

Type number		Marking code
BC847	[1]	1H%
BC847A	[1]	1E%
BC847B	[1]	1F%
BC847C	[1]	1G%
BC847W	[1]	1H%
BC847AW	[1]	1E%
BC847BW	[1]	1F%
BC847CW	[1]	1G%
BC847AM		D4
BC847BM		D5
BC847CM		D6

<sup>[1] % =</sup> placeholder for manufacturing site code

### 45 V, 100 mA NPN general-purpose transistors

### 5. Limiting values

#### **Table 6. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	50	V
$V_{CEO}$	collector-emitter voltage	open base		-	45	V
$V_{EBO}$	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p ≤ 1 ms</sub>		-	200	mA
I <sub>BM</sub>	peak base current	single pulse; t <sub>p ≤ 1 ms</sub>		-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C				
	SOT23		[1]	-	250	mW
	SOT323		[1]	-	200	mW
	SOT883		[2]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

### 6. Thermal characteristics

**Table 7. Thermal characteristics** 

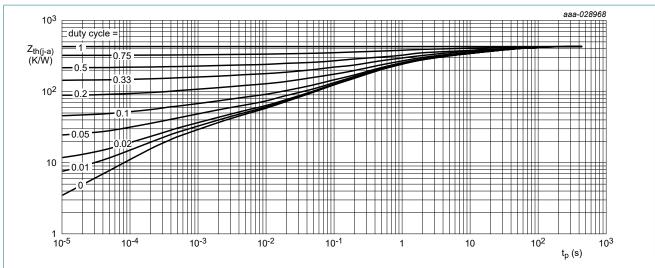
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air					
	SOT23		[1]	-	-	500	K/W
	SOT323		[1]	-	-	625	K/W
	SOT883		[2]	-	-	500	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.

<sup>[2]</sup> Device mounted on an PCB with 60 µm copper strip line, standard footprint.

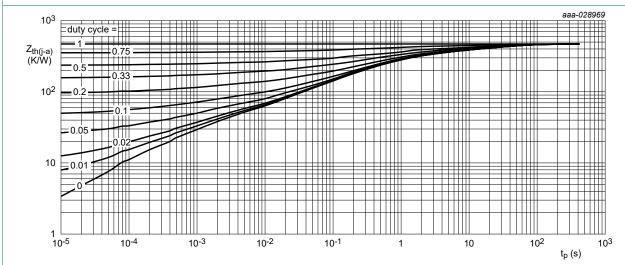
<sup>[2]</sup> Device mounted on an PCB with 60 µm copper strip line, standard footprint.

#### 45 V, 100 mA NPN general-purpose transistors



FR4 PCB; single-sided copper; tin-plated and standard footprint

Fig. 1. SOT23: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided copper; tin-plated and standard footprint

Fig. 2. SOT323: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

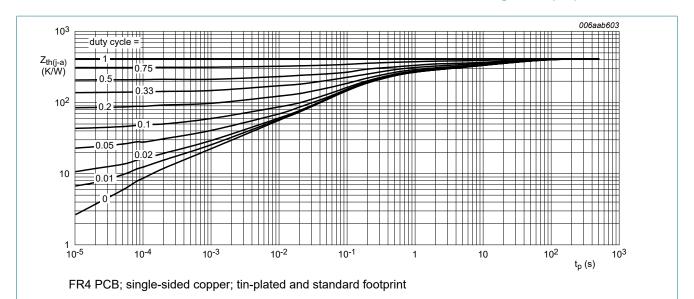


Fig. 3. SOT883: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

### 45 V, 100 mA NPN general-purpose transistors

### 7. Characteristics

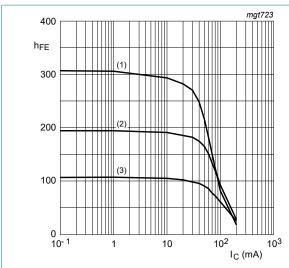
#### **Table 8. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A		50	-	-	V
V <sub>(BR)CES</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = 2 mA; V <sub>BE</sub> = 0 A		45	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	I <sub>C</sub> = 0 A; I <sub>E</sub> = 100 μA		6	-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A		-	-	15	nA
	cut-off current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}$		-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 μA					
	h <sub>FE</sub> group A			-	170	-	
h <sub>FE</sub> group B h <sub>FE</sub> group C DC current gai h <sub>FE</sub> group A h <sub>FE</sub> group B	h <sub>FE</sub> group B			-	280	-	
	h <sub>FE</sub> group C			-	420	-	
	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA		110	-	800	
	h <sub>FE</sub> group A			110	180	220	
	h <sub>FE</sub> group B			200	290	450	
	h <sub>FE</sub> group C			420	520	800	
V <sub>CEsat</sub>	collector-emitter	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA		-	90	200	mV
	saturation voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA	[1]	-	200	400	mV
$V_{BEsat}$	base-emitter saturation	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}$	[2]	-	700	-	mV
	voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA	[2]	-	900	-	mV
$V_{BE}$	base-emitter voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA	[2]	580	660	700	mV
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA		-	-	770	mV
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz		100	-	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$		-	-	1.5	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_C = I_c = 0 \text{ A}; f = 1 \text{ MHz}$		-	11	-	pF
NF	noise figure	$I_C$ = 200 μA; $V_{CE}$ = 5 V; $R_S$ = 2 kΩ; $f$ = 1 kHz; $B$ = 200Hz		-	2	10	dB

<sup>[1]</sup> pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 

<sup>[2]</sup> V<sub>BE</sub> decreases by approximately 2 mV/K with increasing temperature

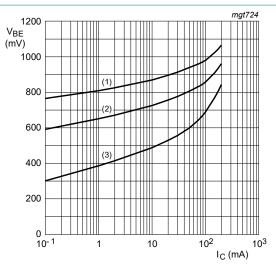


$$V_{CE} = 5 V$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 4. Group A: DC current gain as a function of collector current; typical values



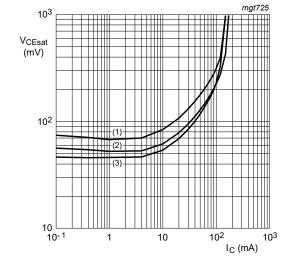
$$V_{CE} = 5 V$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 5. Group A: Base-emitter voltage as a function of collector current; typical values



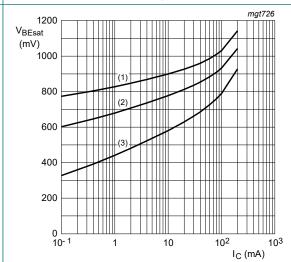
$$I_{\rm C}/I_{\rm B} = 20$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 6. Group A: Collector-emitter saturation voltage as a function of collector current; typical values



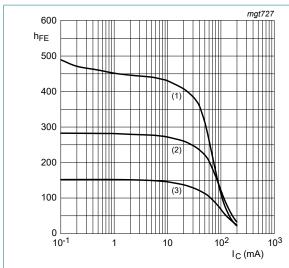
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = -55$$
 °C

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb}$$
 = 150 °C

 Group A: Base-emitter saturation voltage as a function of collector current; typical values

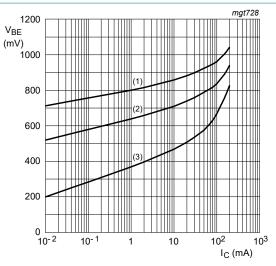


$$V_{CE} = 5 V$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Group B: DC current gain as a function of Fig. 8. collector current; typical values



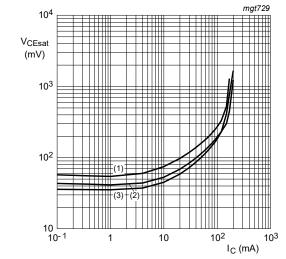
$$V_{CE} = 5 V$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Group B: Base-emitter voltage as a function of Fig. 9. collector current; typical values



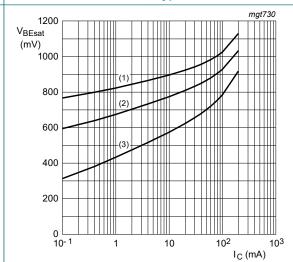
$$I_{\rm C}/I_{\rm B} = 20$$

(1) 
$$T_{amb}$$
 = 150 °C

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 10. Group B: Collector-emitter saturation voltage as Fig. 11. Group B: Base-emitter saturation voltage as a a function of collector current; typical values



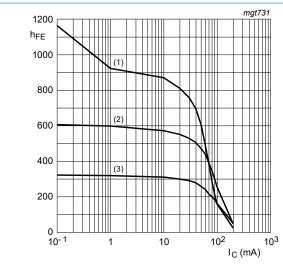
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = -55$$
 °C

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

function of collector current; typical values



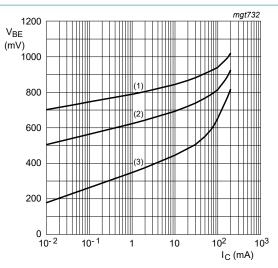
$$V_{CE} = 5 V$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 12. Group C: DC current gain as a function of collector current; typical values



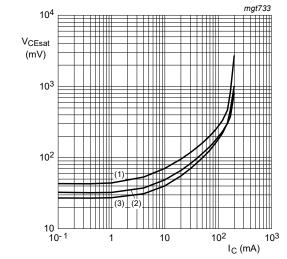
$$V_{CE} = 5 V$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb}$$
 = 150 °C

Fig. 13. Group C: Base-emitter voltage as a function of collector current; typical values



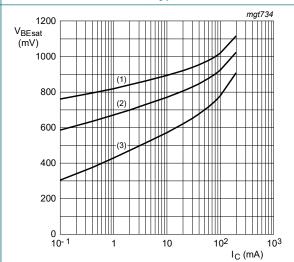
$$I_{\rm C}/I_{\rm B} = 20$$

(1) 
$$T_{amb}$$
 = 150 °C

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 14. Group C: Collector-emitter saturation voltage as Fig. 15. Group C: Base-emitter saturation voltage as a a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = -55$$
 °C

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

function of collector current; typical values

45 V, 100 mA NPN general-purpose transistors

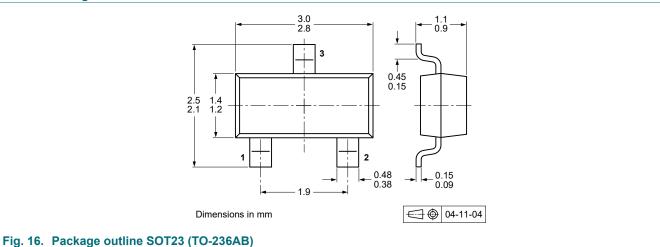
### 8. Test information

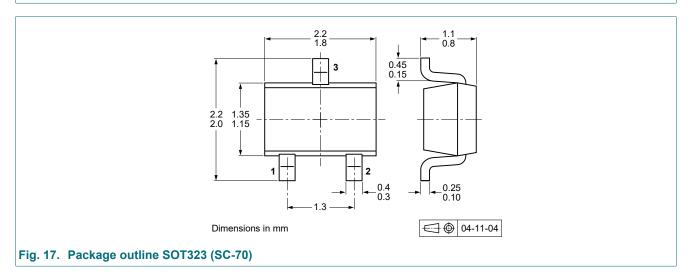
### 8.1. Quality information

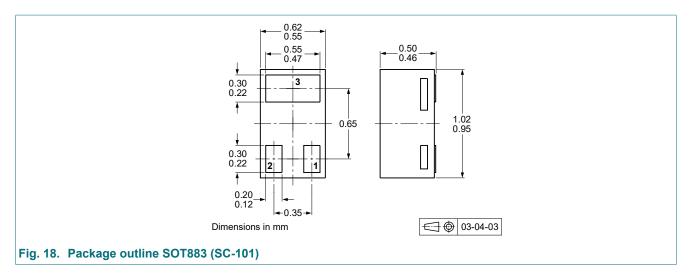
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

### 9. Package outline

Table 9. Package outline



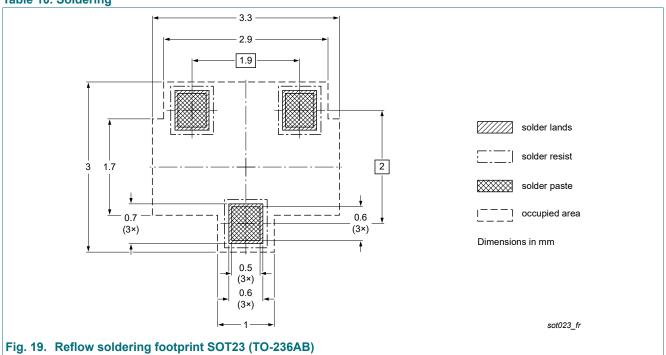


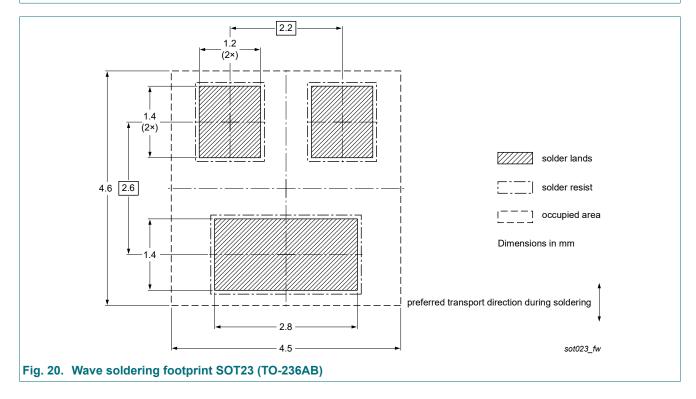


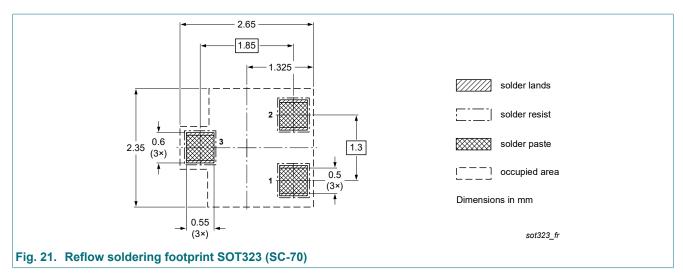
### 45 V, 100 mA NPN general-purpose transistors

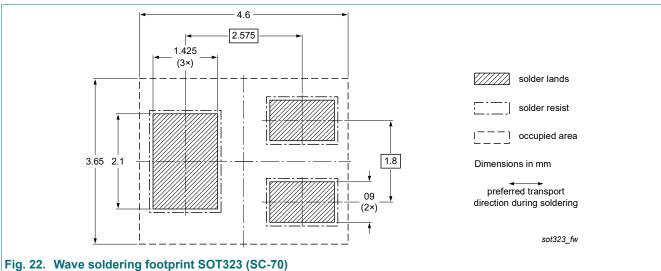
## 10. Soldering

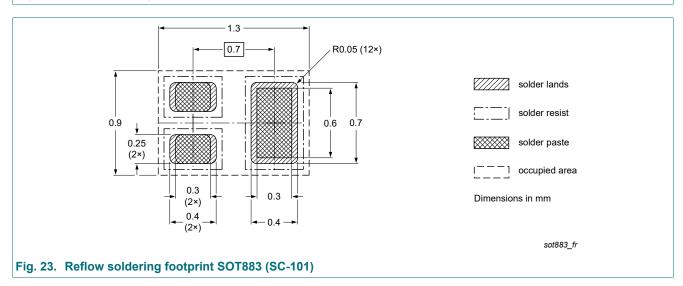












### 45 V, 100 mA NPN general-purpose transistors

## 11. Revision history

### **Table 11. Revision history**

Table 11. Revision mistory				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BC847_SER v.12	20191024	Product data sheet	-	BC847_SER v.11
Modifications:	Table 1: He	ader NPN complement correcte	d to PNP con	nplement
BC847_SER v.11	20181205	Product data sheet	-	BC847_SER v.10
BC847_SER v.10	20180302	Product data sheet	-	BC847_SER v.9
BC847_SER v.9	20140923	Product data sheet	-	BC847_SER v.8
BC847_SER v.8	20120820	Product data sheet	-	BC847_BC547_SER v.7
BC847_BC547_SER v.7	20081210	Product data sheet	-	BC847_BC547_SER v.6
BC847_BC547_SER v.6	20050519	Product data sheet	-	-

### 12. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <a href="https://www.nexperia.com">https://www.nexperia.com</a>.

#### **Definitions**

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

#### **Disclaimers**

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use in automotive applications** — This Nexperia product has been qualified for use in automotive applications. Unless otherwise agreed in writing, the product is not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or

#### 45 V, 100 mA NPN general-purpose transistors

equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <a href="http://www.nexperia.com/profile/terms">http://www.nexperia.com/profile/terms</a>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

#### **Trademarks**

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

BC847\_SER

### 45 V, 100 mA NPN general-purpose transistors

### **Contents**

1. Product profile	1
1.1. General description	1
1.2. Features and benefits	1
1.3. Applications	1
1.4. Quick reference data	2
2. Pinning information	2
3. Ordering information	3
4. Marking	3
5. Limiting values	4
6. Thermal characteristics	4
7. Characteristics	
8. Test information	11
8.1. Quality information	11
9. Package outline	
10. Soldering	
11. Revision history	
12. Legal information	16

For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 24 October 2019

<sup>©</sup> Nexperia B.V. 2019. All rights reserved

## **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

### Nexperia:

BC847,235 BC847A,235 BC847AM,315 BC847A,215 BC847AT,115 BC847AW,135 BC847AW,115 BC847B,235 BC847BM,315 BC847BT,115 BC847BW,135 BC847BW,115 BC847C,235 BC847CM,315 BC847C,215 BC847CT,115 BC847CW,135 BC847CW,115 BC847C/SNVL BC847B/SNVL BC847A/SNVL B