



Sheet: Top layer  
File: wirebug.kicad\_pcb

**Title:**

Size: A4	Date: 2023-12-02_22-55-25
KiCad E.D.A. 7.0.9-7.0.9-ubuntu23.04.1 + KiBot v1.6.3	

Date: 2023-12-02\_22-55-25

Rev:  
Id: 2/3