CprE 381, Computer Organization and Assembly Level Programming

Team Contract – Lab #1

Lab Partners Group #: 00

Team Members: Bitter Thyme

Savory Radish

Course Goals:
Bitter Thyme wants to Pass the course high efficiency to spend time on their part-time internship with John Deere's new herb equipment division.
Savory Radish wants to learn how to design processors like those used in phone processors. Savory wants to build a complete processor that can run compiled C++ code.

Team Expectations:

- **Conduct:** Fundamentally, we expect each other to respect our goals, perspectives, and time. We expect to both have a say in design decisions.
- **Communication:** We both prefer texts in order to arrange meeting times and we've exchanged our phone numbers. Any technical communication will be via our team's slack channel we have created. We expect a response (at least an acknowledgement) within 24 hours.
- **Meetings:** We will work together in each lab as a structured group (i.e., one drive and one navigator identified at all times). Outside of the lab time we will work together in the evenings by holding Google Hangouts while we work on our own responsibilities via VDI. Sunday evening's (back-up is Monday evenings) we will meet in-person in the lab to perform any integration tasks prior to our submissions.

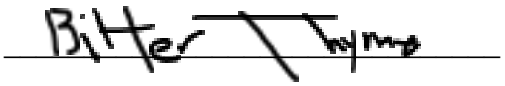
- **Peer Evaluation Criteria:** N/A

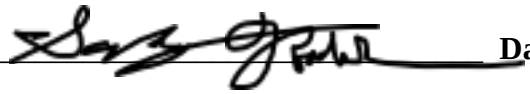
Role Responsibilities for Lab 1:

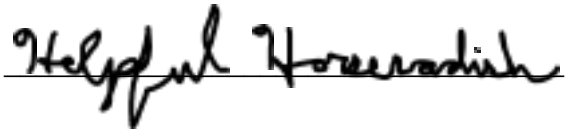
Lab Part	Estimated Time	Design		Test	
		Lead	Deadline	Lead	Deadline
Example Design Cycle	1.5 hr	BT	01/16		
A First Design	1.5 hr	SR	01/17	BT	01/17
Generics	0.5 hr			BT	01/19
One's	0.5 hr	BT	01/19	SR	01/19

Complementor					
Full Adder	2 hr	SR	01/24	BT	01/24
Adder-Subtractor	2 hr	BT	01/26	SR	01/26

Integrity of Work: *Do not delete the following.* We agree that the work we provide to other team members and ultimately submit for a grade is a direct result of our own work as described in the course syllabus. Specifically, we will generate all VHDL code ourselves and not copy VHDL code from online sources, other groups, book companion material, or past student projects to which anyone outside of my team has contributed.

Student Signature  **Date** 01/16/20

Student Signature  **Date** 01/16/20

TA Signature  **Date** 01/16/20