

LM78XX Series Voltage Regulators

General Description

The LM78XX series of three terminal regulators is available with several fixed output voltages making them useful in a wide range of applications. One of these is local on card regulation, eliminating the distribution problems associated with single point regulation. The voltages available allow these regulators to be used in logic systems, instrumentation, HiFi, and other solid state electronic equipment. Although designed primarily as fixed voltage regulators these devices can be used with external components to obtain adjustable voltages and currents.

The LM78XX series is available in an aluminum TO-3 package which will allow over 1.0A load current if adequate heat sinking is provided. Current limiting is included to limit the peak output current to a safe value. Safe area protection for the output transistor is provided to limit internal power dissipation. If internal power dissipation becomes too high for the heat sinking provided, the thermal shutdown circuit takes over preventing the IC from overheating.

Considerable effort was expanded to make the LM78XX series of regulators easy to use and mininize the number

of external components. It is not necessary to bypass the output, although this does improve transient response. Input bypassing is needed only if the regulator is located far from the filter capacitor of the power supply.

For output voltage other than 5V, 12V and 15V the LM117 series provides an output voltage range from 1.2V to 57V.

Features

- Output current in excess of 1A
- Internal thermal overload protection
- No external components required
- Output transistor safe area protection
- Internal short circuit current limit
- Available in the aluminum TO-3 package

Voltage Range

LM7805C 5V LM7812C 12V LM7815C 15V

Schematic and Connection Diagrams Metal Can Package TO-3 (K) 015 **Aluminum** OUTPUT TL/H/7746-2 **Bottom View** Order Number LM7805CK, LM7812CK or LM7815CK See NS Package Number KC02A Plastic Package TO-220 (T) TL/H/7746-3 **Top View** Order Number LM7805CT, LM7812CT or LM7815CT See NS Package Number T03B TL/H/7746-1

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Input Voltage ($V_0 = 5V$, 12V and 15V) Internal Power Dissipation (Note 1) Internally Limited

Operating Temperature Range (T_A) 0°C to +70°C Maximum Junction Temperature (K Package) 150°C (T Package) 150°C Storage Temperature Range -65°C to +150°C

Lead Temperature (Soldering, 10 sec.)

TO-3 Package K 300°C TO-220 Package T 230°C

$\textbf{Electrical Characteristics LM78XXC} \ \ (\text{Note 2}) \ 0^{\circ}\text{C} \leq \text{T} \\ j \leq 125^{\circ}\text{C unless otherwise noted}.$

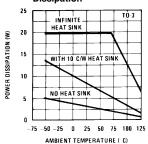
Output Voltage					5V			12V			15V		
Input Voltage (unless otherwise noted)					10V			19V			23V		
Symbol	Parameter		Conditions	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
Vo	Output Voltage	Tj = 25°C, 5	$mA \le I_O \le 1A$	4.8	5	5.2	11.5	12	12.5	14.4	15	15.6	V
		$\begin{aligned} P_D &\leq 15W, 5 \text{ mA} \leq I_O \leq 1A \\ V_{MIN} &\leq V_{IN} \leq V_{MAX} \end{aligned}$		4.75 (7.5 :	≤ V _{IN}	5.25 ≤ 20)	11.4 (14.5	$\leq V_{IN}$	12.6 ≤ 27)	14.25 (17.5	$i \leq V_{IN}$	15.75 ≤ 30)	V
ΔV_{O}	Line Regulation	I _O = 500 mA	$Tj = 25^{\circ}C$ ΔV_{IN}	(7 ≤	3 V _{IN} ≤	50 ≤ 25)	14.5	4 ≤ V _{IN}	120 ≤ 30)	(17.5	$4 \le V_{IN}$	150 ≤ 30)	mV V
			$0^{\circ}C \le Tj \le +125^{\circ}C$ ΔV_{IN}	(8 ≤	V _{IN} ≤	50 ≤ 20)	(15 :	≤ V _{IN} :	120 ≤ 27)	(18.5	$i \leq V_{IN}$	150 ≤ 30)	mV V
		l _O ≤ 1A	$Tj = 25^{\circ}C$ ΔV_{IN}	(7.5 :	≤ V _{IN}	50 ≤ 20)	(14.6	≤ V _{IN}	120 ≤ 27)	(17.7	' ≤ V _{IN}	150 ≤ 30)	mV V
			$\begin{array}{l} 0^{\circ}C \leq Tj \leq + 125^{\circ}C \\ \Delta V_{IN} \end{array}$	(8 ≤	V _{IN} ≤	25 ≤ 12)	(16 :	≤ V _{IN} :	60 ≤ 22)	(20	≤ V _{IN} :	75 ≤ 26)	mV V
ΔV_{O}	Load Regulation	Tj = 25°C	$\begin{array}{l} \text{5 mA} \leq \text{I}_{\text{O}} \leq \text{1.5A} \\ \text{250 mA} \leq \text{I}_{\text{O}} \leq \text{750 mA} \end{array}$		10	50 25		12	120 60		12	150 75	mV mV
		$5 \text{ mA} \le I_{O} \le 1 \text{A}, 0^{\circ}\text{C} \le Tj \le +125^{\circ}\text{C}$				50			120			150	mV
I _Q	Quiescent Current	$I_0 \le 1A$	Tj = 25°C 0 °C $\leq Tj \leq +125$ °C			8 8.5			8 8.5			8 8.5	mA mA
ΔI_Q	Quiescent Current	$5 \text{ mA} \leq I_{O} \leq 1 \text{A}$				0.5			0.5			0.5	mA
	Change	$Tj = 25^{\circ}C, I_{O} \leq 1A$ $V_{MIN} \leq V_{IN} \leq V_{MAX}$		$(7.5 \le V_{IN} \le 20)$			1.0 (14.8 ≤ V _{IN} ≤ 27)			1.0 $(17.9 \le V_{IN} \le 30)$			mA V
		$\begin{split} I_O &\leq 500 \text{ mA, } 0^{\circ}\text{C} \leq \text{Tj} \leq +125^{\circ}\text{C} \\ V_{MIN} &\leq V_{IN} \leq V_{MAX} \end{split}$		1.0 (7 ≤ V _{IN} ≤ 25)			1.0 (14.5 ≤ V _{IN} ≤ 30)			1.0 $(17.5 \le V_{IN} \le 30)$			mA V
V_N	Output Noise Voltage	$T_A = 25^{\circ}C$, 10 Hz $\leq f \leq 100 \text{ kHz}$			40			75			90		μV
$\frac{\Delta V_{IN}}{\Delta V_{OUT}}$	Ripple Rejection	f = 120 Hz	$I_{O} \le$ 1A, Tj = 25°C or $I_{O} \le$ 500 mA 0 °C \le Tj \le +125°C	62 62	80		55 55	72		54 54	70		dB dB
		$V_{MIN} \le V_{IN} \le V_{MAX}$		(8 ≤ V _{IN} ≤ 18)			$(15 \le V_{IN} \le 25)$			$(18.5 \le V_{IN} \le 28.5)$		V	
R _O	Dropout Voltage Output Resistance Short-Circuit Current Peak Output Current Average TC of V _{OUT}	'		2.0 8 2.1 2.4 0.6			2.0 18 1.5 2.4 1.5			2.0 19 1.2 2.4 1.8			V mΩ A A mV/°C
V _{IN}	Input Voltage Required to Maintain Line Regulation	Tj = 25°C, I _O	≤ 1A		7.5		14.6			17.7			V

Note 1: Thermal resistance of the TO-3 package (K, KC) is typically 4°C/W junction to case and 35°C/W case to ambient. Thermal resistance of the TO-220 package (T) is typically 4°C/W junction to case and 50°C/W case to ambient.

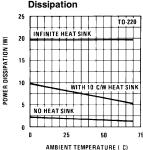
Note 2: All characteristics are measured with capacitor across the input of 0.22 μ F, and a capacitor across the output of 0.1 μ F. All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ($t_w \le 10$ ms, duty cycle $\le 5\%$). Output voltage changes due to changes in internal temperature must be taken into account separately.

Typical Performance Characteristics

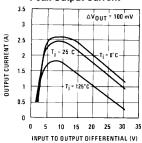




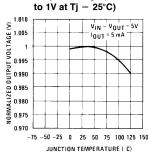
Maximum Average Power Dissipation



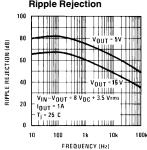
Peak Output Current



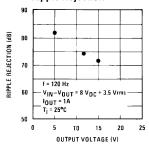
Output Voltage (Normalized



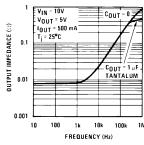
Ripple Rejection



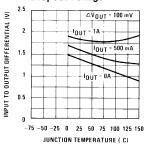
Ripple Rejection



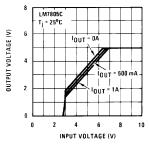
Output Impedance



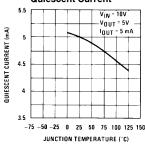
Dropout Voltage



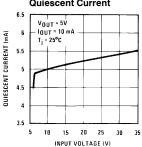
Dropout Characteristics



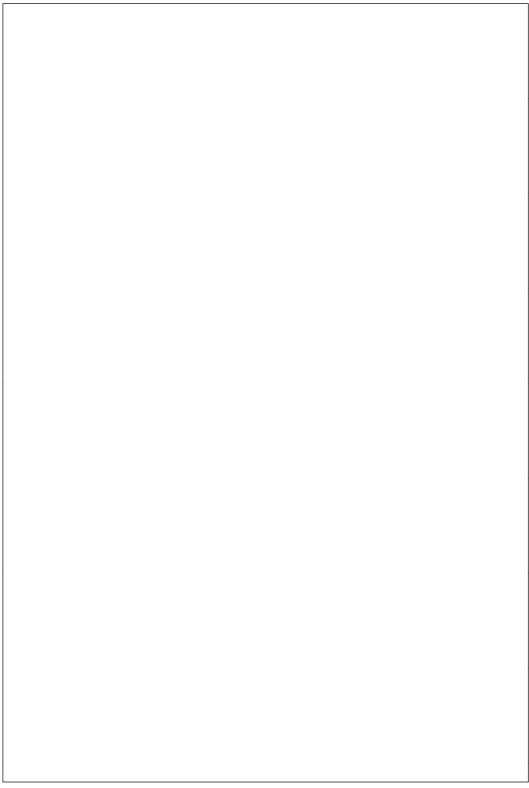
Quiescent Current



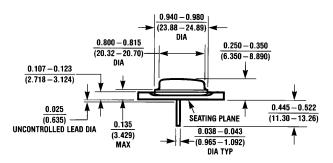
Quiescent Current

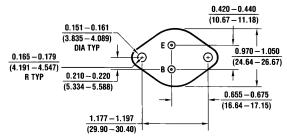


TL/H/7746-4



Physical Dimensions inches (millimeters)

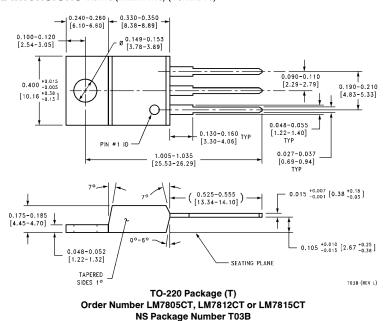




KC02A (REV C)

Aluminum Metal Can Package (KC) Order Number LM7805CK, LM7812CK or LM7815CK NS Package Number KC02A

Physical Dimensions inches (millimeters) (Continued)



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Ocean Centre, 5 Canton I Tsimshatsui, Kowloon Hong Kong Tel: (852) 2737-1600 Fax: (852) 2736-9960 National Semiconductor Japan Ltd. Tel: 81-043-299-2309 Fax: 81-043-299-2408