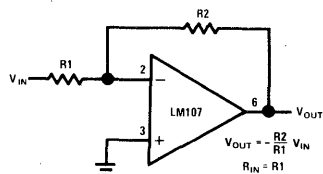




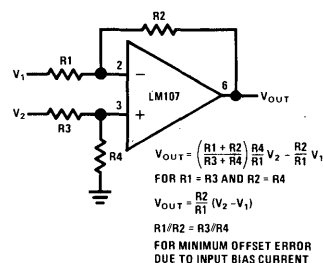
February 1970

op amp circuit collection

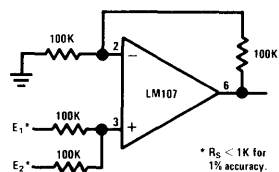
section 1 — basic circuits



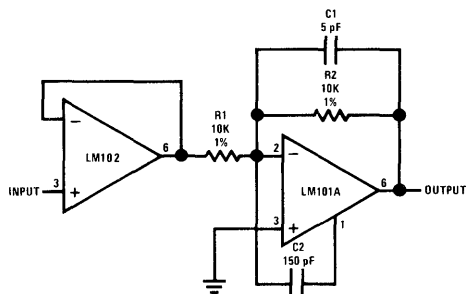
Inverting Amplifier



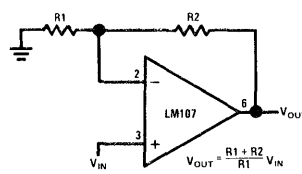
Difference Amplifier



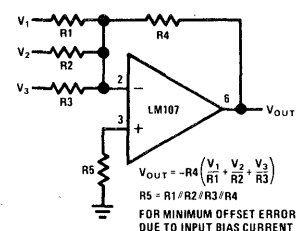
Non-Inverting Summing Amplifier



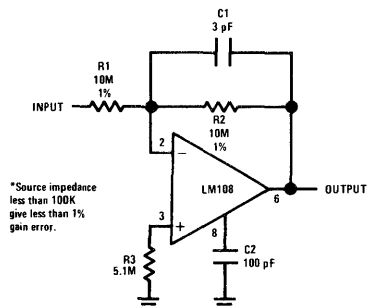
Fast Inverting Amplifier With High Input Impedance



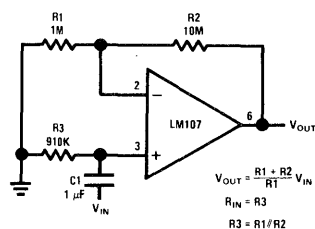
Non-Inverting Amplifier



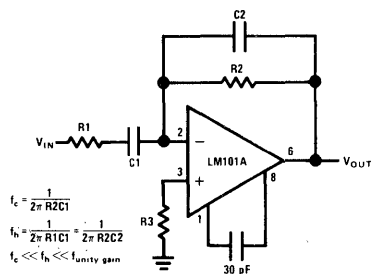
Inverting Summing Amplifier



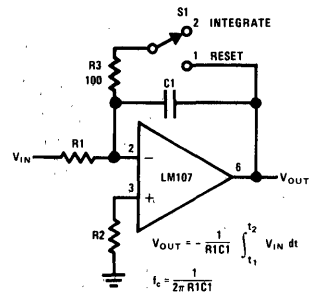
Inverting Amplifier with High
 Input Impedance



Non-Inverting AC Amplifier

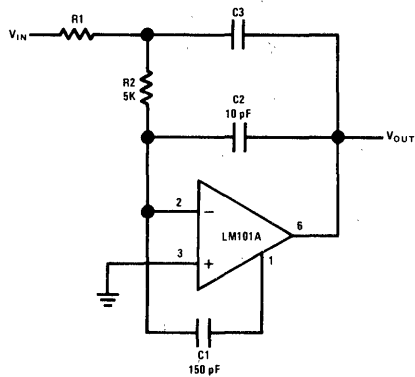


Practical Differentiator

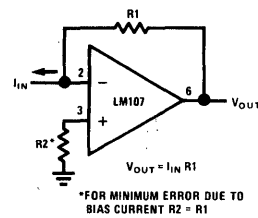


FOR MINIMUM OFFSET ERROR
DUE TO INPUT BIAS CURRENT

Integrator

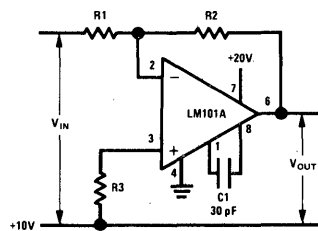


Fast Integrator

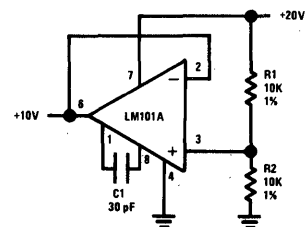


*FOR MINIMUM ERROR DUE TO
BIAS CURRENT $R2 = R1$

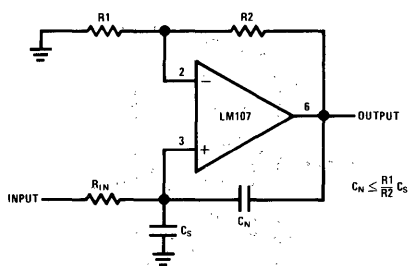
Current to Voltage Converter



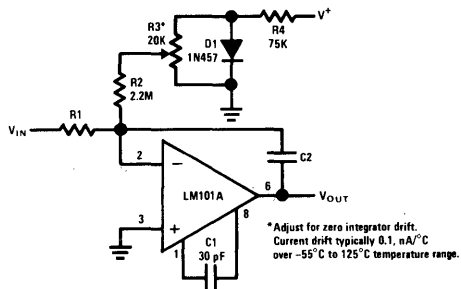
Circuit for Operating the LM101
without a Negative Supply



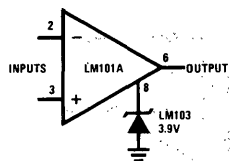
Circuit for Generating the
Second Positive Voltage



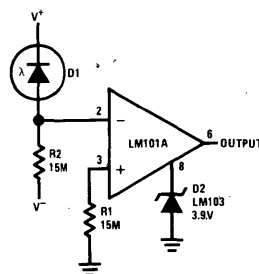
Neutralizing Input Capacitance to Optimize Response Time



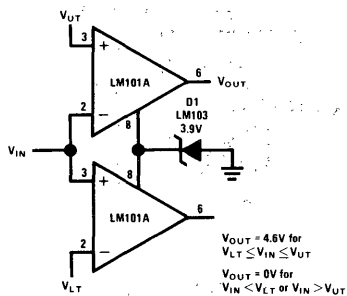
Integrator with Bias Current Compensation



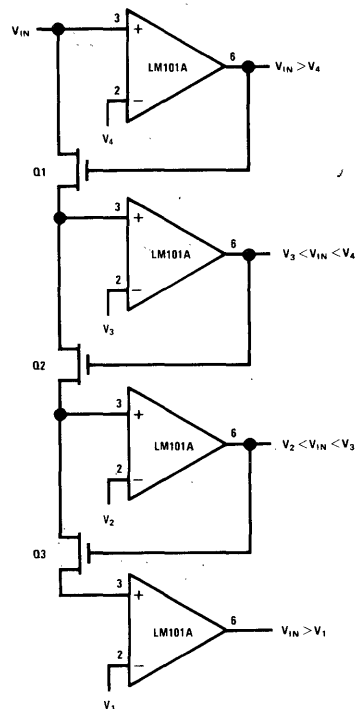
Voltage Comparator for Driving DTL or TTL Integrated Circuits



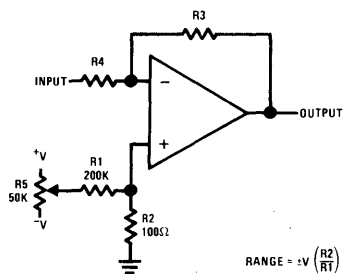
Threshold Detector for Photodiodes



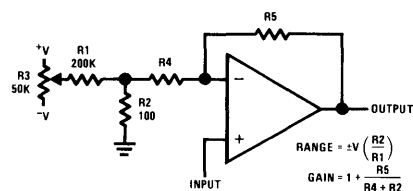
Double-Ended Limit Detector



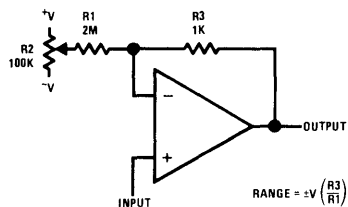
Multiple Aperture Window Discriminator



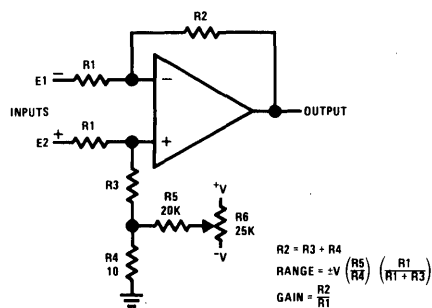
Offset Voltage Adjustment for Inverting Amplifiers
Using Any Type of Feedback Element



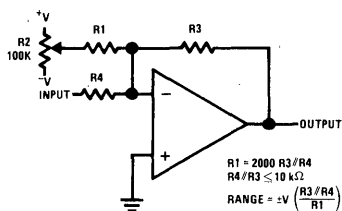
Offset Voltage Adjustment for Non-Inverting
Amplifiers



Offset Voltage Adjustment for Voltage Followers

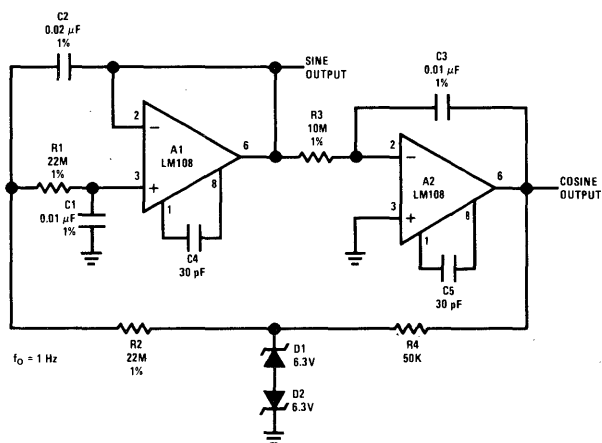


Offset Voltage Adjustment for Differential Amplifiers

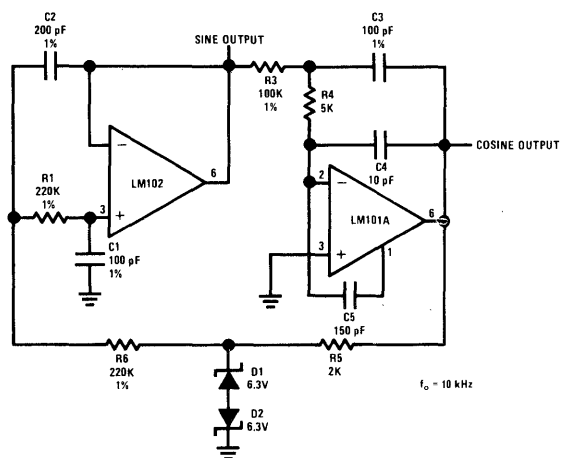


Offset Voltage Adjustment for Inverting
Amplifiers Using 10 kΩ Source Resistance
or Less

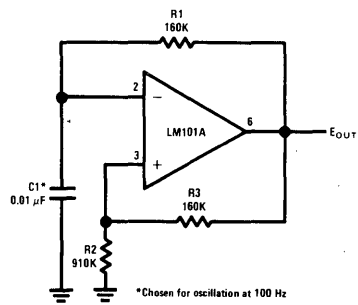
section 2 — signal generation



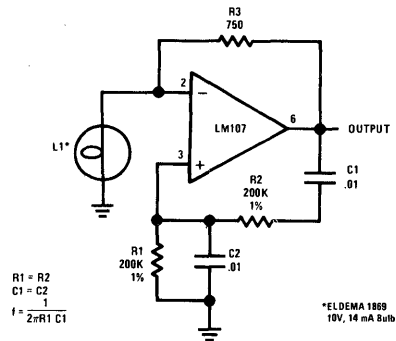
Low Frequency Sine Wave Generator with Quadrature Output



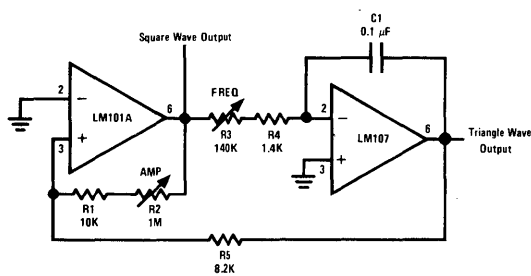
High Frequency Sine Wave Generator with Quadrature Output



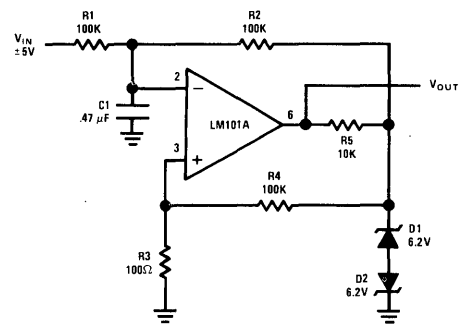
Free-Running Multivibrator



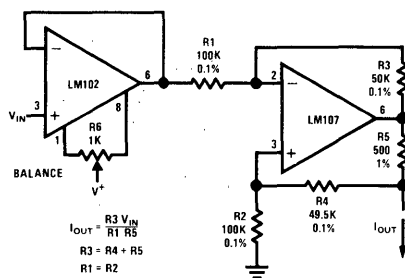
Wein Bridge Sine Wave Oscillator



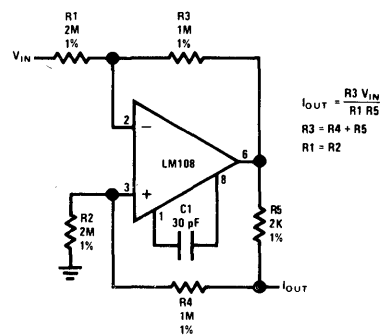
Function Generator



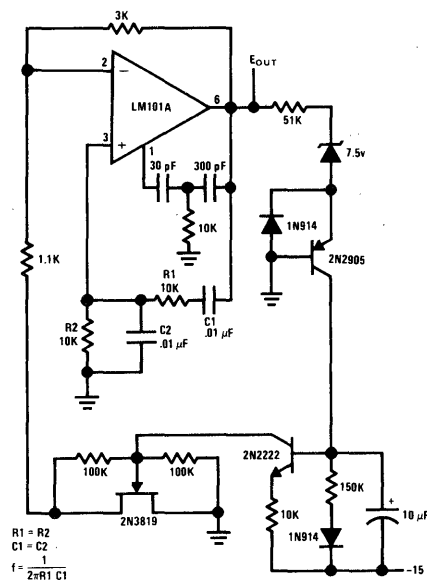
Pulse Width Modulator



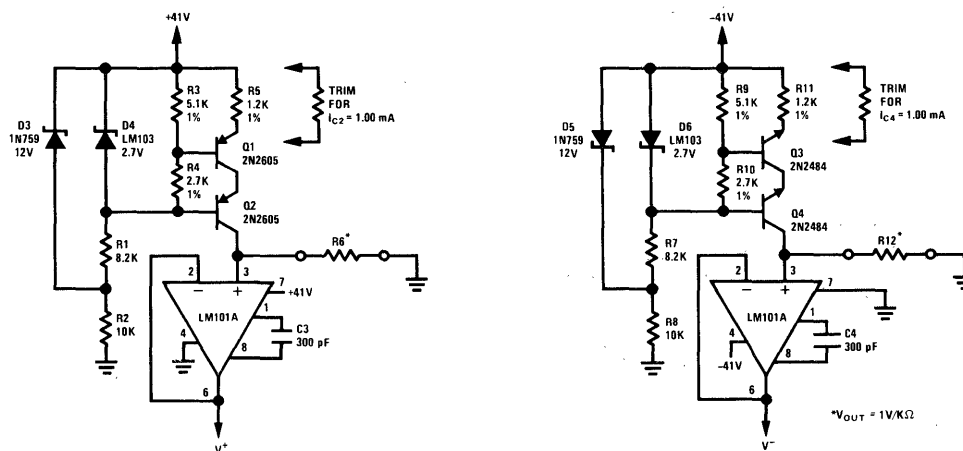
Bilateral Current Source



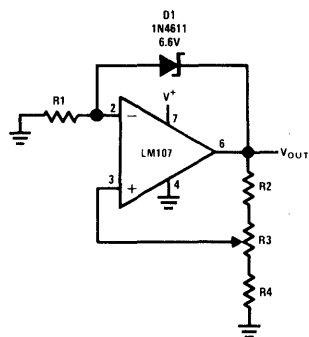
Bilateral Current Source



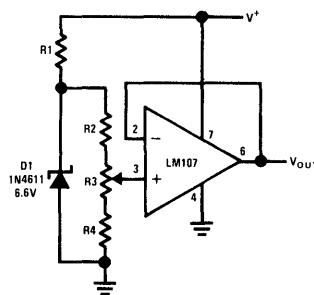
Wein Bridge Oscillator with FET Amplitude Stabilization



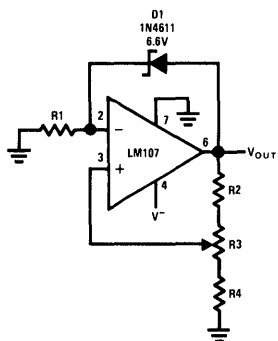
Low Power Supply for Integrated Circuit Testing



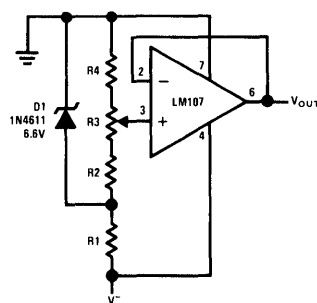
Positive Voltage Reference



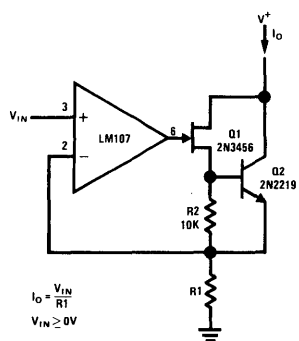
Positive Voltage Reference



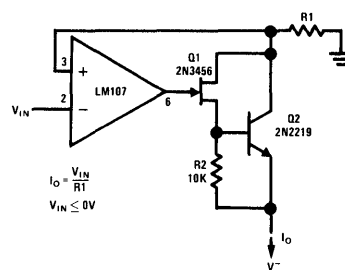
Negative Voltage Reference



Negative Voltage Reference

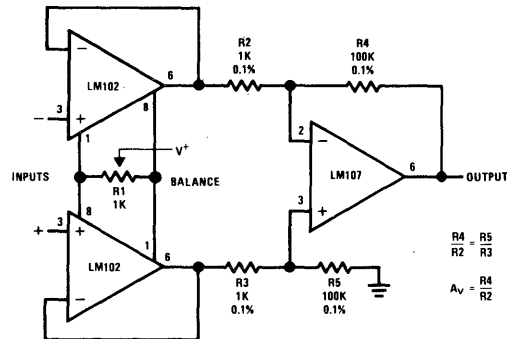


Precision Current Sink

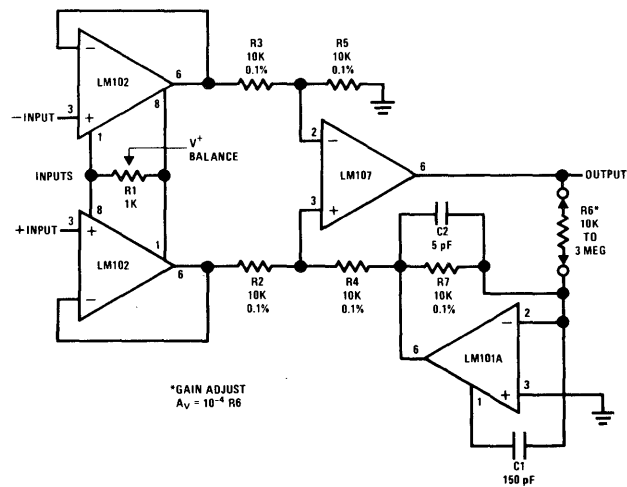


Precision Current Source

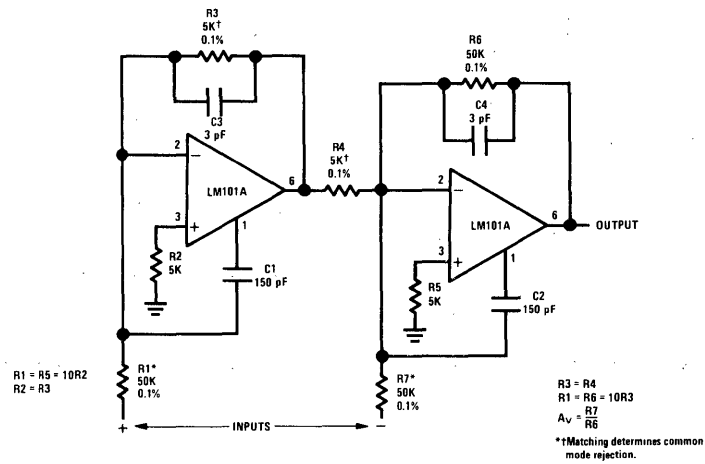
section 3 — signal processing



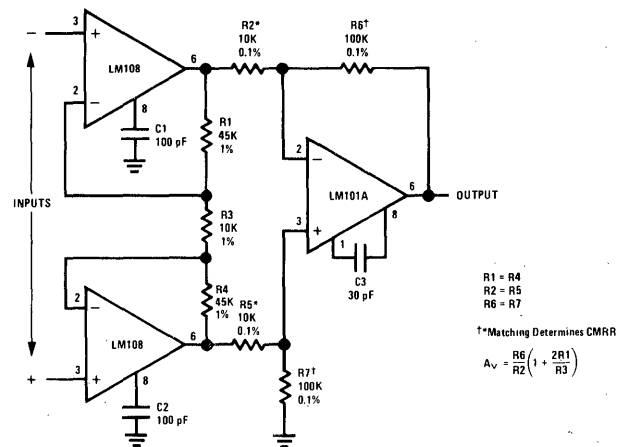
Differential-Input Instrumentation Amplifier



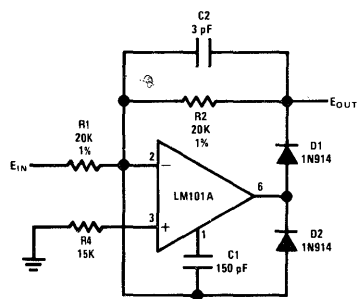
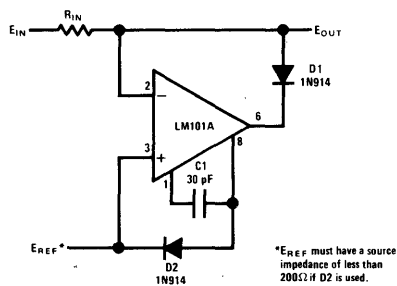
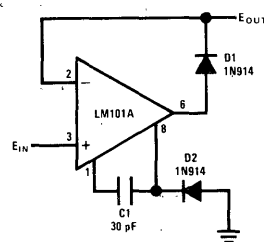
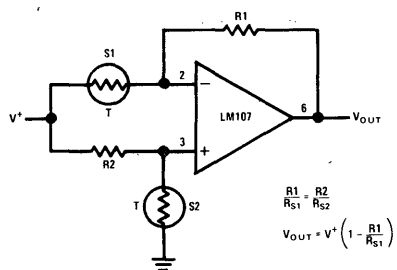
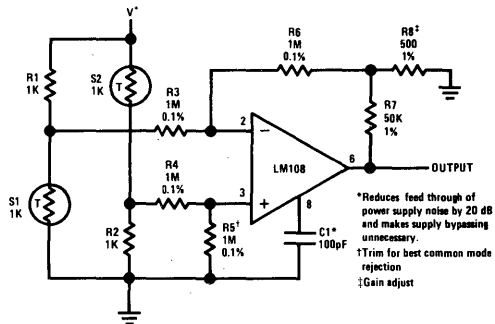
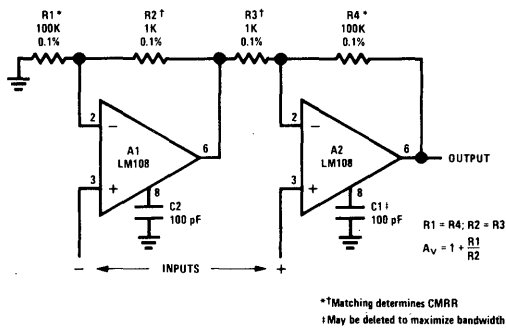
Variable Gain, Differential-Input Instrumentation Amplifier

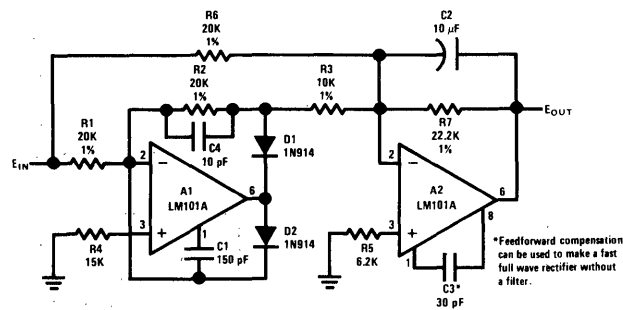


Instrumentation Amplifier with ± 100 Volt Common Mode Range

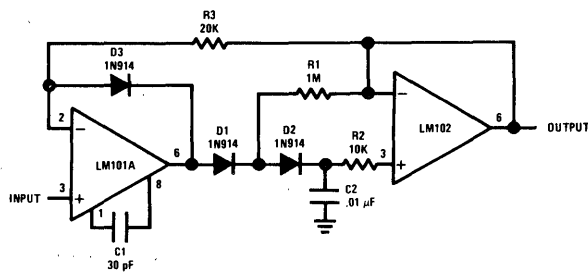


Differential Input Instrumentation Amplifier with High Common Mode Rejection

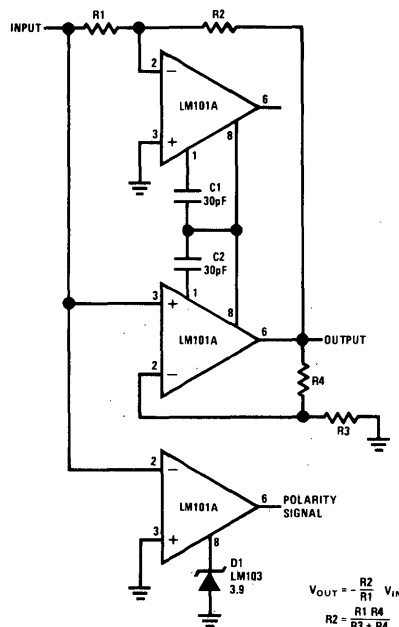




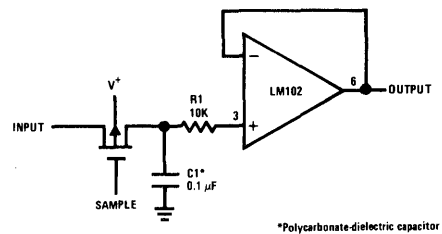
Precision AC to DC Converter



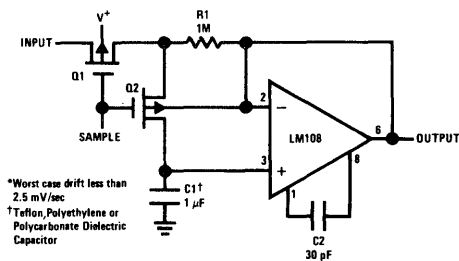
Low Drift Peak Detector



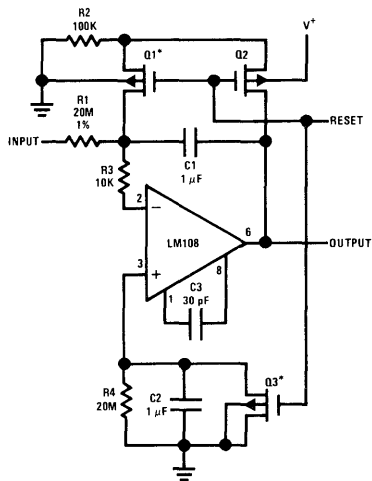
Absolute Value Amplifier with Polarity Detector



Sample and Hold



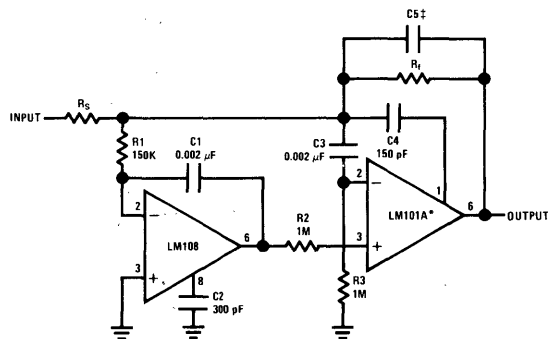
Sample and Hold



*Q1 and Q3 should not have internal gate-protection diodes.

Worst case drift less than 500 $\mu\text{V}/\text{sec}$ over -55°C to $+125^\circ\text{C}$.

Low Drift Integrator

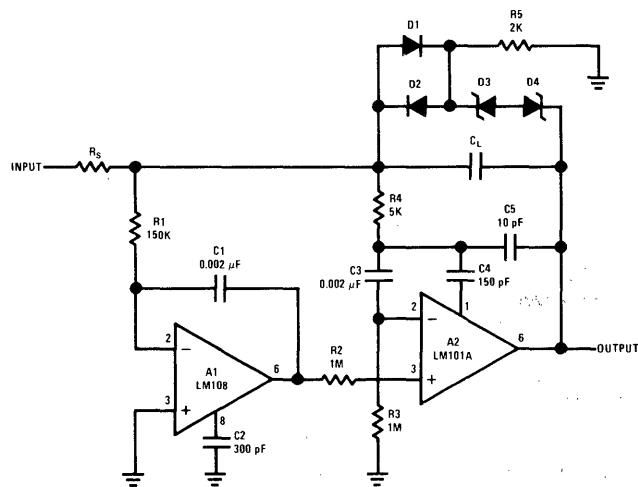


* In addition to increasing speed, the LM101A raises high and low frequency gain, increases output drive capability and eliminates thermal feedback.

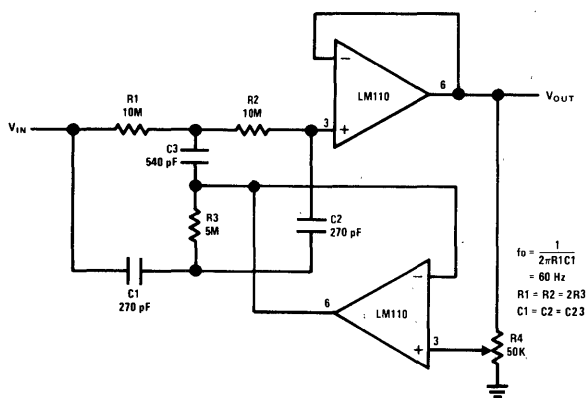
† Power Bandwidth: 250 KHz
Small Signal Bandwidth: 3.5 MHz
Slew Rate: 10V/ μs

‡ $C5 = \frac{6 \times 10^{-8}}{R_1}$

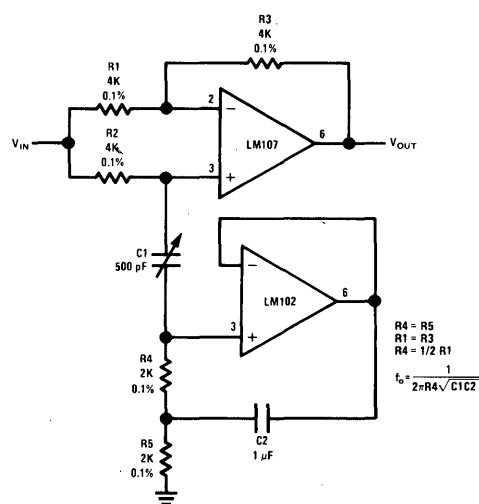
Fast[†] Summing Amplifier with Low Input Current



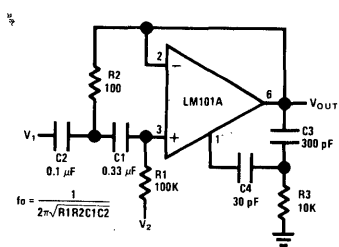
Fast Integrator with Low Input Current



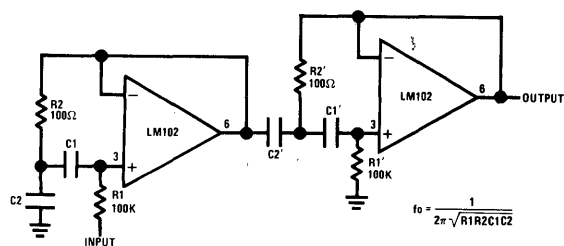
Adjustable Q Notch Filter



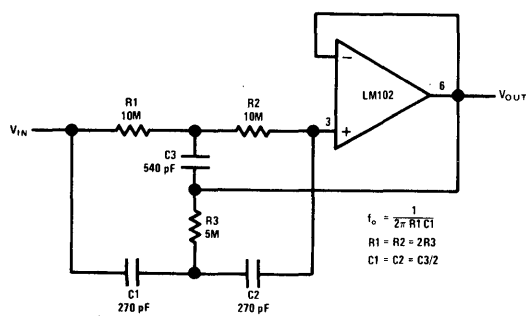
Easily Tuned Notch Filter



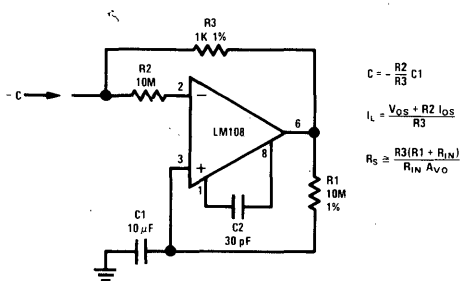
Tuned Circuit



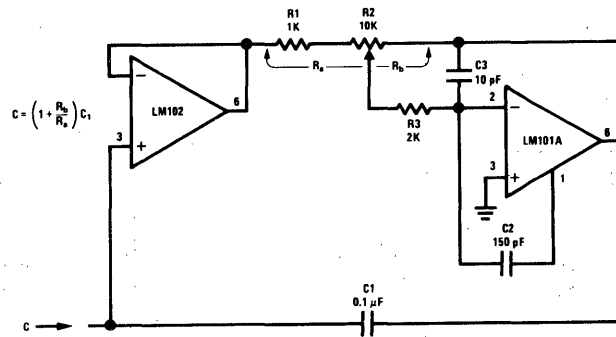
Two-Stage Tuned Circuit



High Q Notch Filter

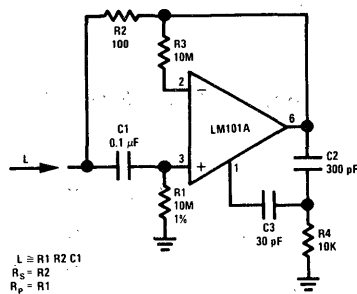


Negative Capacitance Multiplier



$$C = \left(1 + \frac{R_2}{R_3}\right) C_1$$

Variable Capacitance Multiplier

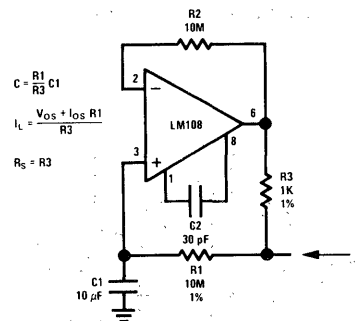


$$L \approx R_1 R_2 C_1$$

$$R_5 = R_2$$

$$R_6 = R_1$$

Simulated Inductor

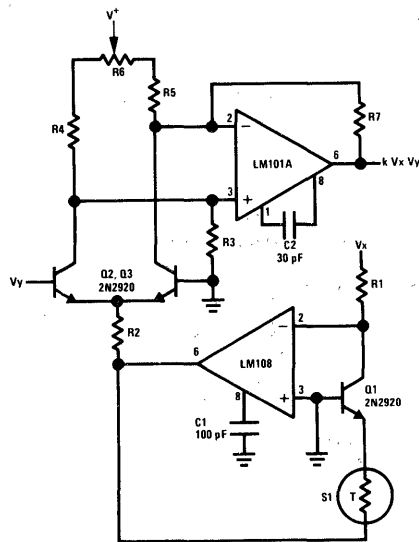


$$C = \frac{R_1 R_2 C_1}{R_3}$$

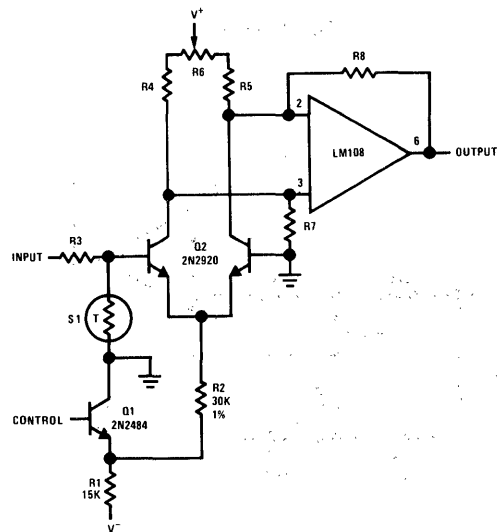
$$I_L = \frac{V_{os} + I_{os} R_1}{R_3}$$

$$R_5 = R_3$$

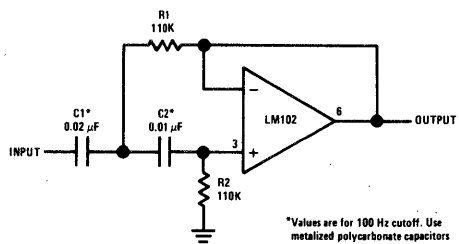
Capacitance Multiplier



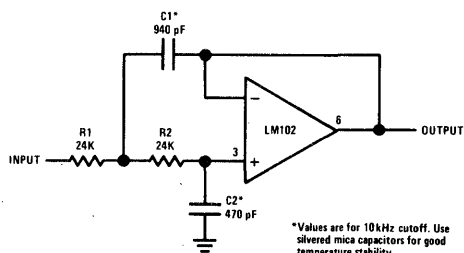
Two Quadrant Multiplier



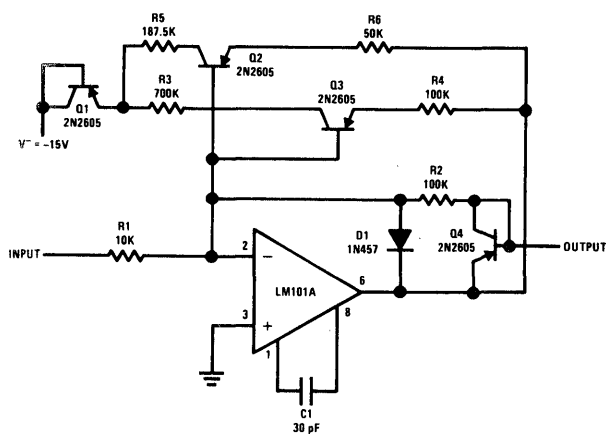
Voltage Controlled Gain Circuit



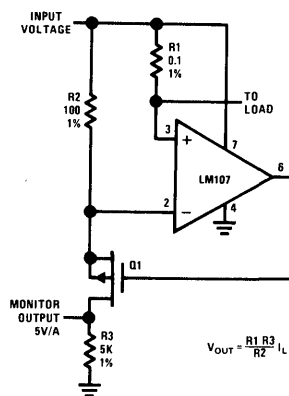
High Pass Active Filter



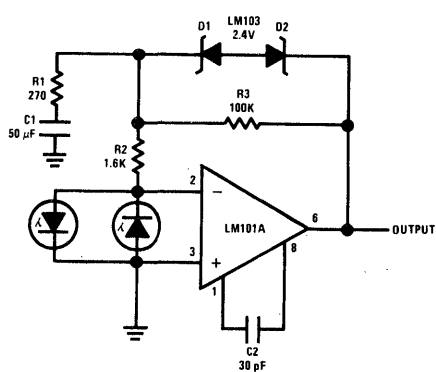
Low Pass Active Filter



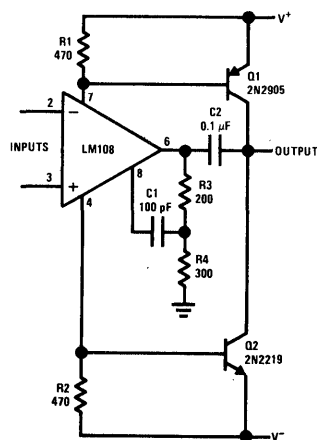
Nonlinear Operational Amplifier with Temperature Compensated Breakpoints



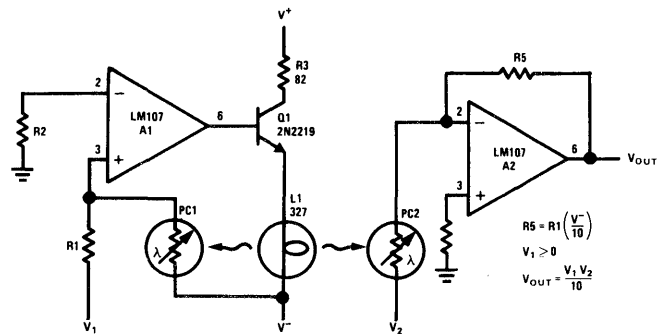
Current Monitor



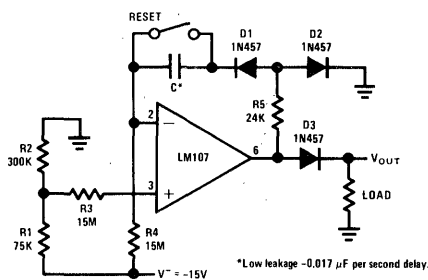
Saturating Servo Preamp with Rate Feedback



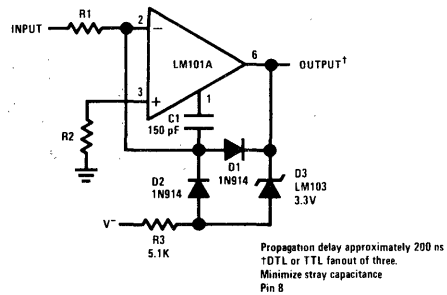
Power Booster



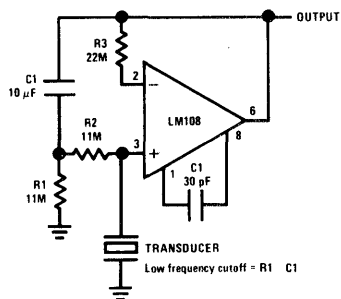
Analog Multiplier



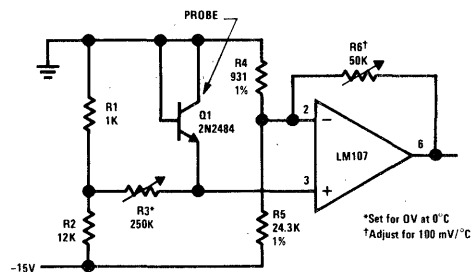
Long Interval Timer



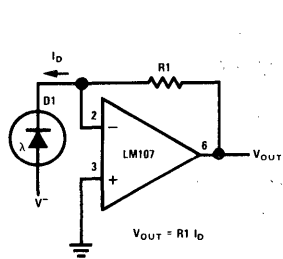
Fast Zero Crossing Detector



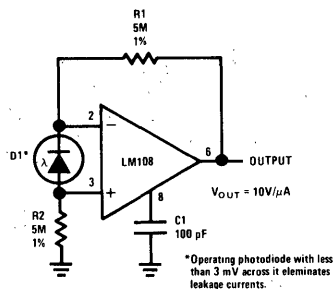
Amplifier for Piezoelectric Transducer



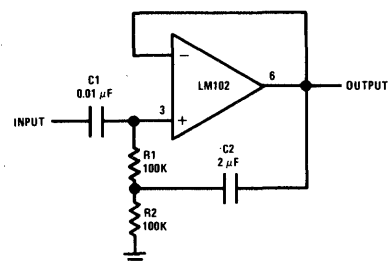
Temperature Probe



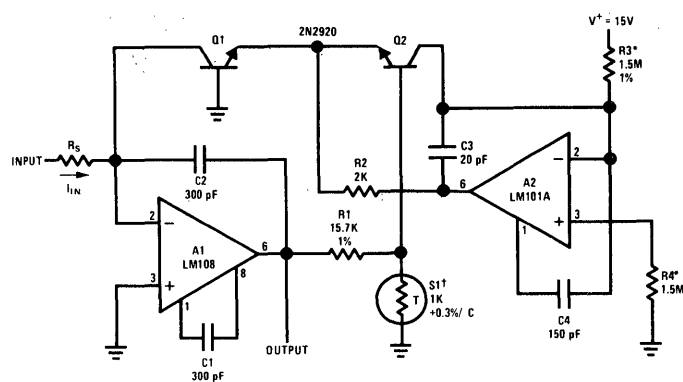
Photodiode Amplifier



Photodiode Amplifier



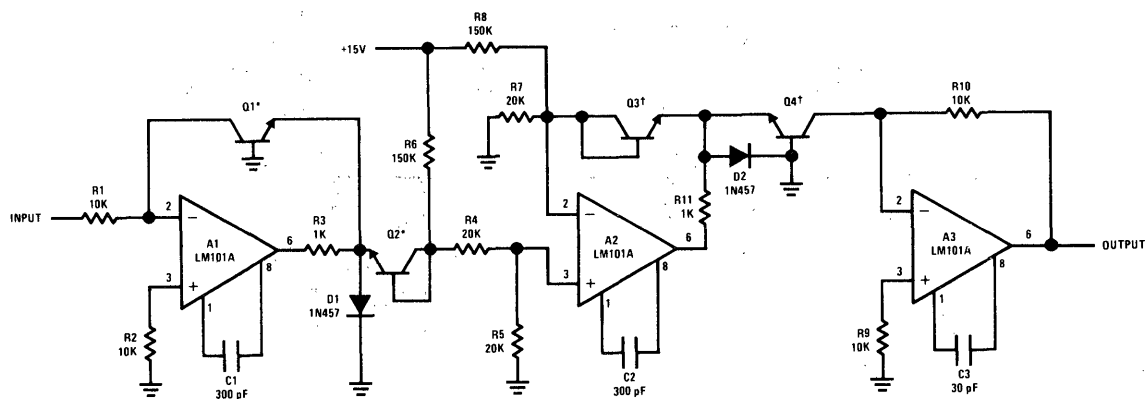
High Input Impedance AC Follower



10 nA < I_{IN} < 1 mA
Sensitivity is 1V per decade.

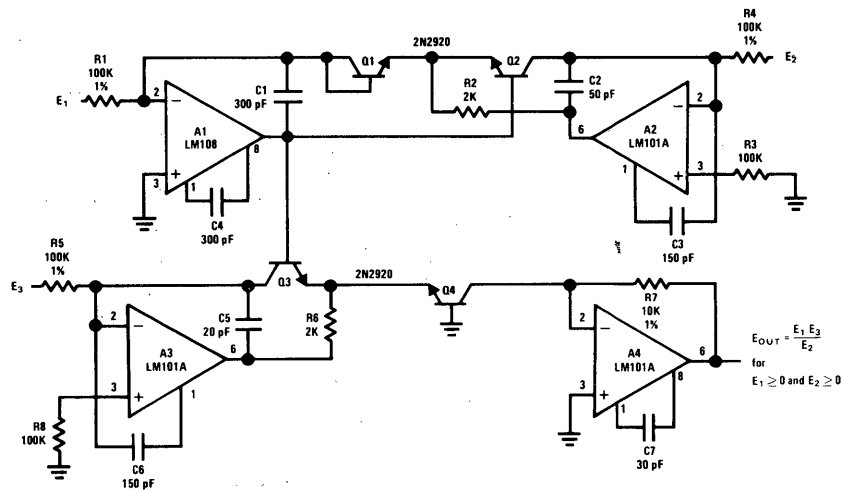
*Available from Tel Labs, Inc.,
Manchester, N.H., Type 081.
*Determines current for zero
crossing on output: 10 μ A
as shown.

Temperature Compensated Logarithmic Converter

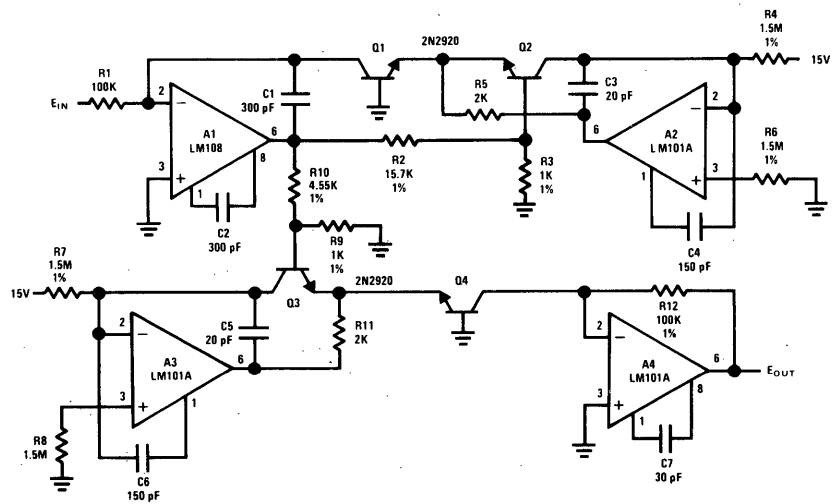


* 12N3728 matched pairs

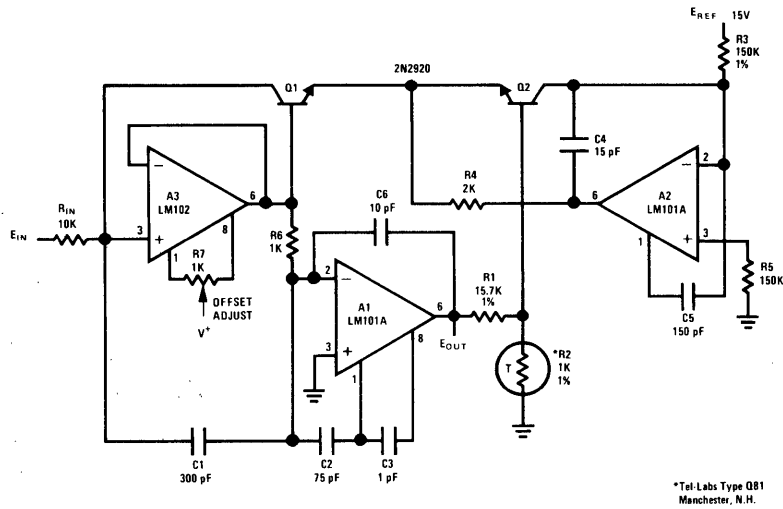
Root Extractor



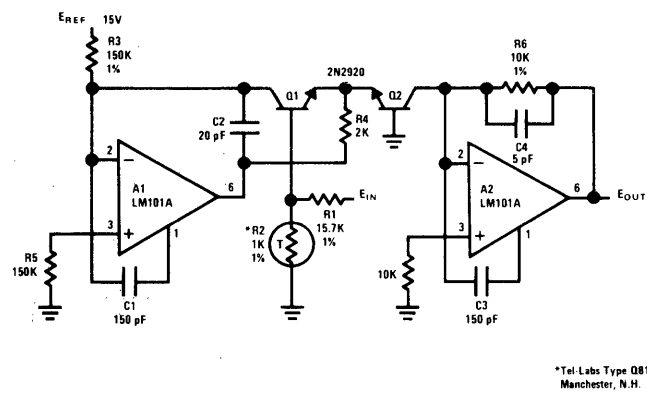
Multiplier/Divider



Cube Generator



Fast Log Generator



Anti-log Generator