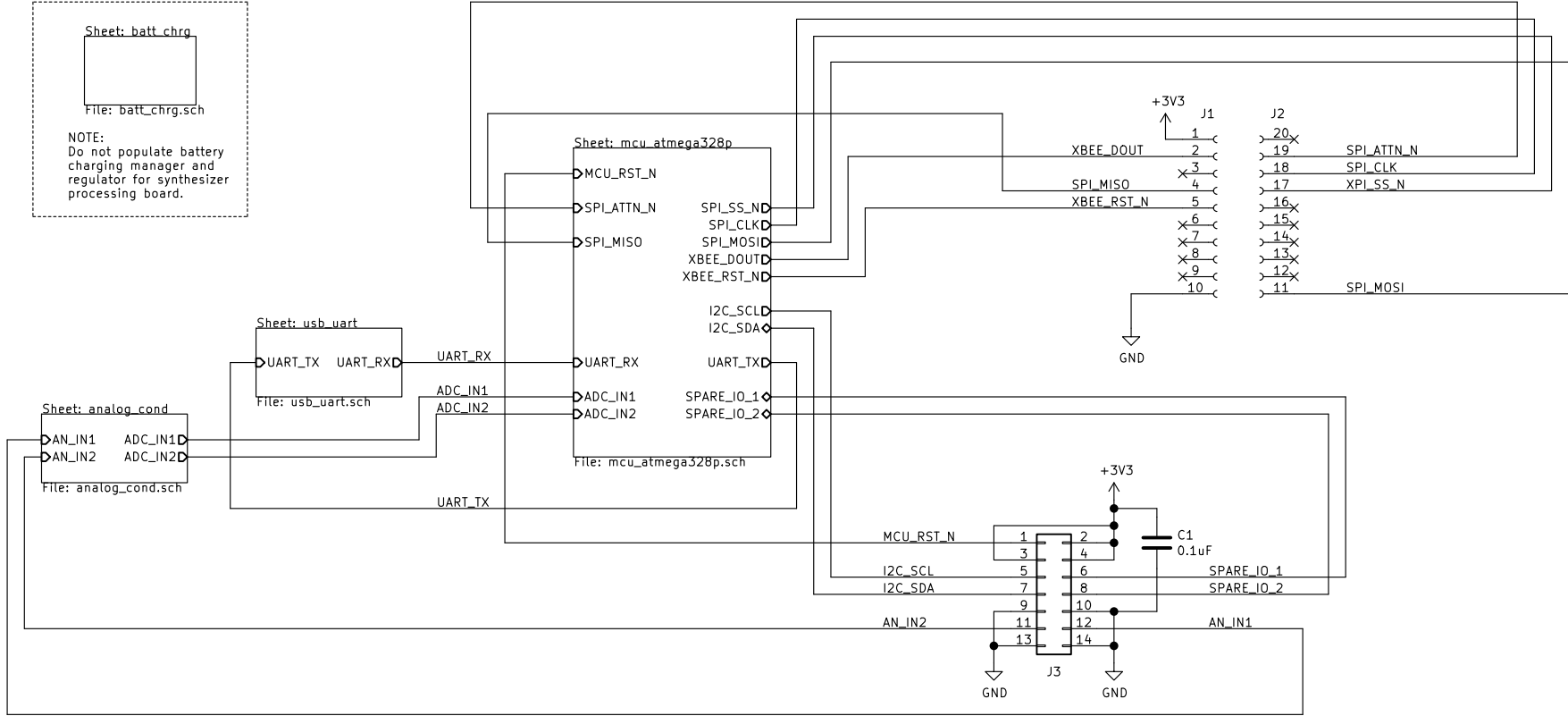
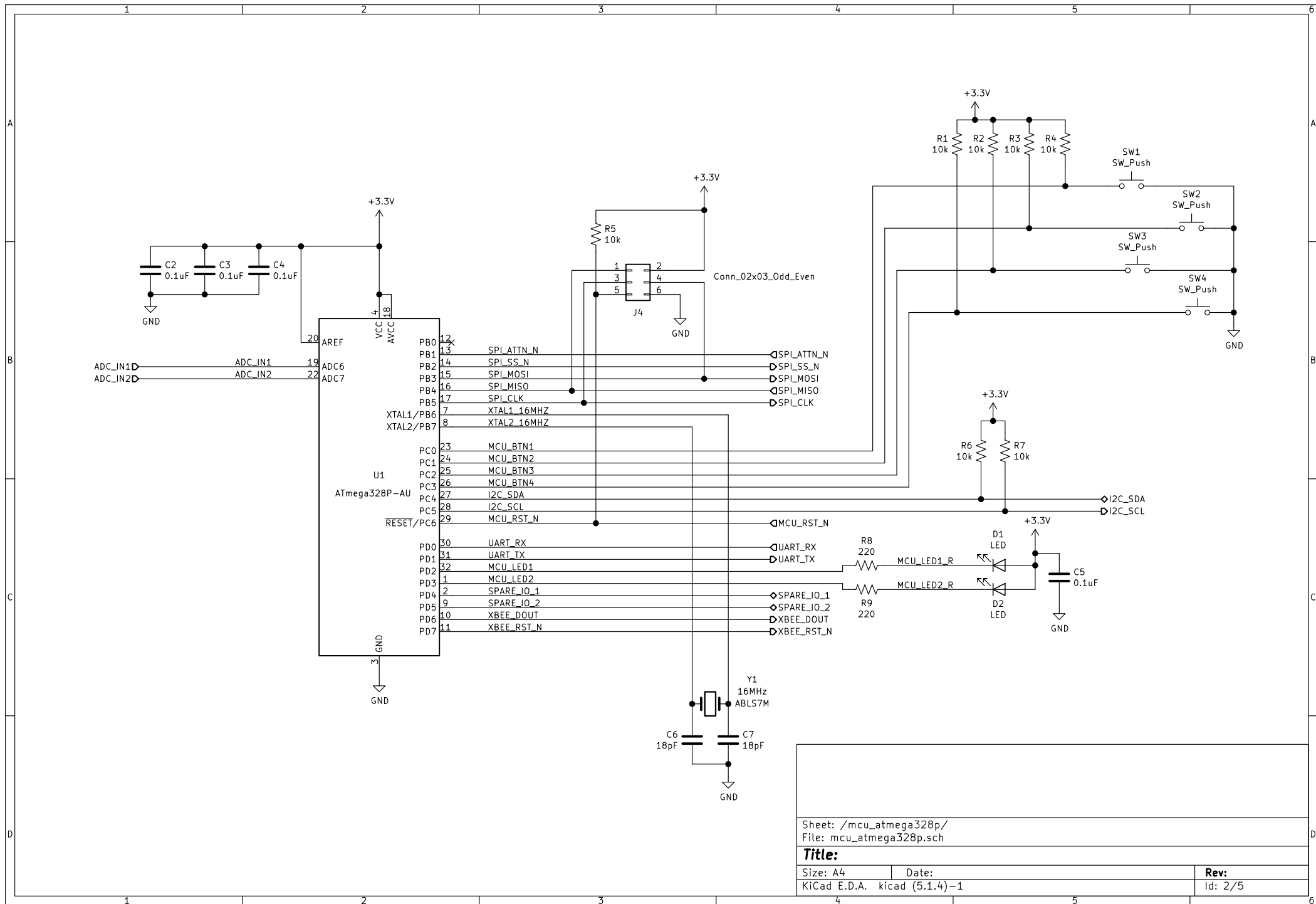


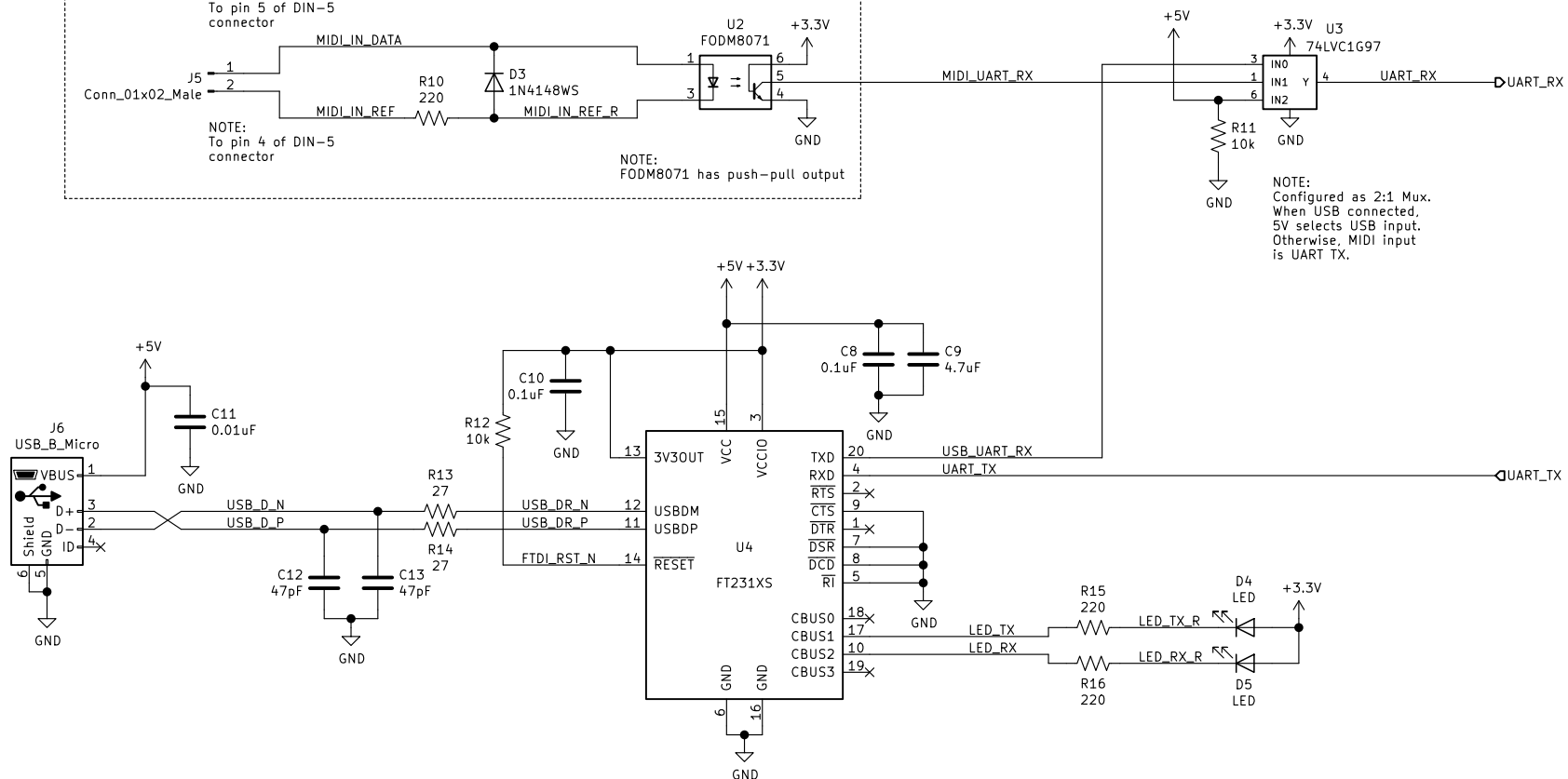
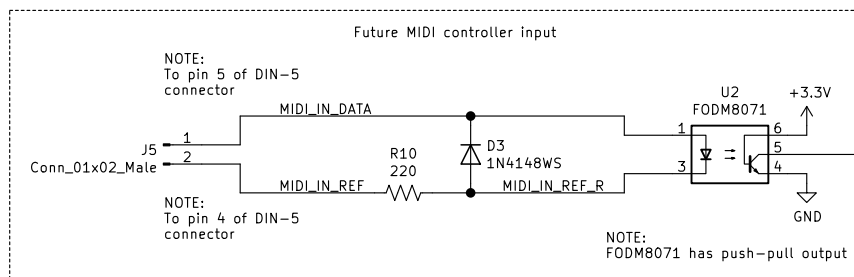
Sheet: batt\_chrg  
File: batt\_chrg.sch

NOTE:  
Do not populate battery  
charging manager and  
regulator for synthesizer  
processing board.



Sheet: /		
File: wiproc.sch		
Title:		
Size: A4	Date:	Rev:
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Sheet: /usb\_uart/  
File: usb\_uart.sch

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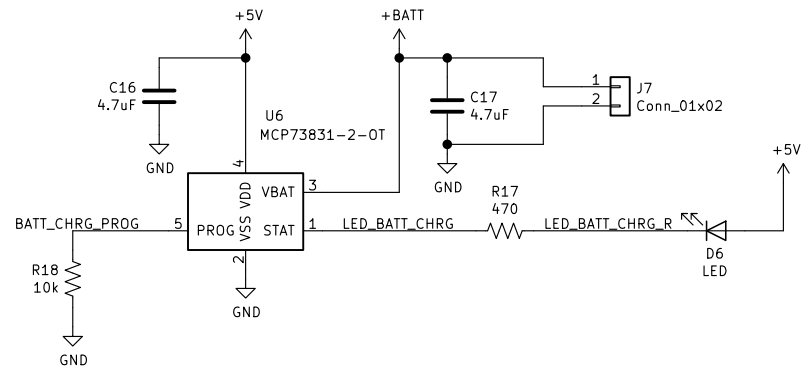
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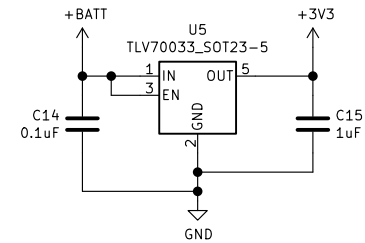
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**Rev:**

Id: 3/5



NOTE:  
10k = 100mA current limiting



Sheet: /batt\_chrg/  
File: batt\_chrg.sch

**Title:**

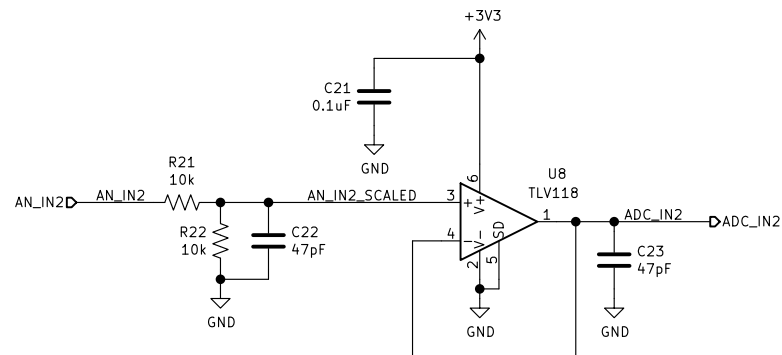
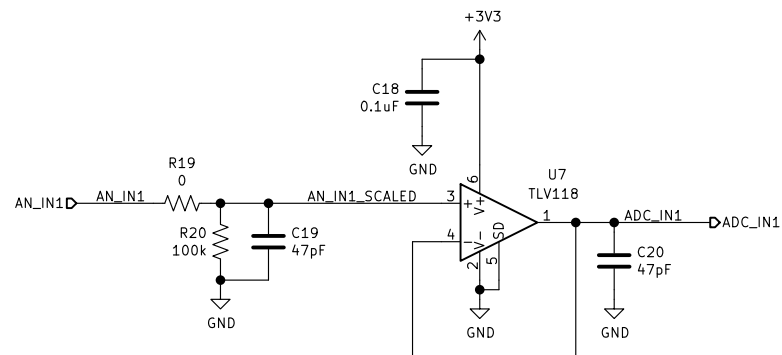
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Sheet: /analog\_cond/  
File: analog\_cond.sch

**Title:**

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Date:  
Id: 5/5

Rev: