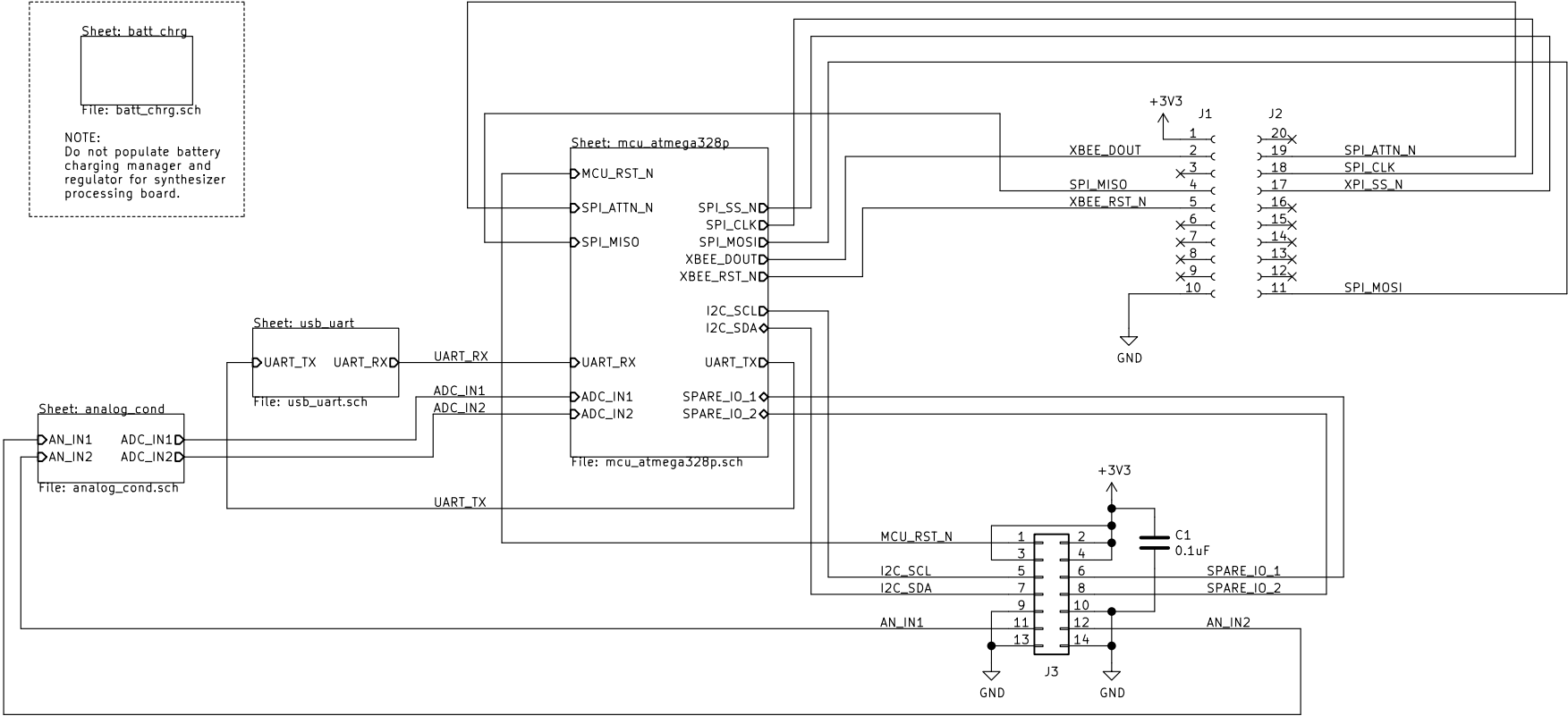


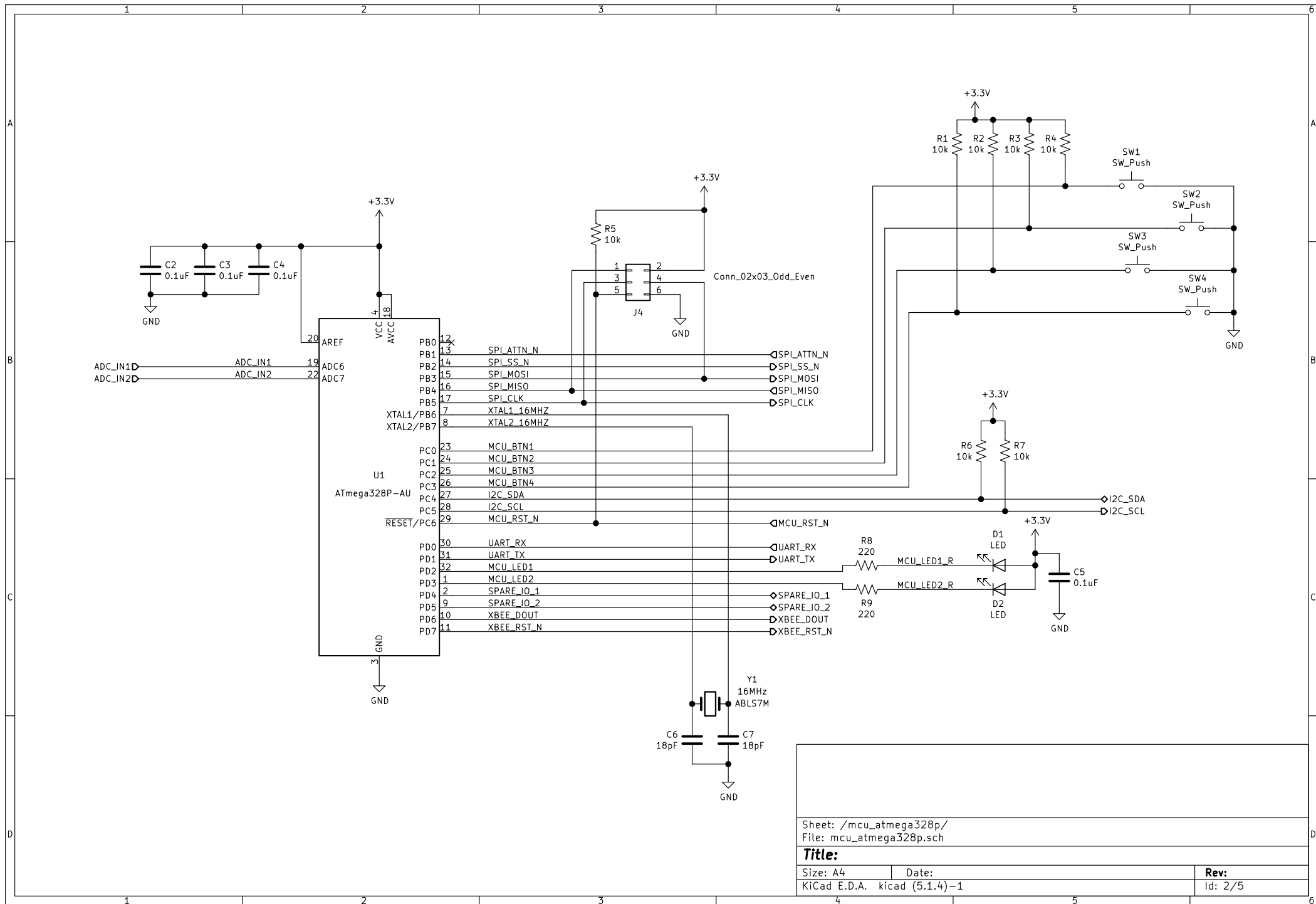
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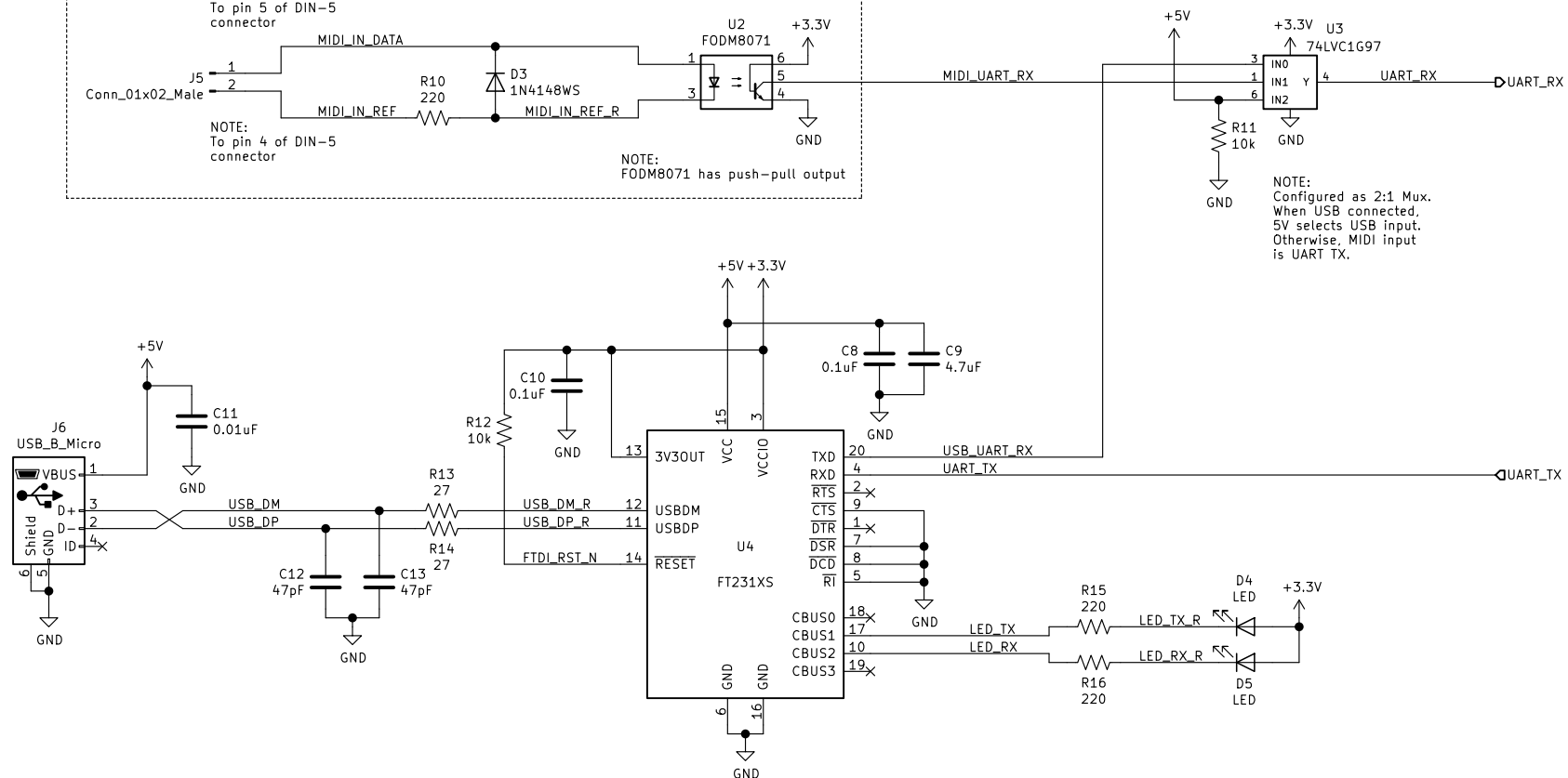
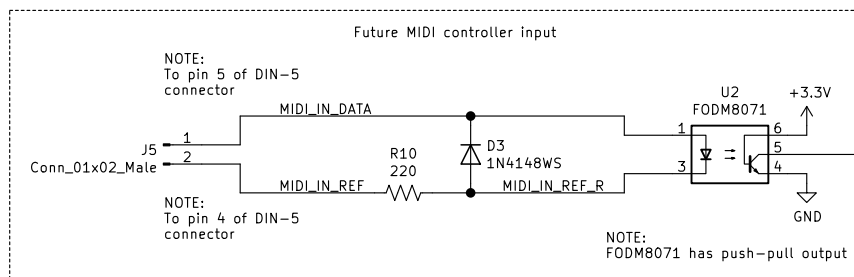
File: batt_chrg.sch

NOTE:
Do not populate battery
charging manager and
regulator for synthesizer
processing board.



Sheet: /		
File: wiproc.sch		
Title:		
Size: A4	Date:	Rev:
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Sheet: /usb_uart/
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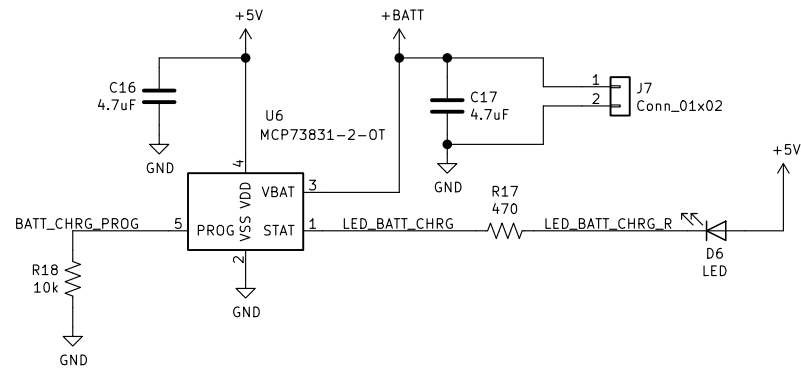
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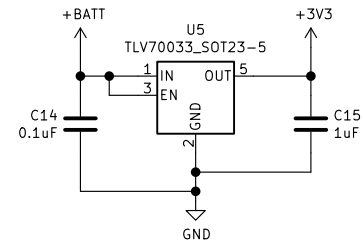
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Rev:

Id: 3/5



NOTE:
10k = 100mA current limiting



Sheet: /batt_chrg/
File: batt_chrg.sch

Title:

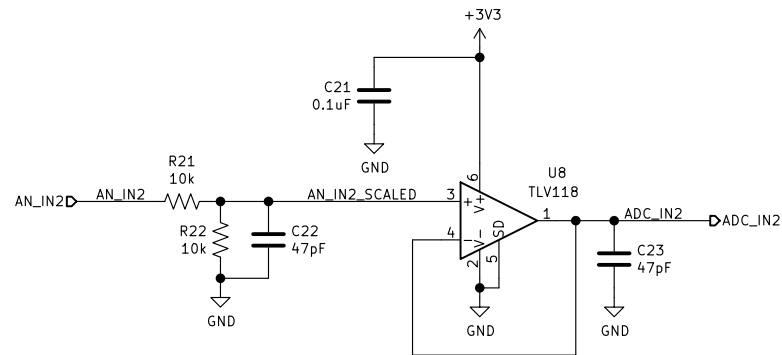
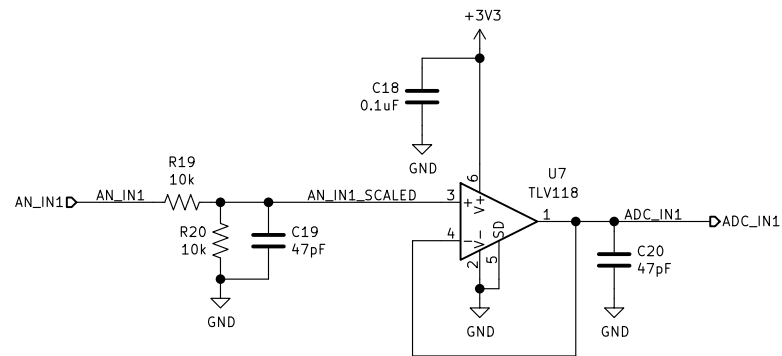
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Sheet: /analog_cond/
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Id: 5/5

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