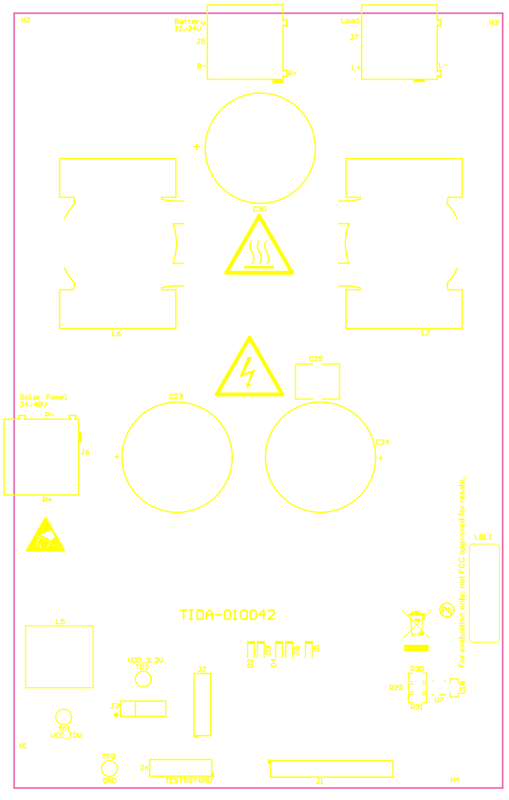
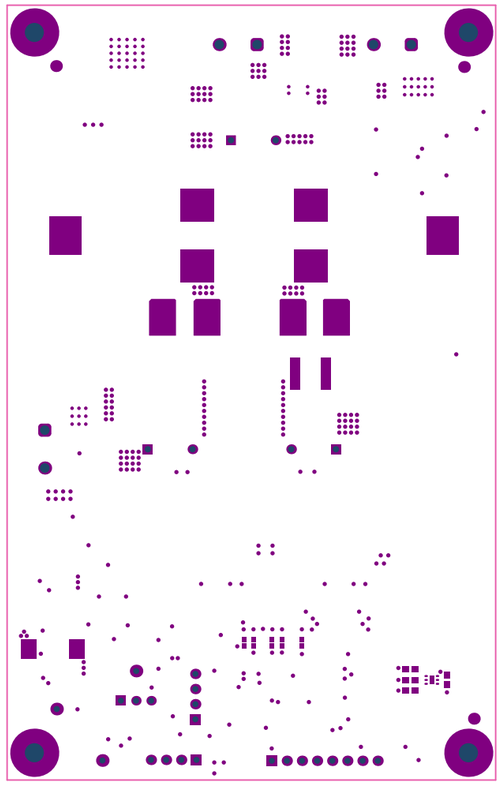
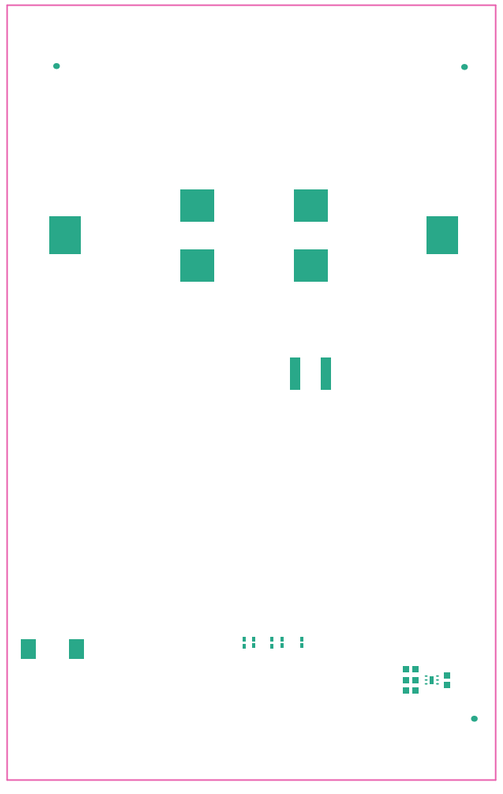


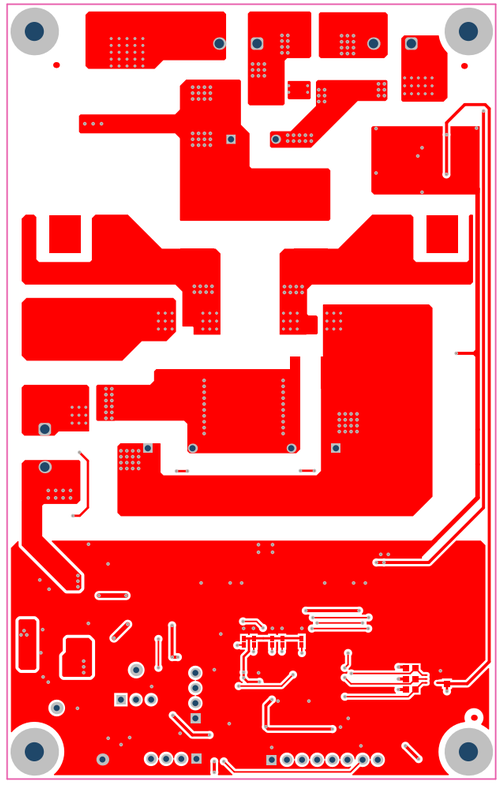
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-010042	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: TIDA-010042		
PLOT NAME = Top Overlay	GENERATED : 11/13/2018 8:50:20 AM		TEXAS INSTRUMENTS



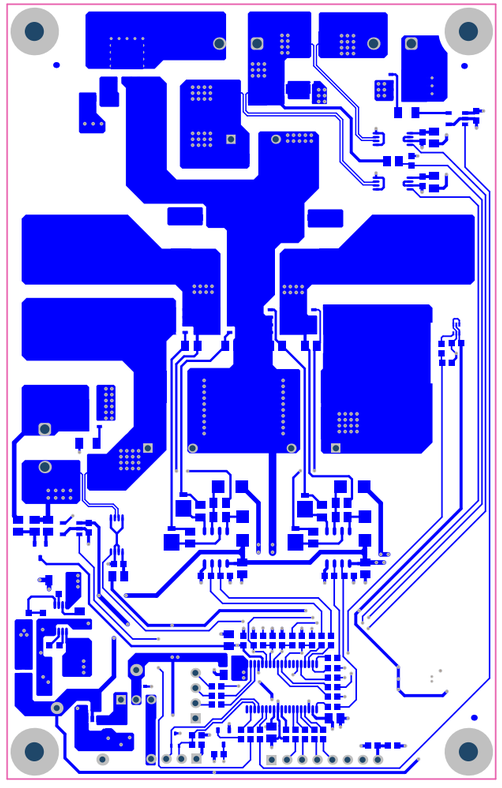
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-010042	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #: TIDA-010042		
PLOT NAME = Top Solder Mask	GENERATED : 11/13/2018 8:50:23 AM		TEXAS INSTRUMENTS



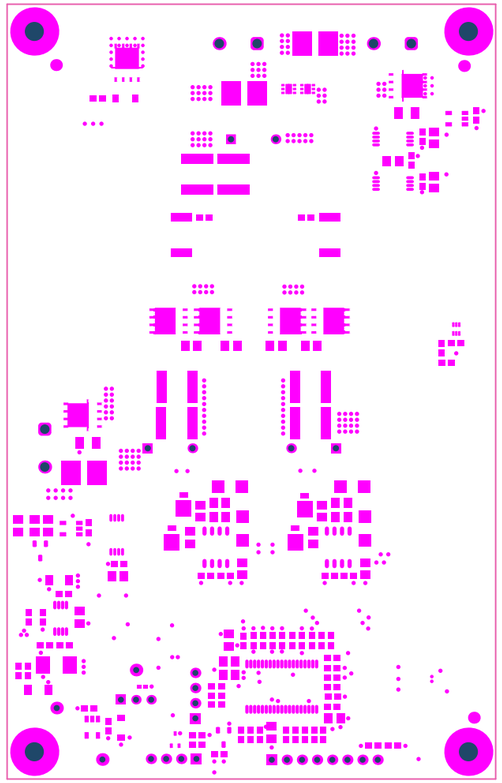
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-010042	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = Top Paste	TID #: TIDA-010042		
PLOT NAME = Top Paste	GENERATED : 11/13/2018 8:50:24 AM		TEXAS INSTRUMENTS



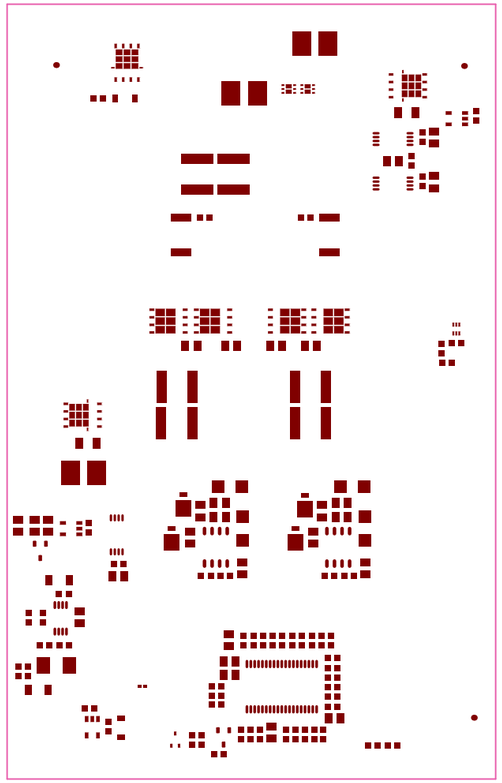
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-010042	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = Top Layer	TID #: TIDA-010042		
PLOT NAME = Top Layer	GENERATED : 11/13/2018 8:50:26 AM		TEXAS INSTRUMENTS



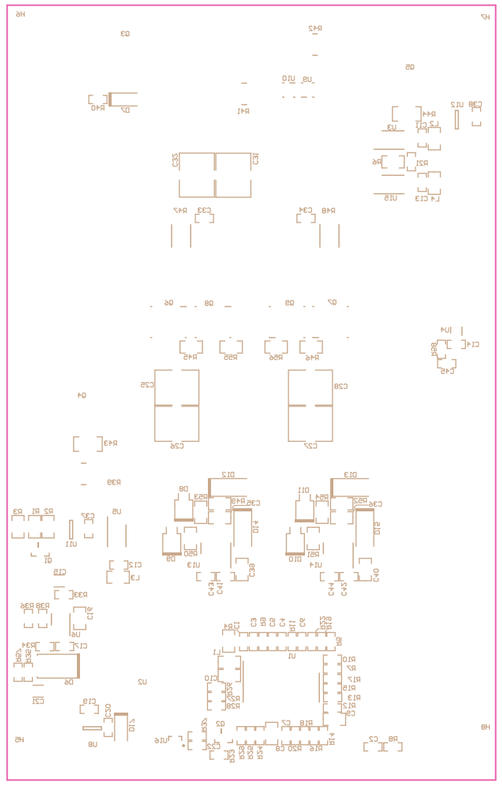
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-010042	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = Bottom Layer	TID #: TIDA-010042		
PLOT NAME = Bottom Layer	GENERATED : 11/13/2018 8:50:28 AM		TEXAS INSTRUMENTS



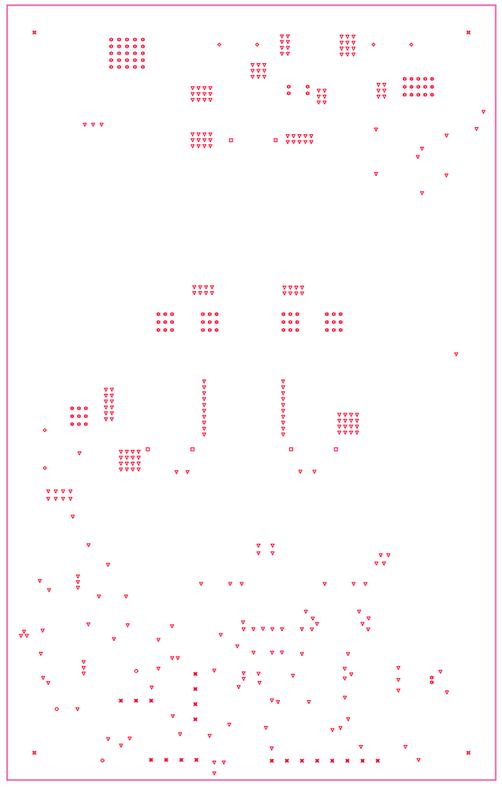
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-010042	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: TIDA-010042		
PLOT NAME = Bottom Solder Mask	GENERATED : 11/13/2018 8:50:29 AM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-010042	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = Bottom Paste	TID #: TIDA-010042		
PLOT NAME = Bottom Paste	GENERATED : 11/13/2018 8:50:31 AM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-010042	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = Bottom Overlay	TID #: TIDA-010042		
PLOT NAME = Bottom Overlay	GENERATED : 11/13/2018 8:50:33 AM		TEXAS INSTRUMENTS



Layer	Name	Material	Thickness	Constant	Board Layer Stack	Board Layer Stack
1	Top Overlay					
2	Top Solder	Solder Resist	0.40mil	3.5		
3	Top Layer	Copper	2.80mil			
4	Dielectric1	FR-4 High Tg	56.00mil	4.8		
5	Bottom Layer	Copper	2.80mil			
6	Bottom Solder	Solder Resist	0.40mil	3.5		
7	Bottom Overlay					

Symbol	Count	Hole Size	Plated	Hole Type
☼	91	7.87mil (0.200mm)	PTH	Round
▽	284	12.00mil (0.305mm)	PTH	Round
✖	19	39.37mil (1.000mm)	PTH	Round
○	3	40.00mil (1.016mm)	PTH	Round
□	6	41.34mil (1.050mm)	PTH	Round
◇	6	62.99mil (1.600mm)	PTH	Round
⌑	4	125.98mil (3.200mm)	PTH	Round
	413 Total			

Drill Table
FOR 7.874MIL DRILL +0/-7.874MIL
FOR 12MIL DRILL +0/-12MIL
FOR PTH DRILL +/-3MIL
FOR NPTH DRILL +/-2MIL

DESIGN INFORMATION

BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)
82MM X 130MM

Number of Layers : 2
MIN. TRACK WIDTH: 7 MIL
MIN. CLEARANCE: 7.7 MIL
MIN. VIA DRILL SIZE: 7.874 MIL
MINIMUM ANNULAR RING 5.5MIL EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

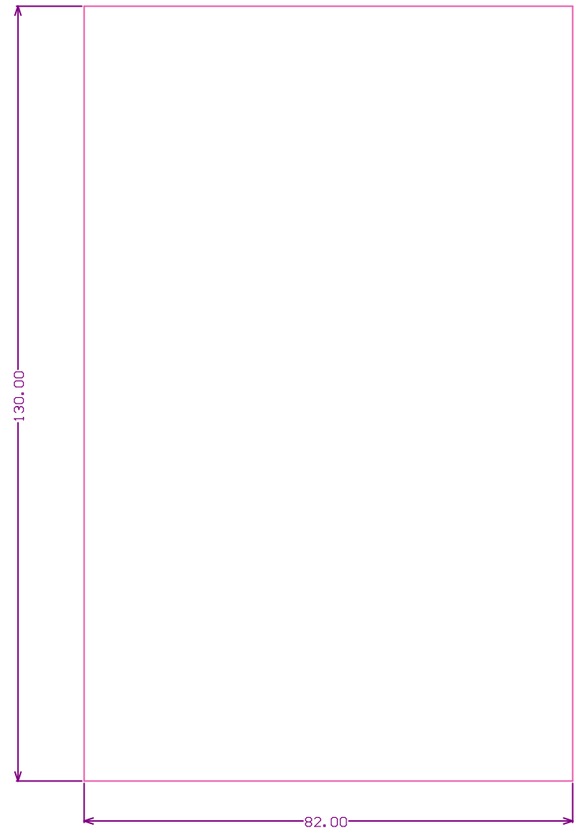
MATERIAL:
[ ] FR-408 [X] FR-4 High Tg [ ] OTHER
THICKNESS: [X] 63 MIL (1.6mm) +/-10% [ ] OTHER
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-
COPPER THICKNESS (FINISHED):
OUTER: [ ] 1.4MIL (1oz) [ ] 2MIL (1.4oz) [X] 2.8MIL (2oz)
INNER SIGNAL: [ ] 1.4MIL (1oz) [ ] 2.8MIL (2oz) [X] N/A
DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH MIN COPPER THICKNESS: [X] 1MIL [ ] OTHER
BOARD FINISH:
SILKSCREEN: [X] TOP [X] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER
SOLDER RESIST COLOR:
[X] GREEN [ ] BLUE [ ] OTHER
SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER
ARRAY/PANEL: [ ] CUT AND TRIM PER MECH LAYER 1
[ ] N.C. ROUTE [X] V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] UL 94V-0 [X] RoHS [ ] OTHER PER ORDER
ADDITIONAL REQUIREMENTS: VIA TENTING: YES [ ] NO [X]
MICROSECTION: [ ] YES IMPEDANCE CONTROL: YES [ ] NO [X]
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER
MANUFACTURER'S UL: [ ] RAIL [ ] METAL [X] SILK

Texas Instruments

PROJECT TITLE: TIDA-010042
DESIGNED FOR: Public Release
FILE NAME: TIDA-010042-E2_PCB.PcbDoc

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-010042	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: TIDA-010042		
PLOT NAME = Drill Drawing	GENERATED : 11/13/2018 8:50:38 AM	TEXAS INSTRUMENTS	

ENGINEER: Vaibhavi	LAYOUT BY: Avinash N
SCALE: 1.00	ALTIUM DESIGNER VERSION: 18.1.9.240



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-010042	REV: E2	SVN REV: Not In VersionControl
LAYER NAME =	TID #: TIDA-010042		
PLOT NAME = Board Dimensions	GENERATED : 11/13/2018 8:50:39 AM		TEXAS INSTRUMENTS

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