Car_alarm

```
NET Alarm  LOC = "R4";  # Bank = 3, Pin name = IO/VREF_3, Type = VREF, Sch name = LD7

 NET Seat LOC = "L13"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW5
NET Key LOC = "N17"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW6
NET Door LOC = "R17"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW7
```

Alarm_Fan

```
NET FAN  LOC = "F4";  # Bank = 3, Pin name = IO, Type = I/O, Sch name = LD6
NET ALARM  LOC = "R4";  # Bank = 3, Pin name = IO/VREF_3, Type = VREF, Sch name = LD7

NET HIGH_TEMP LOC = "L13"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW5
NET LOW_BATT LOC = "N17"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW6
NET CORD LOC = "R17"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW7
```

Prob2.1

```
NET F2  LOC = "R4";  # Bank = 3, Pin name = IO/VREF_3, Type = VREF, Sch name = LD7

NET D LOC = "L14"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW4
NET C LOC = "L13"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW5
NET B LOC = "N17"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW6
NET A LOC = "R17"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW7
```

Prob2.2

```
NET F2  LOC = "R4";  # Bank = 3, Pin name = IO/VREF_3, Type = VREF, Sch name = LD7

NET D LOC = "L14"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW4
NET C LOC = "L13"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW5
NET B LOC = "N17"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW6
NET A LOC = "R17"; # Bank = 1, Pin name = IP, Type = INPUT, Sch name = SW7
```

LAB #3 CAD

Ty MADSEN
ECEN 220
25-09-13

PIN ASSIGNMENTS, DOWNLOAD + TESTING

INPUTS			OUTPUTS			
			SIMULATION		DOWNLOAD	
CARD	LOWBATT	TEMP	ALARM	FAN	ALARM	FAN
0	0	0	0	0	0	0
0	0	1	0	1	0	1
0	1	0	1	0	1	0
0	1	1	1	0	1	0
1	0	0	0	0	0	0
1	0	1	0	1	0	1
1	1	0	0	0	0	0
1	1	1	0	1	0	1

ALARM_FAN

INPUTS				RESULTS			
				LAB 2.1		LAB 2.2	
A	B	C	D	SIM	DNLD	SIM	DNLD
0	0	0	0	0	0	0	0
0	0	0	1	1	1	0	0
0	0	1	0	0	0	1	1
0	0	1	1	0	0	1	1
0	1	0	0	0	0	0	0
0	1	0	1	1	1	1	1
0	1	1	0	0	0	1	1
0	1	1	1	0	0	1	1
1	0	0	0	0	0	1	1
1	0	0	1	1	1	1	1
1	0	1	0	0	0	1	1
1	0	1	1	0	0	0	0
1	1	0	0	0	0	1	1
1	1	0	1	0	0	1	1
1	1	1	0	0	0	0	0
1	1	1	1	0	0	0	0

INPUTS			OUTPUTS	
DOOR	KEY	[illegible]	SIM	DNLD
0	0	0	0	0
0	0	1	0	0
0	1	0	1	1
0	1	1	1	1
1	0	0	0	0
1	0	1	0	0
1	1	0	1	1
1	1	1	0	0

ANOMALIES:

NONE FOUND