

Rev	Date	Name	Specs/Instances Affected	Sheets Affected	Description
A0A	5/1/2015	N. Ellis	RN2483	ALL	- HT RN2483 LoRa EU (868/434 MHz) Module Initial RTP Candidate
	5/14/2015	Phuwaneth T.		VBT	- Add VB Module "DatalogFunctions"
				flow_lora_rm2483	- Add "q1-prd-std" - Revise test bin number - Revise test flow to MTAI format
				X1_MAN_47L_MODULE	- Add new channel map "X1_MAN478L_MODULE" - Add "ITL_x1_Config" to support channel map x1 site.
A0B	6/4/2015	Phuwaneth T.	RN2483	flow_lora_rm2483 VBT	- Revise UTL of EU_I_IDLE form 5mA to be 7mA both FT&QC to recover false fail issue. - Add new module "setupDlogOutput_on" in VBT for auto save data log file.
A0C	7/9/2015	N. Ellis	RN2483	TI, FLOW, several VB modules	- HT RN2483 LoRa EU (868/434 MHz) Module Multi-Site Candidate
A0D	9/1/2015	N. Ellis	RN2483 & RN2903	MOST	HT x4 RN2483 and RN2903 Combination FT/QC Program. RN2483 expanded to x4 (128-pin J750_AERO). NOTE: RN2903 not implemented.
	10/12/2015	Phuwaneth T.			New GPIO functional test for testing programmability of GPIO0-GPIO13 on module.
					Revise the TP to match with MTAI format.
A0E	10/29/2015	N. Ellis	RN2903	MOST RN2903	- HT x4 RN2483 and RN2903 Combination FT/QC Program.
	11/9/2015	Phuwaneth T.	RN2483 & RN2903	RN2483	- Expanded to x4 (128-pin J750_AERO). RN2903 x4 implemented.
				All	- Revise the TP to match with MTAI format.
B0A	11/23/2015	Phuwaneth T.	RN2483	TI, FLOW, several VB modules	- Add more sheet below to support qtp module 1. flow_pkg_lora 2. MasterBinList 3. lvi_pgm 4. tm_std 5. Pattern_sub 6. (S)QTP Setup 8. ti_prod_rm - Add 9 patterns for qtp module 1. icsp_dm_rd_qtp1k_cp.PAT 2. ics
B0B	9/28/2016	Phuwaneth T.	RN2483	icsp_pfm_rd_qtp64k_cp, icsp_pfm_rd_qtp64K	- Change UID data on address 200000 - 200007 from FFFF to be F0F0 to mach with the actual reading from the module.
B0C	10/12/2016	Phuwaneth T.	RN2903, RN2483	icsp_pfm_rd_qtp64k_cp, icsp_pfm_rd_qtp64K, icsp_pfm_wr_qtp64K	- Separate QTP pattern file for RN2483 and RN2903 to fix UID issue.
B0D	1/11/2017	Phuwaneth T.	RN2903, RN2483	flow_pkg_lora VBT	- Add test "EU_I_SLEEP_REV, NA_I_SLEEP_REV" for deep sleep measurement. - Add new part name "RN2903A" - Add VBT Sub routine " rn2483_i_sleep_rev, rn2903a_i_sleep_rev" - Add new pattern "uart_m2483_sleep_revised"
B0E	1/25/2017	Phuwaneth T.	RN2483, RN2903, RN2903A	VBT	- Unload pattern "uart_rm2483r1_gpio_full" in VBT Onprogramvalidated.
B0F	2/16/2017	Ty S.	RN2483, RN2903, RN2903A	VBT	-Added xtal offset test
	2/20/2017	Phuwaneth T.	RN2483A	flow_pkg_lora VBT	- Added new part name "RN2483A"
C0A	2/23/2017	Phuwaneth T.	All	flow_pkg_lora VBT	- API conversion project. Added/Modified specific modules for the TevAXRF and NISync conversion - Optimize settling time of IPD test from 3.75s to be 4.5s.
C0B	10/11/2017	Phuwaneth T.	All	VBT	- Edit UID Pattern to match with the actual part of RN2483A.
C0C	10/18/2017	Phuwaneth T.	RN2903A	uart_rm2903_gpio_full	- Add more delay time 5ms form 3ms at vector number 4825 to support new FW v1.0.3 of RN2903A.
??	1/10/2018	Ty S.	RN2483	VBT, TI, Flow, PAT	Added 433 MHz TX CW power test (copy of 868 but 433 added).
					Flow sheet (one row, noted in comments)
					TI sheet (one row, noted in comments)
					Exec_IP_Module - modied RFOOnProgramValidated_LoRa, only one line, noted in comments
					VBT_RF module - added rn2483_tx433_cw function.
COD	1/31/2018	Ty S.	RN2903 and RN2483 (868 & 433)	VBT, TI, Flow, Pats	added pattern: uart_rm2483_tx433_cw.pat to pattern file
					Added 2 updated patterns and a new pattern (433MHz)
					modified vBT to capture RX packet over uart
					modified AC specs and TSB for patten tests
					Open issue: the signal sourced from the AXRF doesn't appear to be reaching the DUT (all the DUTs in the socket fail).
open issue: the datlog test numbers don't match the flow-sheet listed test numbers					