

CDCL SAT Solvers

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Theory and Practice of SAT Solving

Dagstuhl Workshop

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The Success of SAT

- Well-known NP-complete decision problem

[C71]

The Success of SAT

- Well-known NP-complete decision problem
- In practice, SAT is a success story of Computer Science
 - Hundreds (even more?) of practical applications

[C71]

The Success of SAT

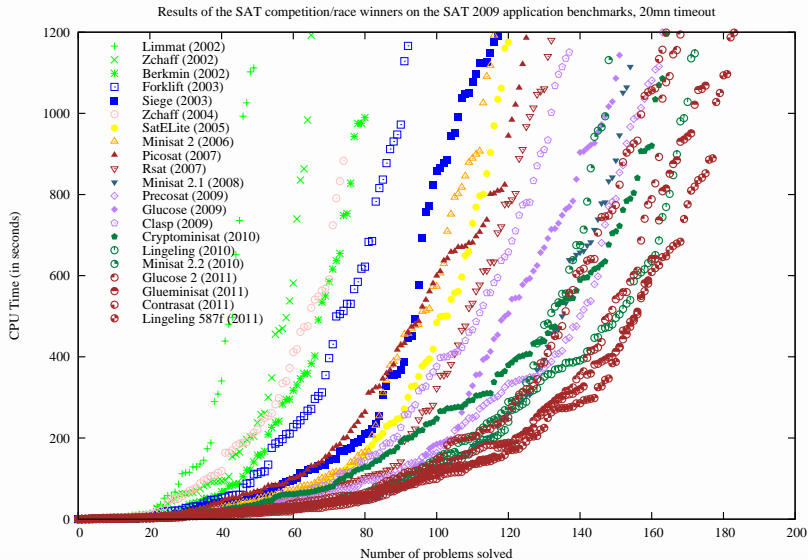
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[C71]



SAT Solver Improvement

[Source: Le Berre&Biere 2011]



SAT-Based Problem Solving



This Talk

- Review key ideas in implementing CDCL SAT solvers
 - Review standard concepts
 - ▶ Unit propagation
 - ▶ Resolution
 - ▶ Proof traces
 - ▶ ...
 - Outline organization of DPLL/CDCL SAT solvers
 - Survey most effective techniques
 - ▶ Clause learning & non-chronological backtracking
 - ▶ UIPs
 - ▶ Clause minimization
 - ▶ Search restarts
 - ▶ Several heuristics
 - ▶ ...

Outline

Basic Definitions

DPLL Solvers

CDCL Solvers

What Next in CDCL Solvers?

CNF Encodings

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Basic Definitions

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CNF Encodings

Preliminaries

- **Variables:** $w, x, y, z, a, b, c, \dots$
- **Literals:** $w, \bar{x}, \bar{y}, a, \dots$, but also $\neg w, \neg y, \dots$
- **Clauses:** disjunction of literals **or** set of literals
- **Formula:** conjunction of clauses **or** set of clauses
- **Model** (**satisfying assignment**): partial/total mapping from variables to $\{0, 1\}$ that satisfies formula
- Formula can be **SAT**/**UNSAT**

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- **Model (satisfying assignment):** partial/total mapping from variables to $\{0, 1\}$ that satisfies formula
- Formula can be **SAT/UNSAT**
- Example:

$$\mathcal{F} \triangleq (r) \wedge (\bar{r} \vee s) \wedge (\bar{w} \vee a) \wedge (\bar{x} \vee b) \wedge (\bar{y} \vee \bar{z} \vee c) \wedge (\bar{b} \vee \bar{c} \vee d)$$

– Example models:

- ▶ $\{r, s, a, b, c, d\}$
- ▶ $\{r, s, \bar{x}, y, \bar{w}, z, \bar{a}, b, c, d\}$

Resolution

- Resolution rule:

[DP60,R65]

$$\frac{(\alpha \vee x) \qquad (\beta \vee \bar{x})}{(\alpha \vee \beta)}$$

- Complete proof system for propositional logic

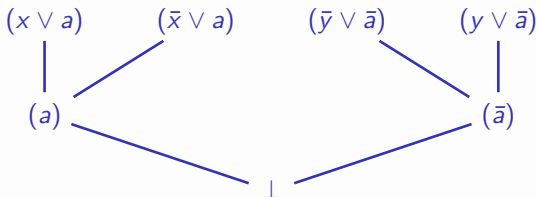
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- Extensively used with (CDCL) SAT solvers

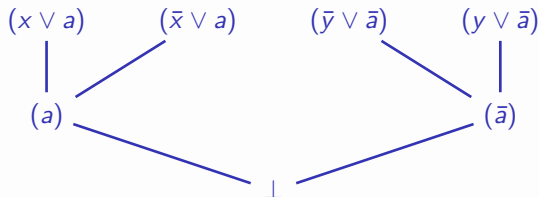
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- Self-subsuming resolution (with $\alpha' \subseteq \alpha$):

[e.g. SP04,EB05]

$$\frac{(\alpha \vee x) \quad (\alpha' \vee \bar{x})}{(\alpha)}$$

- (α) subsumes $(\alpha \vee x)$

Unit Propagation

$$\begin{aligned}\mathcal{F} = & (r) \wedge (\bar{r} \vee s) \wedge \\ & (\bar{w} \vee a) \wedge (\bar{x} \vee \bar{a} \vee b) \\ & (\bar{y} \vee \bar{z} \vee c) \wedge (\bar{b} \vee \bar{c} \vee d)\end{aligned}$$

Unit Propagation

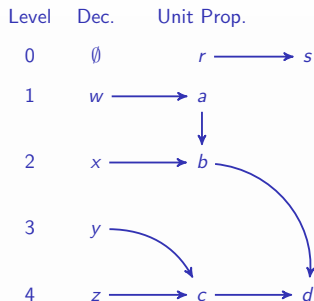
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- Decisions / Variable Branchings:
 $w = 1, x = 1, y = 1, z = 1$

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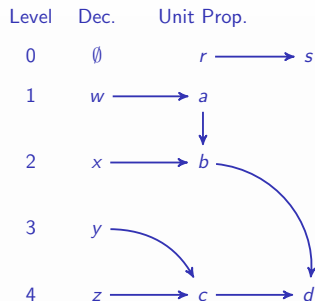


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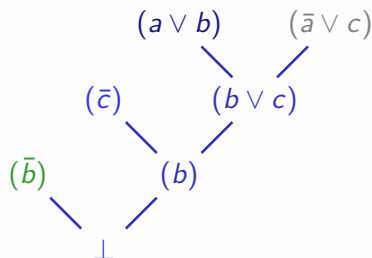


- Additional definitions:

- Antecedent (or reason) of an implied assignment
 - $(\bar{b} \vee \bar{c} \vee d)$ for d
- Associate assignment with decision levels
 - $w = 1 @ 1, x = 1 @ 2, y = 1 @ 3, z = 1 @ 4$
 - $r = 1 @ 0, d = 1 @ 4, \dots$

Resolution Proofs

- Refutation of unsatisfiable formula by iterated resolution operations produces **resolution proof**
- An example:
 $\mathcal{F} = (\bar{c}) \wedge (\bar{b}) \wedge (\bar{a} \vee c) \wedge (a \vee b) \wedge (a \vee \bar{d}) \wedge (\bar{a} \vee \bar{d})$
- Resolution proof:



- A modern SAT solver can generate resolution proofs using clauses learned by the solver

Unsatisfiable Cores & Proof Traces

- CNF formula:

$$\mathcal{F} = (\bar{c}) \wedge (\bar{b}) \wedge (\bar{a} \vee c) \wedge (a \vee b) \wedge (a \vee \bar{d}) \wedge (\bar{a} \vee \bar{d})$$

Level	Dec.	Unit Prop.
0	\emptyset	$\bar{b} \longrightarrow a$ \downarrow $\bar{c} \longrightarrow \perp$

Implication graph with **conflict**

Unsatisfiable Cores & Proof Traces

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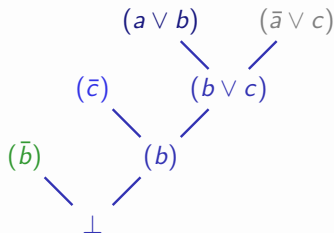
Proof trace \perp : $(\bar{a} \vee c)$ $(a \vee b)$ (\bar{c}) (\bar{b})

Unsatisfiable Cores & Proof Traces

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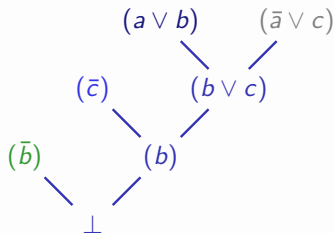
Resolution proof follows **structure of conflicts**

Unsatisfiable Cores & Proof Traces

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Unsatisfiable subformula (core): $(\bar{c}), (\bar{b}), (\bar{a} \vee c), (a \vee b)$

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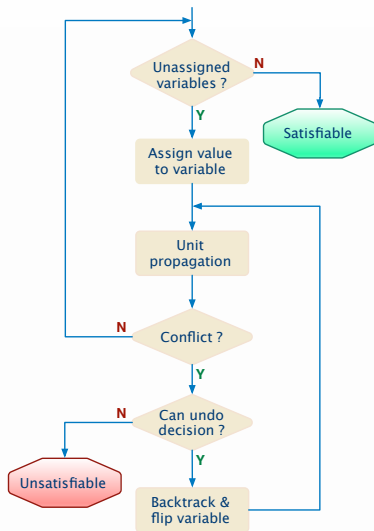
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CDCL Solvers

What Next in CDCL Solvers?

CNF Encodings

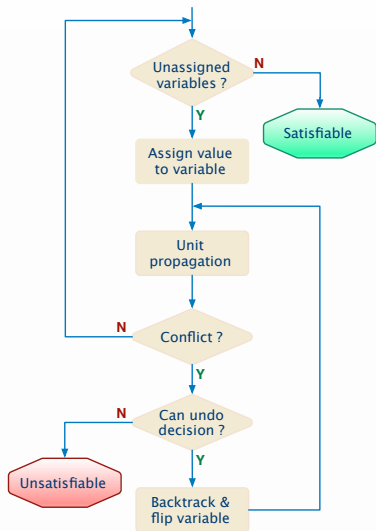
The DPLL Algorithm



- Optional: pure literal rule

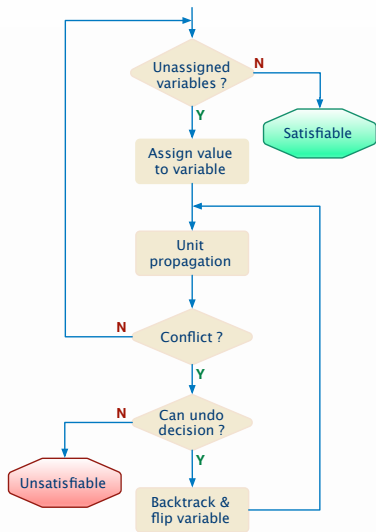
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$$\mathcal{F} = (x \vee y) \wedge (a \vee b) \wedge (\bar{a} \vee b) \wedge (a \vee \bar{b}) \wedge (\bar{a} \vee \bar{b})$$



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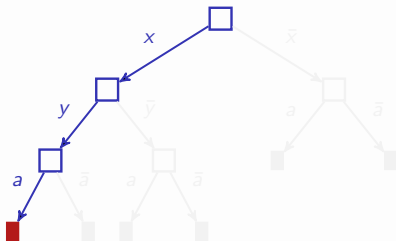
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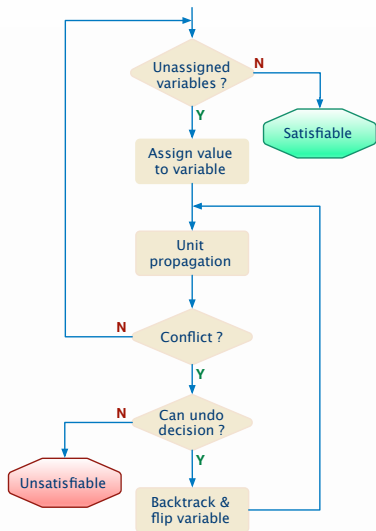
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0	\emptyset	
1	x	
2	y	
3	a	$a \longrightarrow b \longrightarrow \perp$



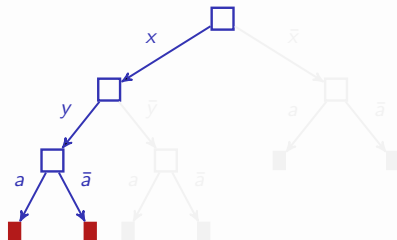
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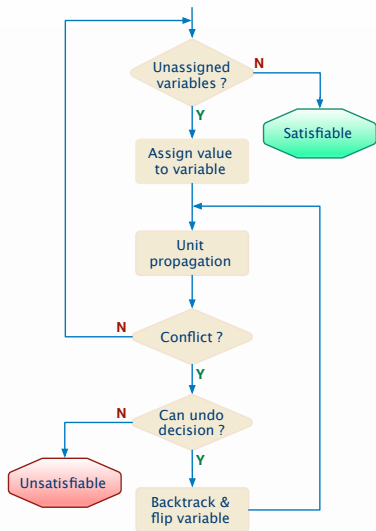
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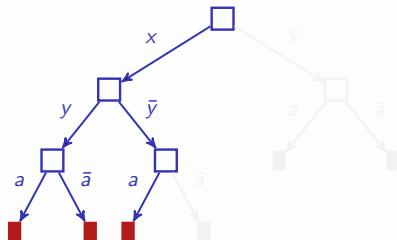
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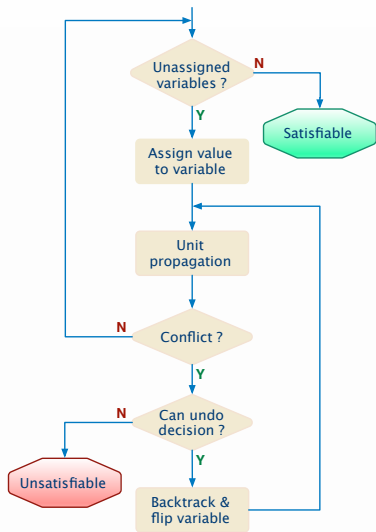
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2	\bar{y}	
3	$a \longrightarrow b \longrightarrow \perp$	



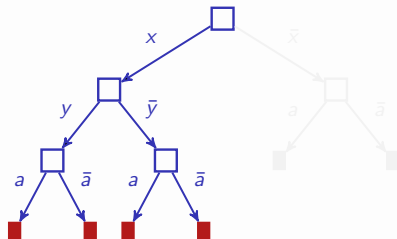
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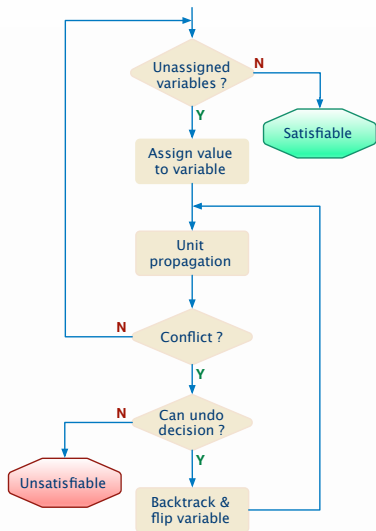
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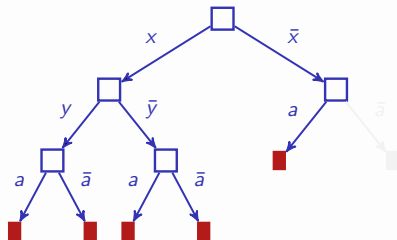


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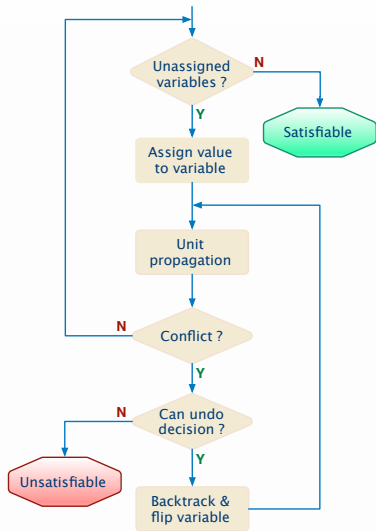
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Level	Dec.	Unit Prop.
0	\emptyset	
1	$\bar{x} \longrightarrow y$	
2	$a \longrightarrow b$	\perp

A curved arrow points from the 'a' in the Level 2 decision to the 'a' in the Level 2 unit propagation, indicating a conflict.



The DPLL Algorithm

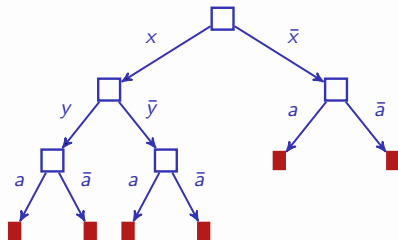


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Level	Dec.	Unit Prop.
0	\emptyset	
1	$\bar{x} \longrightarrow y$	
2	$\bar{a} \longrightarrow \bar{b}$	\perp

A curved arrow points from the 'Dec.' column at level 2 to the 'Unit Prop.' column at level 2, indicating a conflict.



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Basic Definitions

DPLL Solvers

CDCL Solvers

What Next in CDCL Solvers?

CNF Encodings

What is a CDCL SAT Solver?

- Extend DPLL SAT solver with:

[DP60,DLL62]

- Clause learning & non-chronological backtracking

[MSS96,BS97,Z97]

- Search restarts

[GSK98,BMS00,H07,B08]

- Lazy data structures

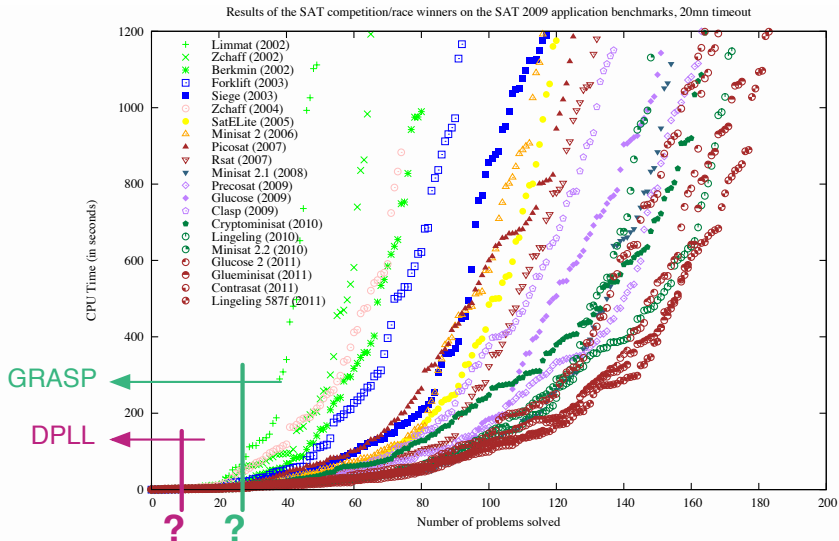
- Conflict-guided branching

- ...

What is a CDCL SAT Solver?

- Extend DPLL SAT solver with:
 - Clause learning & non-chronological backtracking [DP60,DLL62]
 - ▶ Exploit UIPs [MSS96,BS97,Z97]
 - ▶ Minimize learned clauses [MSS96,SSS12]
 - ▶ Opportunistically delete clauses [SB09,VG09]
 - Search restarts [MSS96,MSS99,GN02]
 - Lazy data structures [GSK98,BMS00,H07,B08]
 - ▶ Watched literals [MMZZM01]
 - Conflict-guided branching [MMZZM01]
 - ▶ Lightweight branching heuristics [PD07]
 - ▶ Phase saving
 - ...

How Significant are CDCL SAT Solvers?



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CDCL Solvers

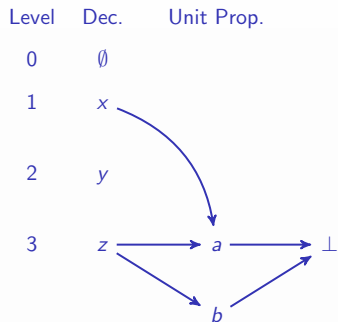
Clause Learning, UIPs & Minimization

Search Restarts & Lazy Data Structures

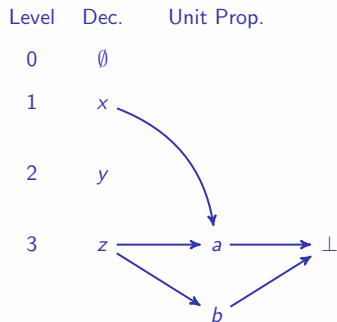
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Clause Learning

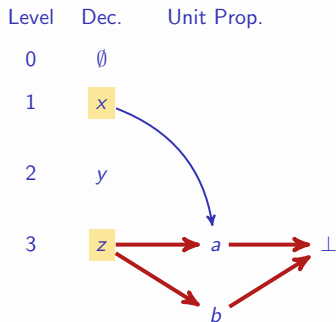


Clause Learning



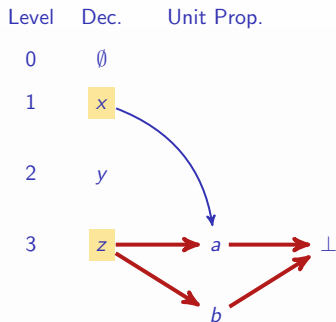
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Clause Learning



- Analyze conflict
 - Reasons: x and z
 - ▶ Decision variable & literals assigned at lower decision levels

Clause Learning



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 - Reasons: x and z
 - ▶ Decision variable & literals assigned at lower decision levels
 - Create **new** clause: $(\bar{x} \vee \bar{z})$

Clause Learning

Level Dec. Unit Prop.

0 \emptyset

1 x

2 y

3 z

```
graph LR; x[x] -- blue --> a[a]; z[z] -- red --> a; z -- red --> b[b]; a -- red --> perp[⊥]; b -- red --> perp;
```

$(\bar{a} \vee \bar{b})$

$(\bar{z} \vee b)$

$(\bar{x} \vee \bar{z} \vee a)$

- Analyze conflict
 - Reasons: x and z
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- Can relate **clause learning** with resolution

Clause Learning

Level Dec. Unit Prop.

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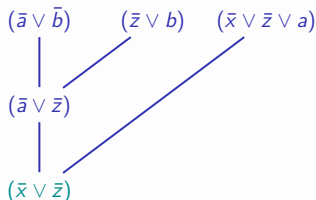
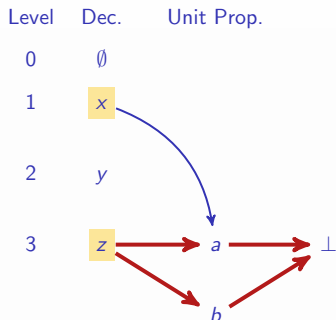
2 y

3 z a \perp
 b

$$\begin{array}{ccc} (\bar{a} \vee \bar{b}) & (\bar{z} \vee b) & (\bar{x} \vee \bar{z} \vee a) \\ | & \swarrow & \\ (\bar{a} \vee \bar{z}) & & \end{array}$$

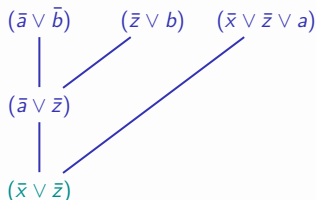
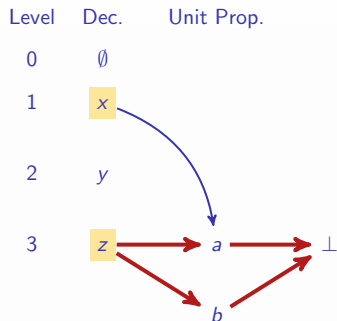
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Clause Learning



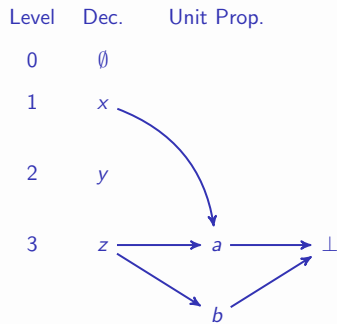
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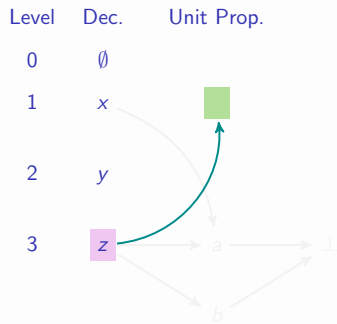


- Analyze conflict
 - Reasons: x and z
 - ▶ Decision variable & literals assigned at lower decision levels
 - Create **new** clause: $(\bar{x} \vee \bar{z})$
- Can relate **clause learning** with resolution
 - Learned clauses result from (**selected**) resolution operations

Clause Learning – After Backtracking



Clause Learning – After Backtracking



- Clause $(\bar{x} \vee \bar{z})$ is **asserting** at decision level 1

Clause Learning – After Backtracking

Level	Dec.	Unit Prop.
-------	------	------------

0	\emptyset	
---	-------------	--

1	x	
---	-----	--

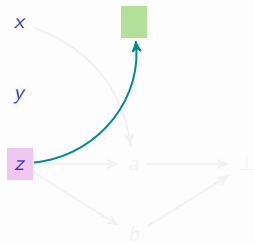
2	y	
---	-----	--

3	z	
---	-----	--

Level	Dec.	Unit Prop.
-------	------	------------

0	\emptyset	
---	-------------	--

1	$x \longrightarrow \bar{z}$	
---	-----------------------------	--



- Clause $(\bar{x} \vee \bar{z})$ is **asserting** at decision level 1

Clause Learning – After Backtracking

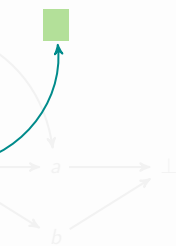
Level	Dec.	Unit Prop.
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0	\emptyset	
---	-------------	--

1	x	
---	-----	--

2	y	
---	-----	--

3	z	
---	-----	--



Level	Dec.	Unit Prop.
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0	\emptyset	
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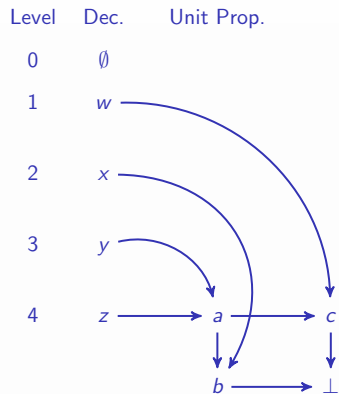
1	$x \longrightarrow \bar{z}$	
---	-----------------------------	--

- Clause $(\bar{x} \vee \bar{z})$ is **asserting** at decision level 1
- Learned clauses are **always** asserting
- Backtracking differs from plain DPLL:
 - Always **backtrack after a conflict**

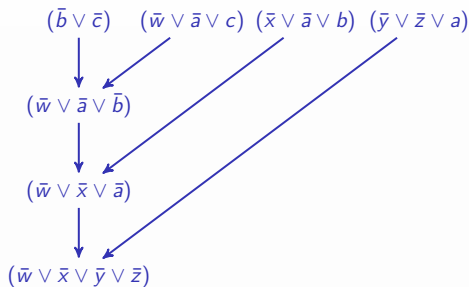
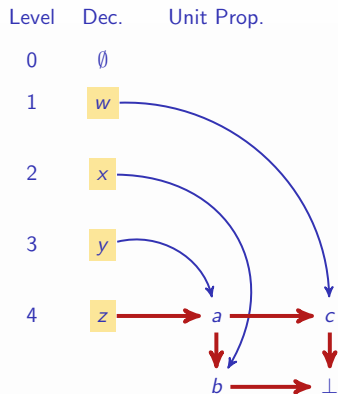
[MSS96,MSS99]

[MMZZM01]

Unique Implication Points (UIPs)

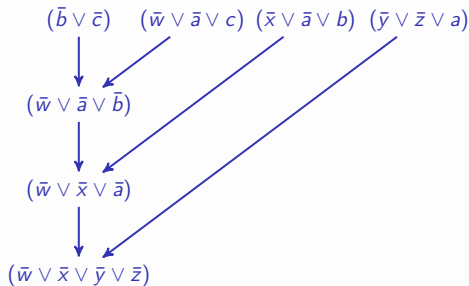
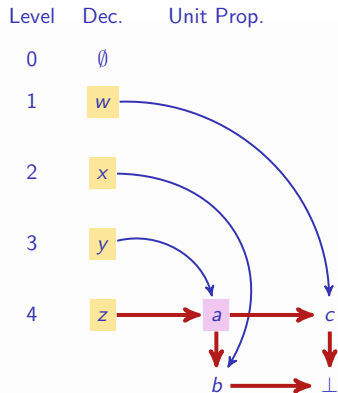


Unique Implication Points (UIPs)



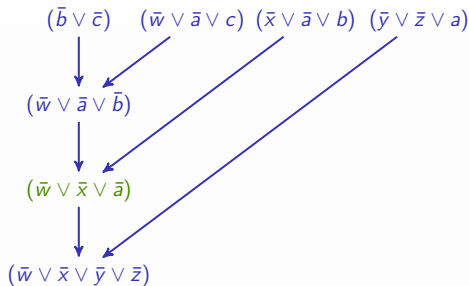
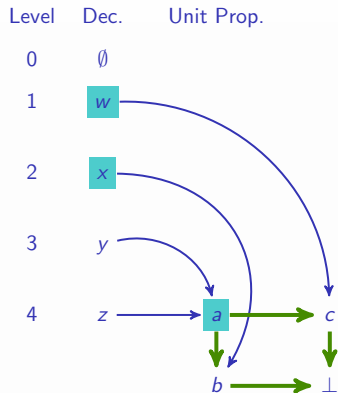
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Multiple UIPs

Level Dec. Unit Prop.

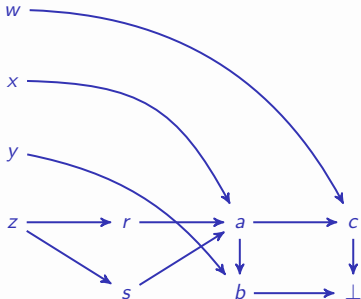
0 \emptyset

1 w

2 x

3 y

4 $z \rightarrow r \rightarrow a \rightarrow c$
 $\searrow \quad \nearrow$
 $s \quad b \rightarrow \perp$



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Level	Dec.	Unit Prop.
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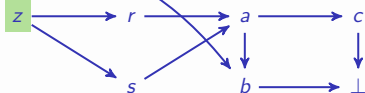
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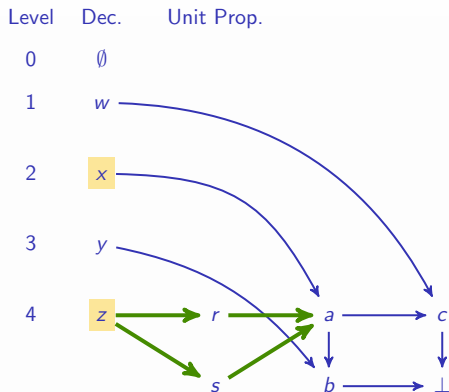
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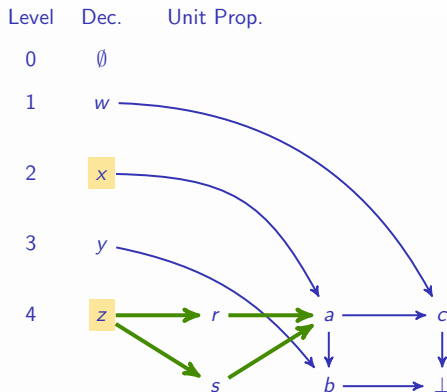
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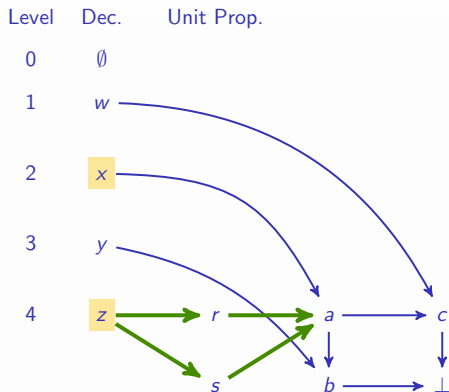
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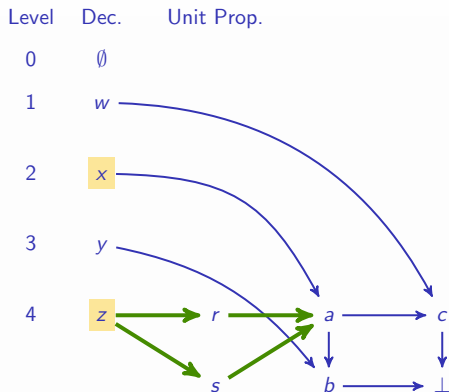
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 - First UIP learning proposed in Chaff
- Not used in recent state of the art CDCL SAT solvers

[MSS96]

[MMZZM01]

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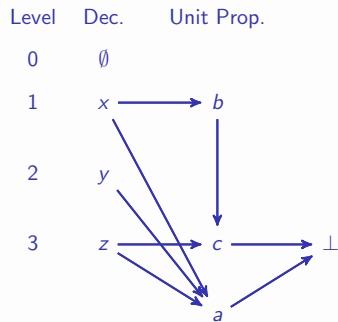
[MSS96]

[MMZZM01]

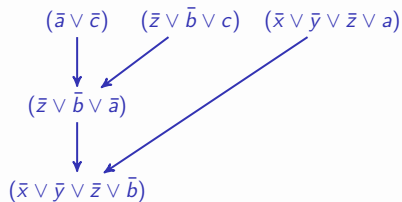
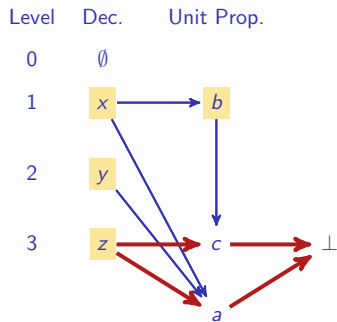
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- Recent results show it can be beneficial on current instances

[SSS12]

Clause Minimization I

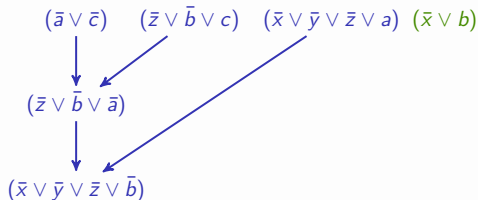
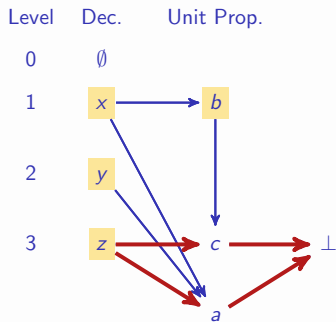


Clause Minimization I



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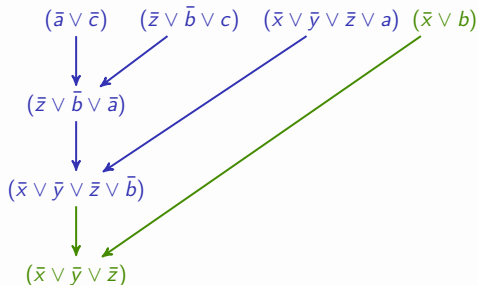
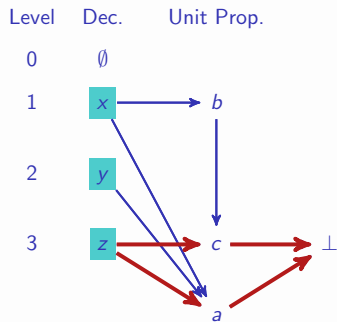
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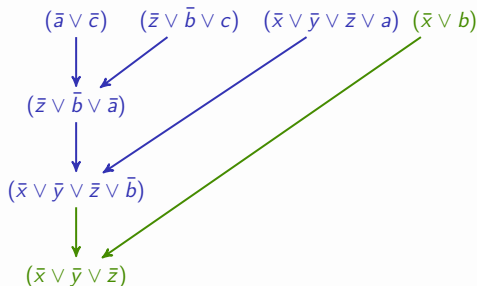
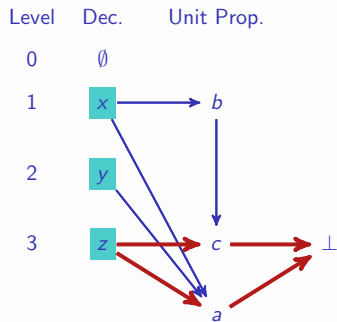
[SB09]

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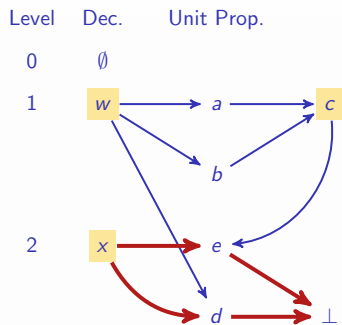
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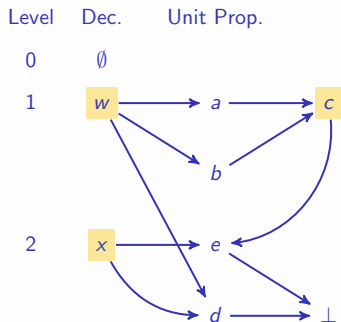
2 $x \rightarrow e$
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 $e \rightarrow \perp$
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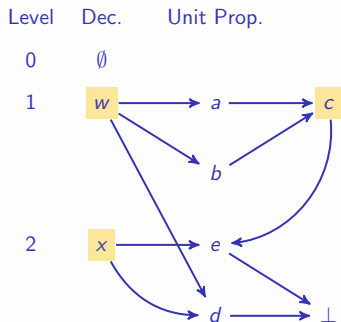
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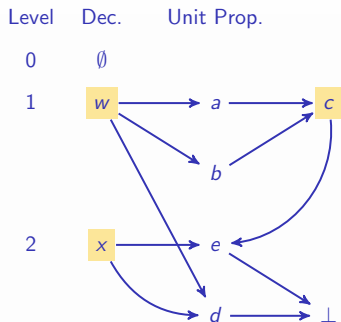
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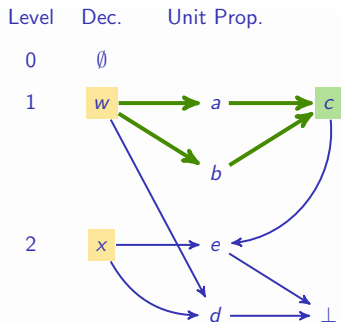


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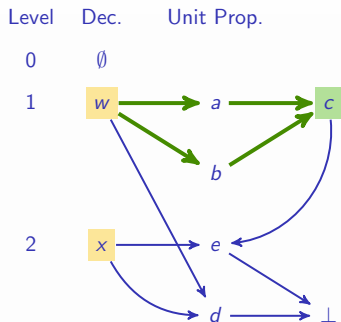


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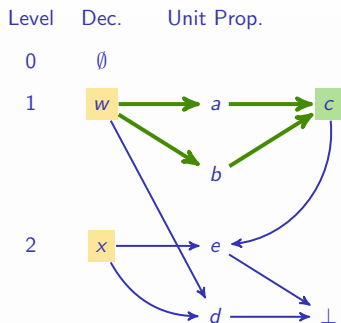


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[SB09]

Outline

Basic Definitions

DPLL Solvers

CDCL Solvers

Clause Learning, UIPs & Minimization

Search Restarts & Lazy Data Structures

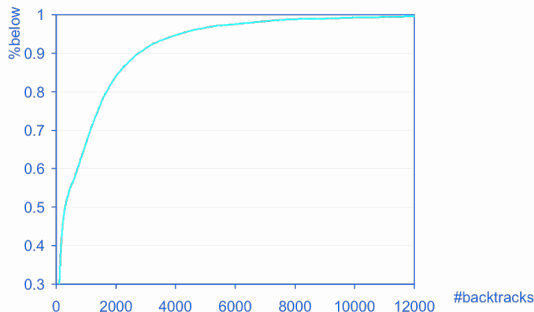
What Next in CDCL Solvers?

CNF Encodings

Search Restarts I

- Heavy-tail behavior:

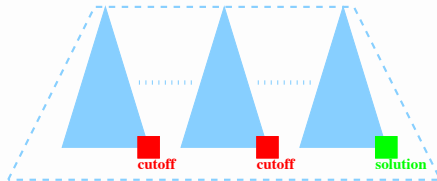
[GSK98]



- 10000 runs, branching randomization on industrial instance
 - Use **rapid randomized restarts** (**search restarts**)

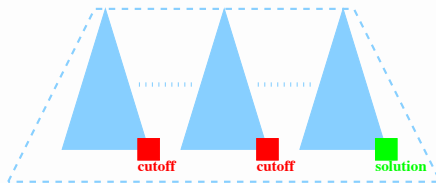
Search Restarts II

- Restart search after a number of conflicts



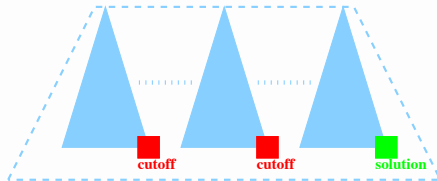
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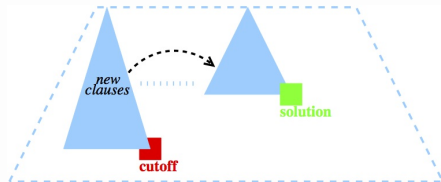
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- Learned clauses effective after restart(s)



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 - Why?

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 - Watched literals are one example of lazy data structures
 - ▶ But there are others

Watched Literals

[MMZZM01]

- Important states of a clause

literals0 = 4
literals1 = 0
size = 5



unit

literals0 = 4
literals1 = 1
size = 5



satisfied

literals0 = 5
literals1 = 0
size = 5

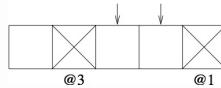
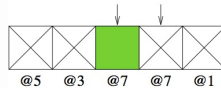
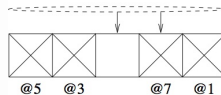
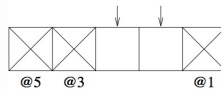
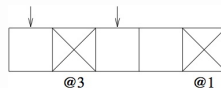


unsatisfied

Watched Literals

[MMZZM01]

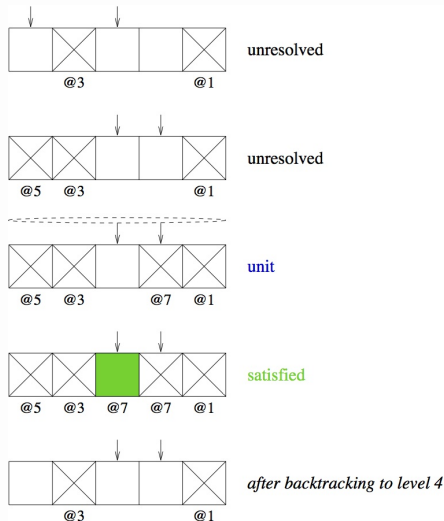
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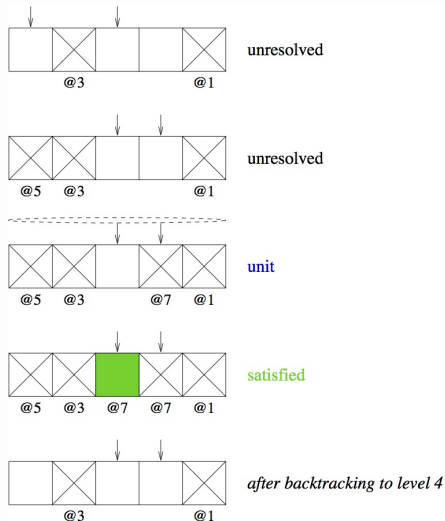
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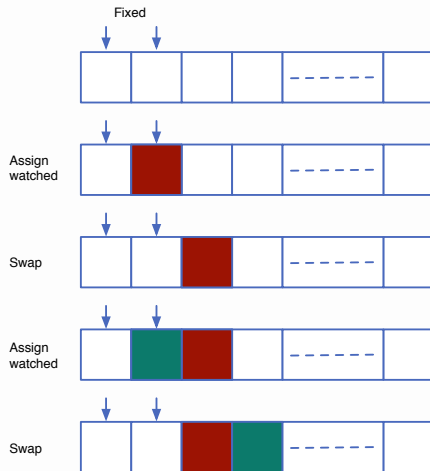
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Watched Literals, in Practice

[ES03,G13]

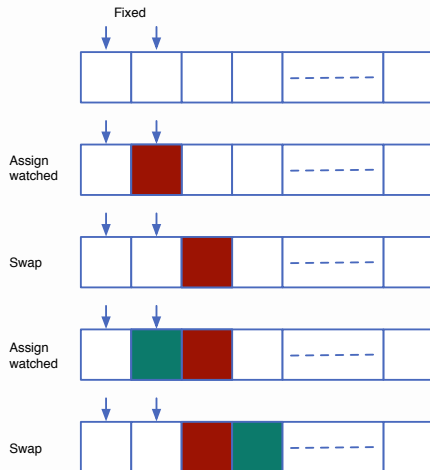
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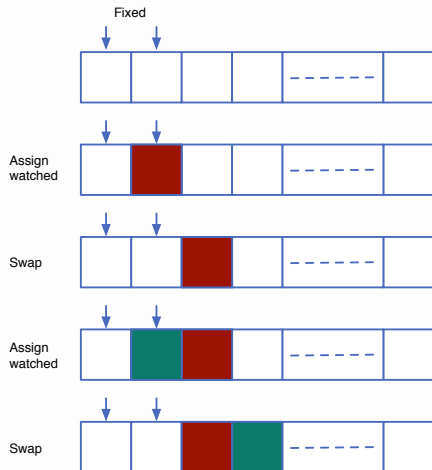
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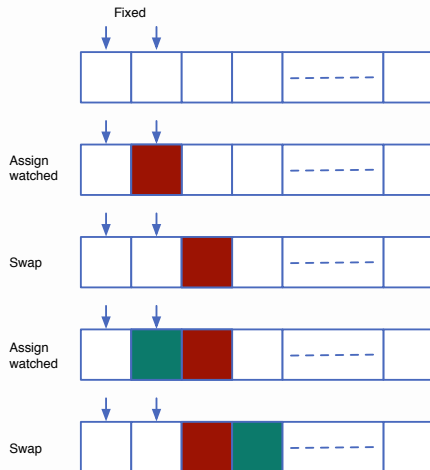
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[ES03,G13]

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- In practice, no gains observed from considering alternative implementations (see previous slide)



Additional Key Techniques

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[e.g. MMZZM01]

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- Proven recent techniques:

- Phase saving
- Luby restarts
- Literal blocks distance

[PD07]

[H07]

[AS09]

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CNF Encodings

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- Application-driven improvements

- Incremental SAT
 - ▶ Handling of assumptions due to MUS extractors
 - ▶ Changing SAT solvers to better suit applications

[LB13]

[AS13]

But Also, SAT-Based Problem Solving



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Encoding to CNF

- What to encode?
 - Boolean formulas
 - ▶ Tseitin's encoding
 - ▶ Plaisted&Greenbaum's encoding
 - ▶ ...
 - Cardinality constraints
 - Pseudo-Boolean (PB) constraints
 - Can also translate to SAT:
 - ▶ Constraint Satisfaction Problems (CSPs)
 - ▶ Answer Set Programming (ASP)
 - ▶ Model Finding
 - ▶ ...
- Key issues:
 - Encoding size
 - Arc-consistency?

Outline

Basic Definitions

DPLL Solvers

CDCL Solvers

What Next in CDCL Solvers?

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Pseudo-Boolean Constraints

Encoding CSPs

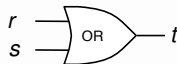
Representing Boolean Formulas / Circuits I

- Satisfiability problems can be defined on Boolean circuits/formulas
- Can represent circuits/formulas as CNF formulas [T68,PG86]
 - For each (simple) gate, CNF formula encodes the **consistent** assignments to the gate's inputs and output
 - ▶ Given $z = \text{OP}(x, y)$, represent in CNF $z \leftrightarrow \text{OP}(x, y)$
 - CNF formula for the circuit is the conjunction of CNF formula for each gate

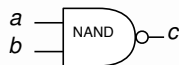
$$\mathcal{F}_c = (a \vee c) \wedge (b \vee c) \wedge (\bar{a} \vee \bar{b} \vee \bar{c})$$



$$\mathcal{F}_t = (\bar{r} \vee t) \wedge (\bar{s} \vee t) \wedge (r \vee s \vee \bar{t})$$



Representing Boolean Formulas / Circuits II



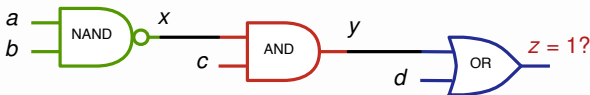
ab		00	01	11	10
c	0	0	0	1	0
	1	1	1	0	1

a	b	c	$\mathcal{F}_c(a,b,c)$
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	0

$$\mathcal{F}_c = (a \vee c) \wedge (b \vee c) \wedge (\bar{a} \vee \bar{b} \vee \bar{c})$$

Representing Boolean Formulas / Circuits III

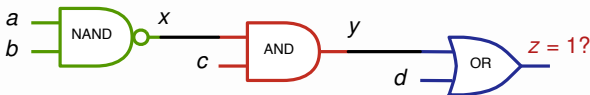
- CNF formula for the circuit is the conjunction of the CNF formula for each gate
 - Can specify objectives with additional clauses



$$\begin{aligned}\mathcal{F} = & (a \vee x) \wedge (b \vee x) \wedge (\bar{a} \vee \bar{b} \vee \bar{x}) \wedge \\ & (x \vee \bar{y}) \wedge (c \vee \bar{y}) \wedge (\bar{x} \vee \bar{c} \vee y) \wedge \\ & (\bar{y} \vee z) \wedge (\bar{d} \vee z) \wedge (y \vee d \vee \bar{z}) \wedge (z)\end{aligned}$$

Representing Boolean Formulas / Circuits III

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- Note: $z = d \vee (c \wedge (\neg(a \wedge b)))$
 - **No** distinction between Boolean circuits and formulas

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Cardinality Constraints

- How to handle cardinality constraints, $\sum_{j=1}^n x_j \leq k$?
 - How to handle AtMost1 constraints, $\sum_{j=1}^n x_j \leq 1$?
 - General form: $\sum_{j=1}^n x_j \bowtie k$, with $\bowtie \in \{<, \leq, =, \geq, >\}$
- Solution #1:
 - Use native PB solver, e.g. BSOLO, PBS, Galena, Pueblo, etc.
 - Difficult to keep up with advances in SAT technology
 - For SAT/UNSAT, best solvers already encode to CNF
 - ▶ E.g. Minisat+, WBO, etc.

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 - ▶ E.g. Minisat+, WBO, etc.
- Solution #2:
 - Encode cardinality constraints to CNF
 - Use SAT solver

Equals1, AtLeast1 & AtMost1 Constraints

- $\sum_{j=1}^n x_j = 1$: encode with $(\sum_{j=1}^n x_j \leq 1) \wedge (\sum_{j=1}^n x_j \geq 1)$
- $\sum_{j=1}^n x_j \geq 1$: encode with $(x_1 \vee x_2 \vee \dots \vee x_n)$
- $\sum_{j=1}^n x_j \leq 1$ encode with:
 - Pairwise encoding
 - ▶ Clauses: $\mathcal{O}(n^2)$; No auxiliary variables
 - Sequential counter [S05]
 - ▶ Clauses: $\mathcal{O}(n)$; Auxiliary variables: $\mathcal{O}(n)$
 - Bitwise encoding [P07,FP01]
 - ▶ Clauses: $\mathcal{O}(n \log n)$; Auxiliary variables: $\mathcal{O}(\log n)$
 - ...

Bitwise Encoding

- Encode $\sum_{j=1}^n x_j \leq 1$ with bitwise encoding:

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Bitwise Encoding

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 - Auxiliary variables v_0, \dots, v_{r-1} ; $r = \lceil \log n \rceil$ (with $n > 1$)
 - If $x_j = 1$, then $v_0 \dots v_{r-1} = b_0 \dots b_{r-1}$, the binary encoding of $j-1$
 $x_j \rightarrow (v_0 = b_0) \wedge \dots \wedge (v_{r-1} = b_{r-1}) \Leftrightarrow (\bar{x}_j \vee (v_0 = b_0) \wedge \dots \wedge (v_{r-1} = b_{r-1}))$

- An example: $x_1 + x_2 + x_3 \leq 1$

	$j-1$	$v_1 v_0$
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 - ▶ $l_i \equiv v_i$, if $b_i = 1$
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$$\begin{aligned} &(\bar{x}_1 \vee \bar{v}_1) \wedge (\bar{x}_1 \vee \bar{v}_0) \\ &(\bar{x}_2 \vee \bar{v}_1) \wedge (\bar{x}_2 \vee v_0) \\ &(\bar{x}_3 \vee v_1) \wedge (\bar{x}_3 \vee \bar{v}_0) \end{aligned}$$

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 - ▶ $l_i \equiv v_i$, if $b_i = 1$
 - ▶ $l_i \equiv \bar{v}_i$, otherwise
 - If $x_j = 1$, assignment to v_i variables **must** encode $j - 1$
 - ▶ All other x variables **must** take value 0
 - If all $x_j = 0$, **any** assignment to v_i variables is consistent
 - $\mathcal{O}(n \log n)$ clauses ; $\mathcal{O}(\log n)$ auxiliary variables
- An example: $x_1 + x_2 + x_3 \leq 1$

	$j - 1$	$v_1 v_0$	
x_1	0	00	$(\bar{x}_1 \vee \bar{v}_1) \wedge (\bar{x}_1 \vee \bar{v}_0)$
x_2	1	01	$(\bar{x}_2 \vee \bar{v}_1) \wedge (\bar{x}_2 \vee v_0)$
x_3	2	10	$(\bar{x}_3 \vee v_1) \wedge (\bar{x}_3 \vee \bar{v}_0)$

General Cardinality Constraints

- General form: $\sum_{j=1}^n x_j \leq k$ (or $\sum_{j=1}^n x_j \geq k$)
 - Sequential counters [S05]
 - ▶ Clauses/Variables: $\mathcal{O}(n k)$
 - BDDs [ES06]
 - ▶ Clauses/Variables: $\mathcal{O}(n k)$
 - Sorting networks [ES06]
 - ▶ Clauses/Variables: $\mathcal{O}(n \log^2 n)$
 - Cardinality Networks: [ANORC09,ANORC11a]
 - ▶ Clauses/Variables: $\mathcal{O}(n \log^2 k)$
 - Pairwise Cardinality Networks: [CZ110]
 - ...

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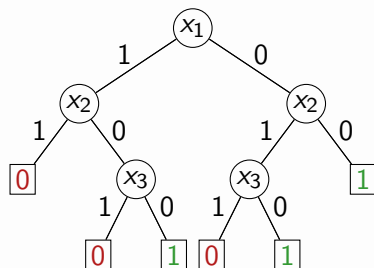
Encoding CSPs

Pseudo-Boolean Constraints

- General form: $\sum_{j=1}^n a_j x_j \leq b$
 - Operational encoding [W98]
 - ▶ Clauses/Variables: $\mathcal{O}(n)$
 - ▶ Does **not** guarantee arc-consistency
 - BDDs [ES06]
 - ▶ Worst-case exponential number of clauses
 - Polynomial watchdog encoding [BBR09]
 - ▶ Let $\nu(n) = \log(n) \log(a_{\max})$
 - ▶ Clauses: $\mathcal{O}(n^3 \nu(n))$; Aux variables: $\mathcal{O}(n^2 \nu(n))$
 - Improved polynomial watchdog encoding [ANORC11b]
 - ▶ Clauses & aux variables: $\mathcal{O}(n^3 \log(a_{\max}))$
 - ...

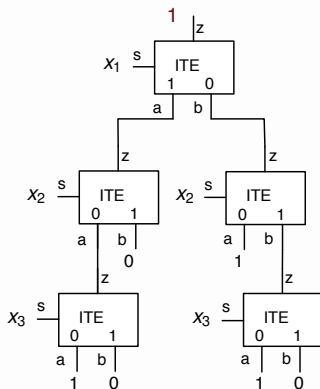
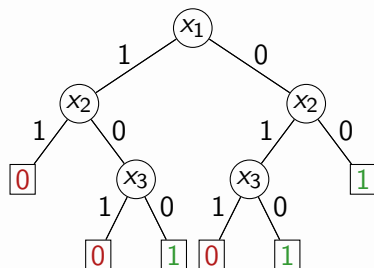
Encoding PB Constraints with BDDs I

- Encode $3x_1 + 3x_2 + x_3 \leq 3$
- Construct BDD
 - E.g. analyze variables by decreasing coefficients
- Extract ITE-based circuit from BDD



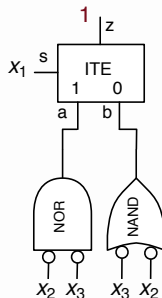
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Encoding PB Constraints with BDDs II

- Encode $3x_1 + 3x_2 + x_3 \leq 3$
- Extract ITE-based circuit from BDD
- Simplify and create final circuit:



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- Either constraint can still be satisfied, but **not** both

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CSP Constraints

- Many possible encodings:
 - Direct encoding [dK89,GJ96,W00]
 - Log encoding [W00]
 - Support encoding [K90,G02]
 - Log-Support encoding [G07]
 - Order encoding for finite linear CSPs [TTKB09]
 - ...

Direct Encoding for CSP w/ Binary Constraints

- Variable x_i with domain D_i , with $m_i = |D_i|$
- Represent values of x_i with Boolean variables $x_{i,1}, \dots, x_{i,m_i}$
- Require $\sum_{k=1}^{m_i} x_{i,k} = 1$
 - Suffices to require $\sum_{k=1}^{m_i} x_{i,k} \geq 1$
- If the pair of assignments $x_i = v_i \wedge x_j = v_j$ is not allowed, add binary clause $(\bar{x}_{i,v_i} \vee \bar{x}_{j,v_j})$

[W00]

Thanks!