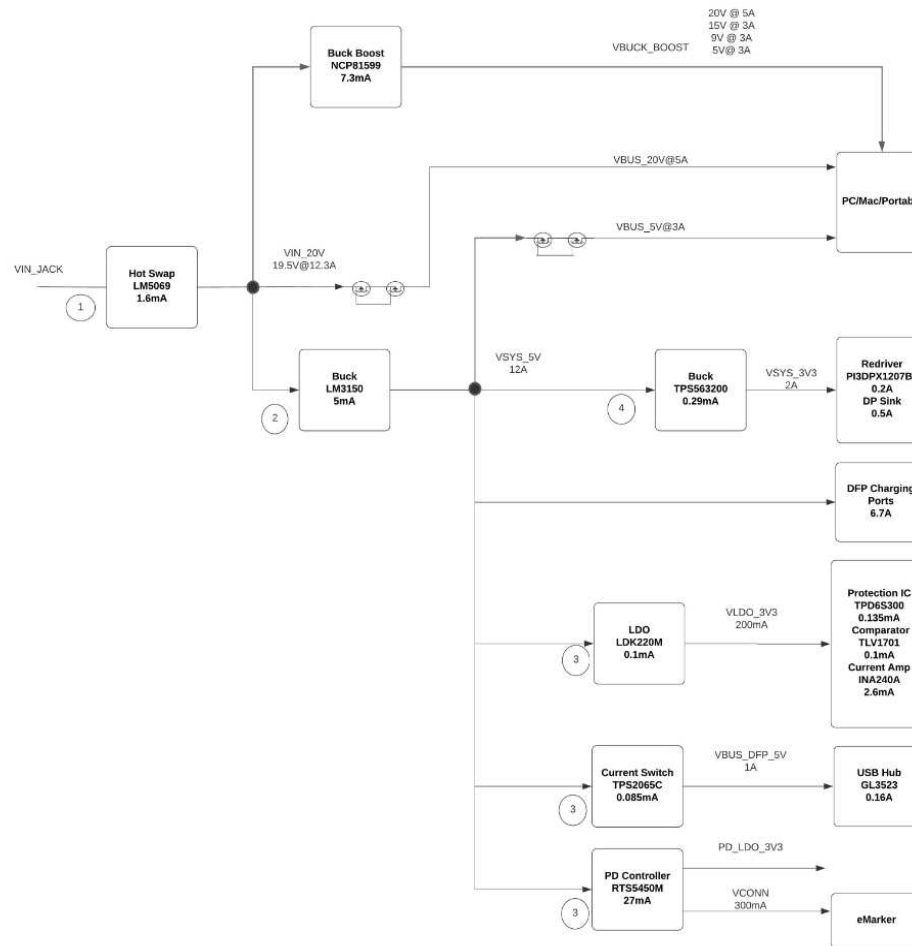
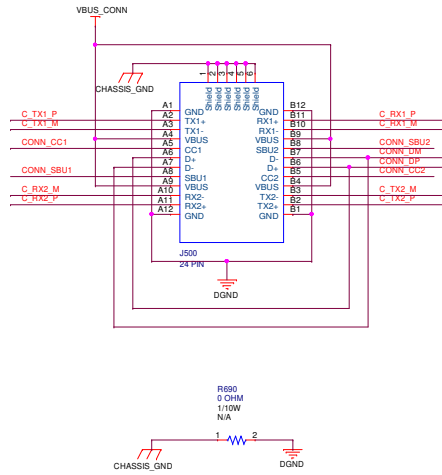


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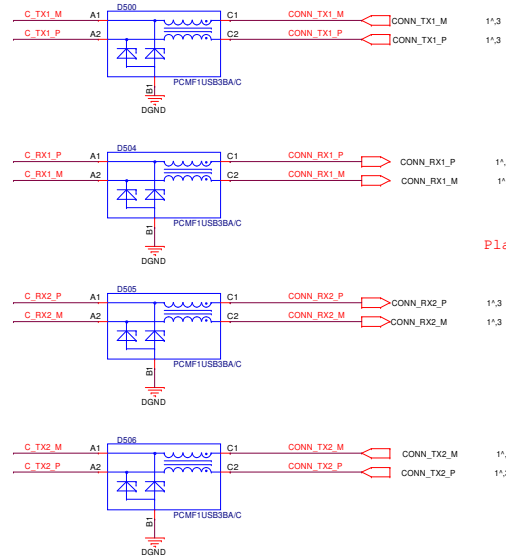


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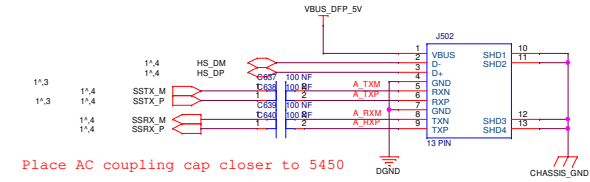
Type C Receptacle



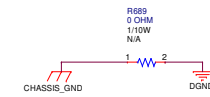
CL = 0.3pF
Rdyn = 0.29ohm
Rated 15kV
@5Ghz
Sdd21 = 1.5dB
Scc21 = -22dB
Xtalk = -50dB



Type A Receptacle

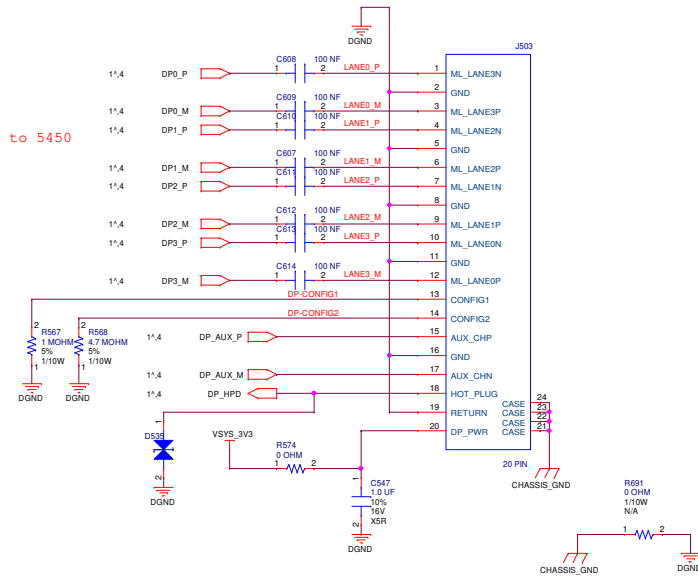


Place AC coupling cap closer to 5450

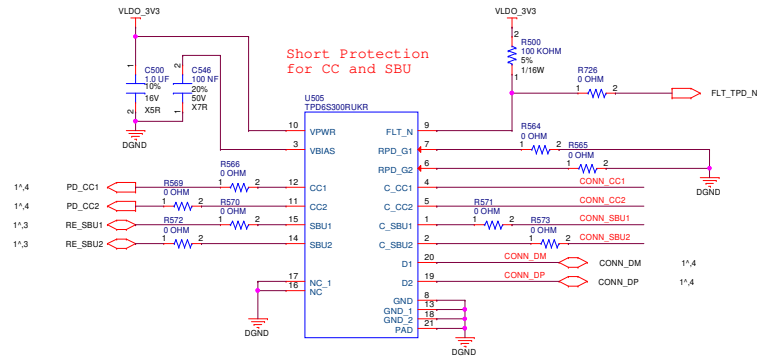


Displayport Receptacle

Place AC coupling cap closer to 5450



Power Source
Rd for DB is not required



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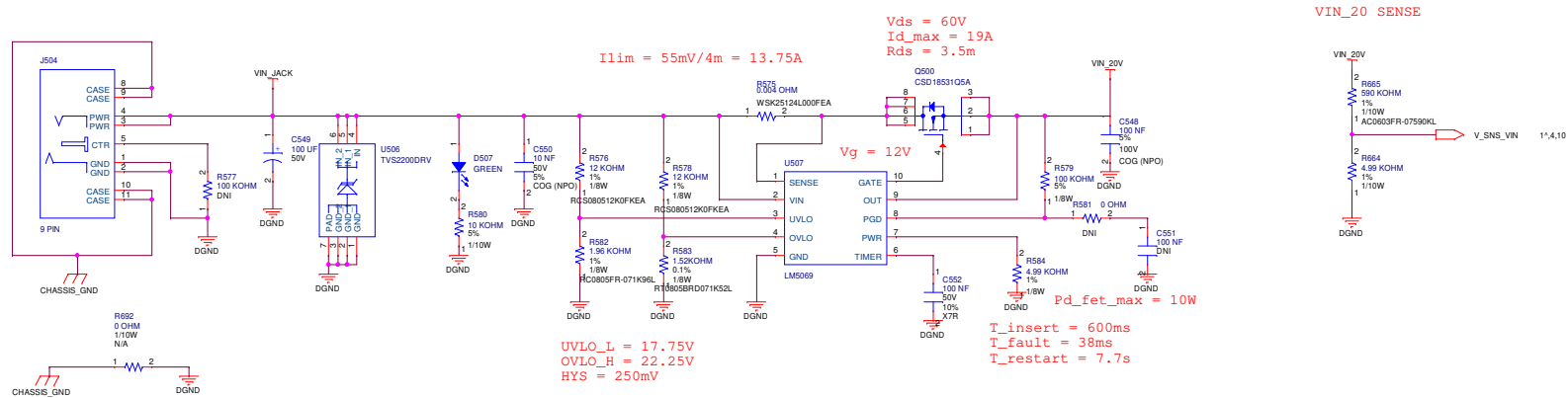
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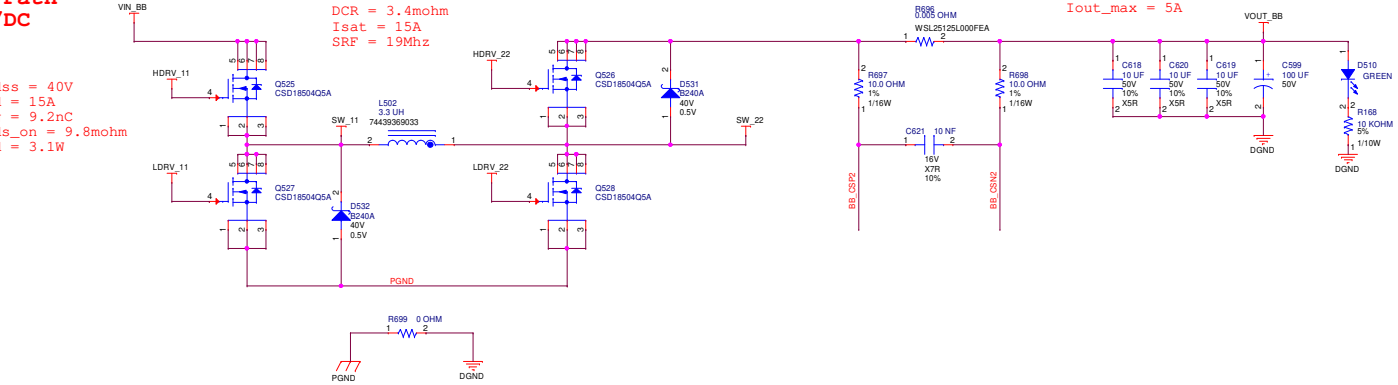
5V, 9V, 15V and 20V Power Path
V_{sns}, I_{sns}, V_{dis} by DC/DC

Fsw = 600kHz (I2C programmable)

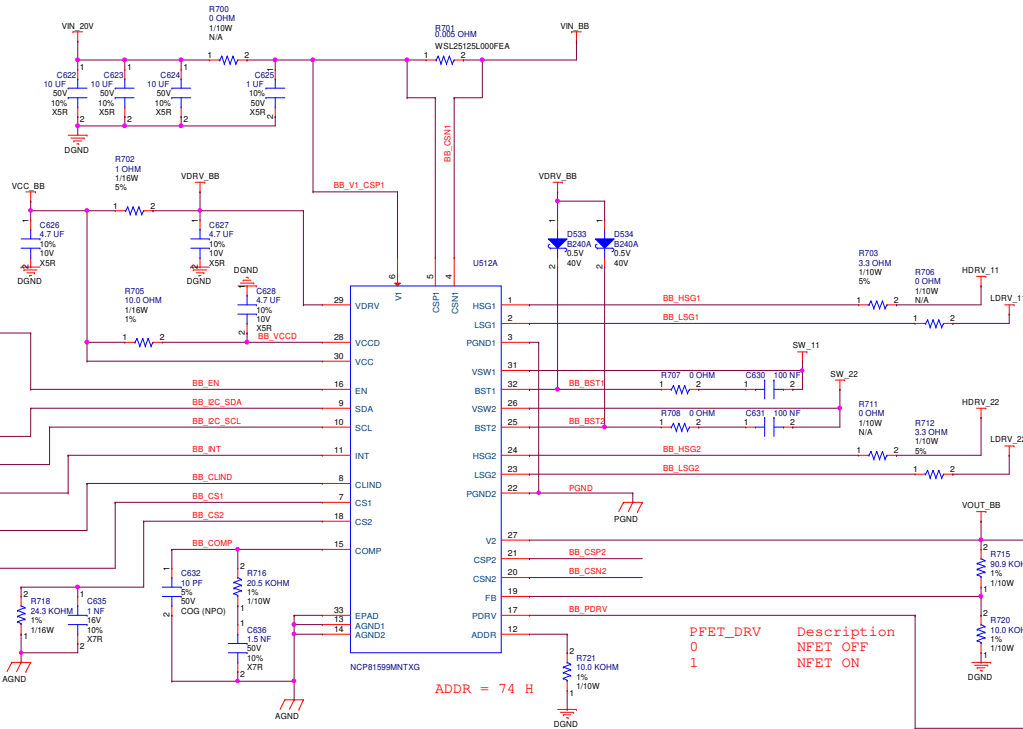
DCR = 3.4mohm
Isat = 15A
SRF = 19MHz

Vout_max = 20V
Iout_max = 5A

Vdss = 40V
Id = 15A
Qg = 9.2nC
Rds_on = 9.8mohm
Pd = 3.1W



VCC: Internal VRBG output
VCCD: Digital power supply
VDRV: Gate drive voltage
Range: 4.5 ~ 5.5V



Vds = -30V
Id_max = -20.8A
Rds = 7.8m
Pd = 3.5W

Active High INT

Vcs < 2.56V

PFET_DRV
0
1

Description
NFET OFF
NFET ON

ADDR = 74 H

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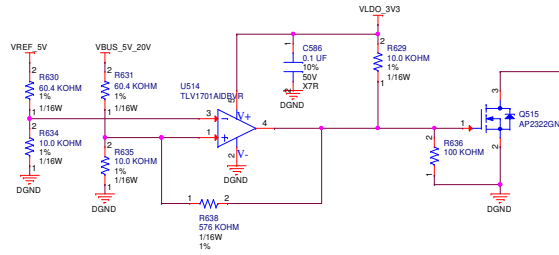
5V and 20V Power Path

V_sns, I_sns, V_dis by PD

PDO 1:
5V VBUS from VSYS

Vds = -30V
Id_max = -20.8A
Rds = 7.8m
Pd = 3.5W

5V & VBUS Comparator

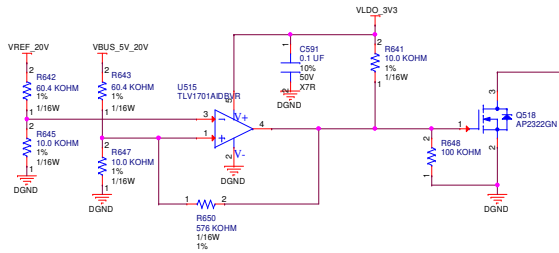


Comparator as RCE

PDO 2:
VBUS connects to DC IN

Vds = -30V
Id_max = -20.8A
Rds = 7.8m
Pd = 3.5W

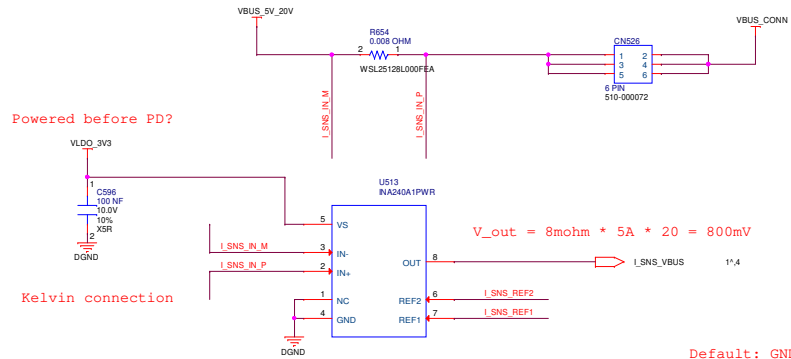
20V & VBUS Comparator



PD to enable PFET on 20V

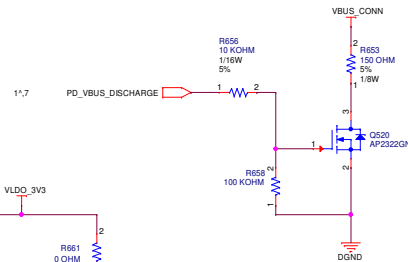
VBUS_20V_E

VBUS current sensing

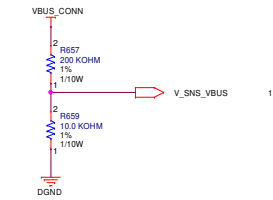


Default: GND Ref
($V_{out} = 0$ when 0 input)

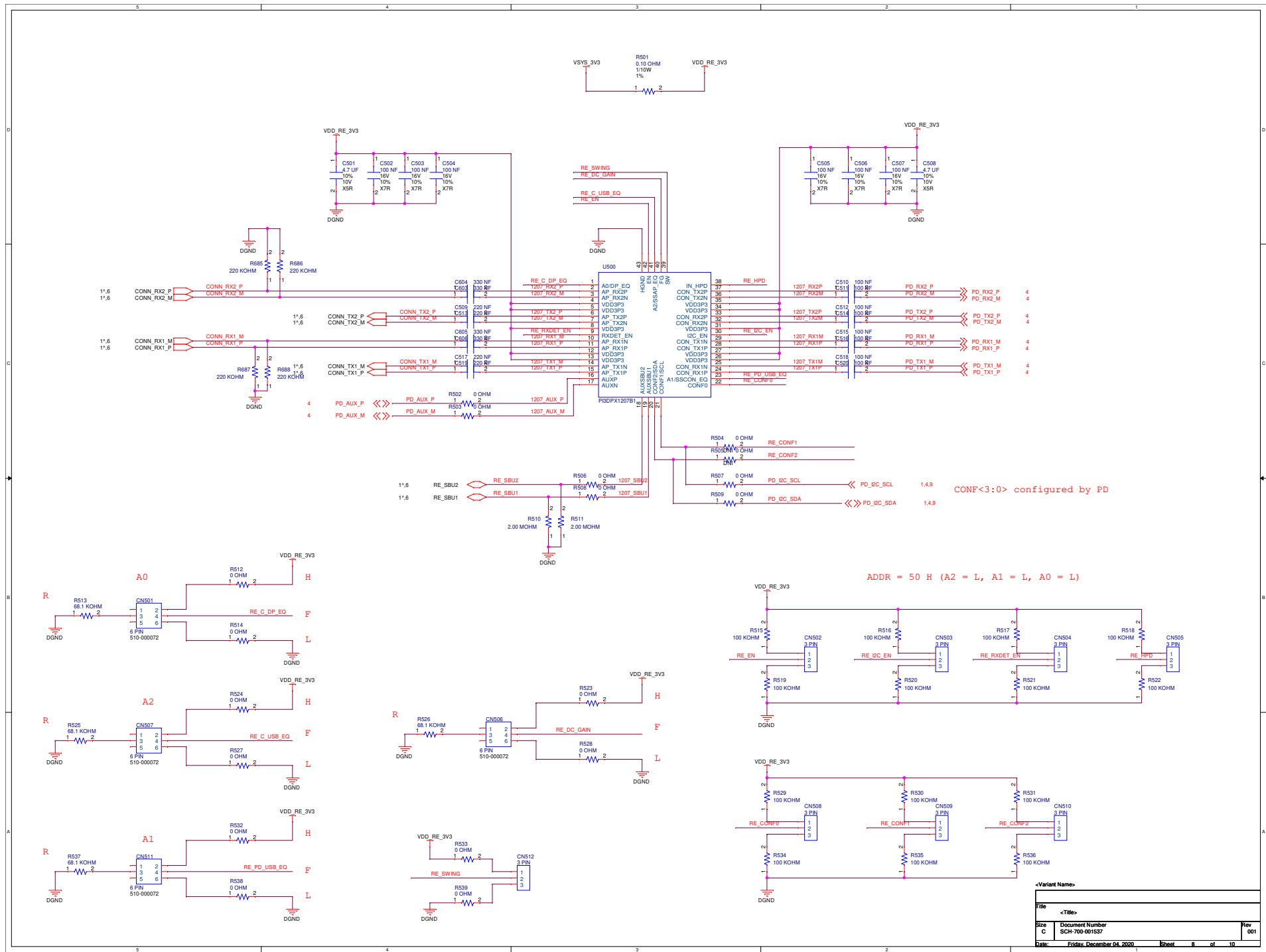
VBUS DISCHARGE

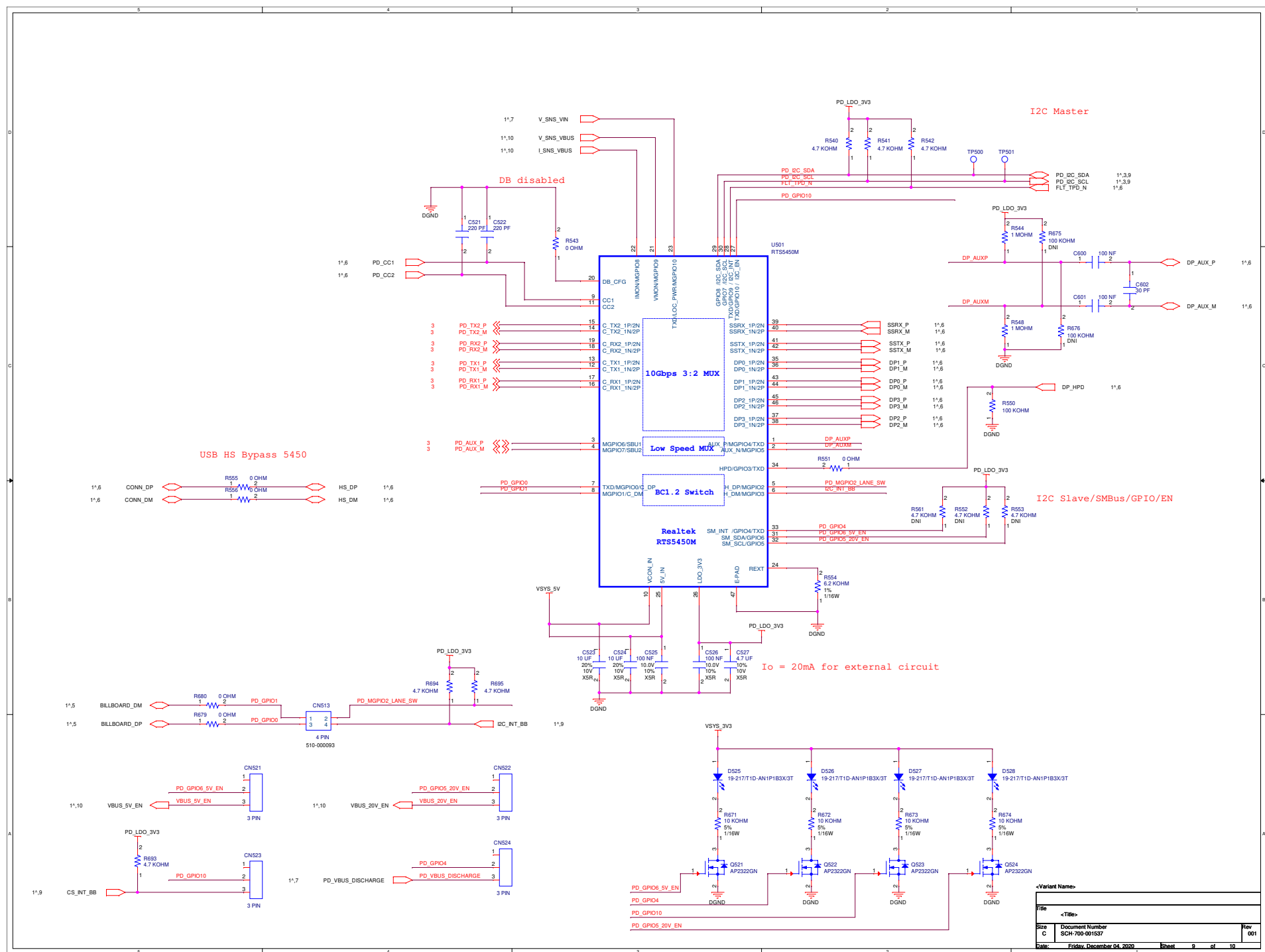


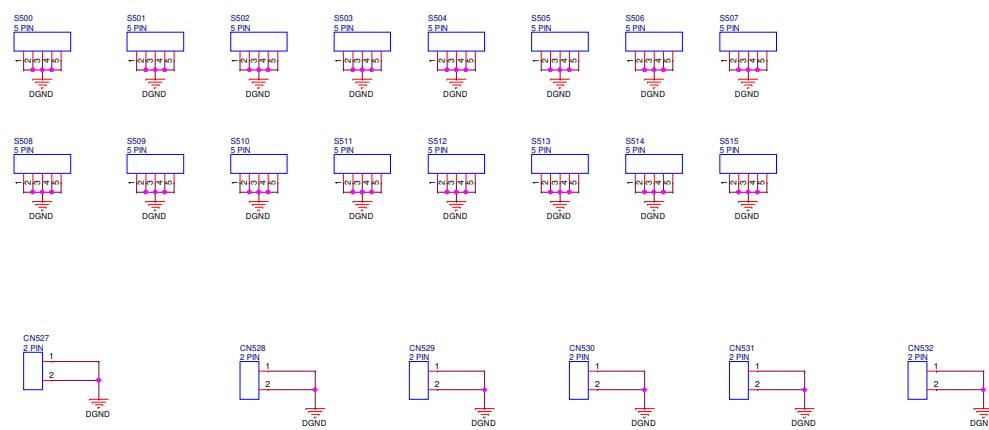
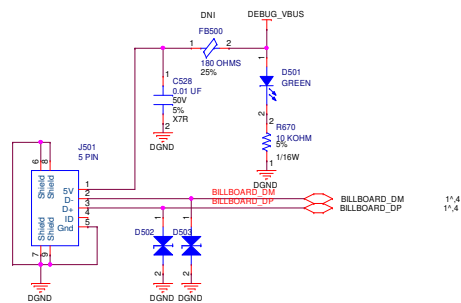
VBUS SENSE



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