

VE320 Intro to Semiconductor Devices

Chapter 12. The Bipolar Transistor (BJT)

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1 Objectives

2 The BJT Working Principle

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Objective

1. Learn the working principle for BJT
 - 1.1 BJT structure
 - 1.2 Basic principle of operation
 - 1.3 Different operation modes
 - 1.4 Minority carrier distribution
2. Learn the non-ideal effect: early effect
3. Practice how to apply the concepts and formulas in solving problems

1 Objectives

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BJT Structure

1. BJT has three doped regions and two pn junction
2. Three terminal connections: emitter, base, and collector

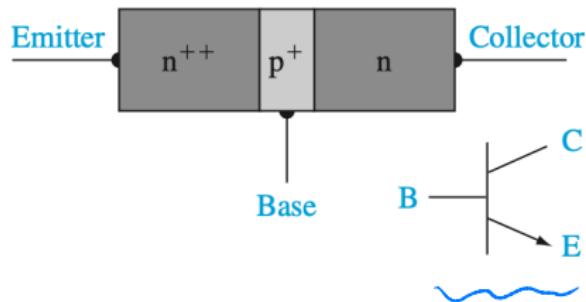


Figure: npn BJT

circuit symbol

BJT Structure

3. The (++) and (+) notation indicates the relative magnitudes of the impurity doping concentrations
4. The width of the base region is small compared to the minority carrier diffusion length

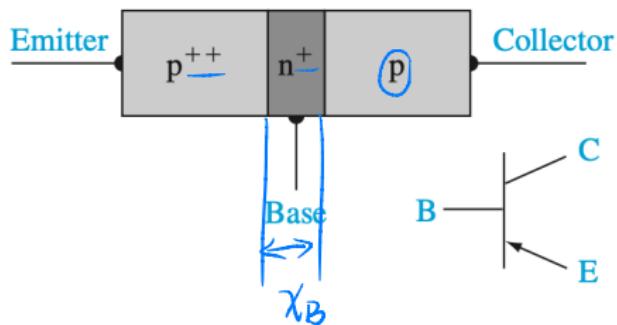
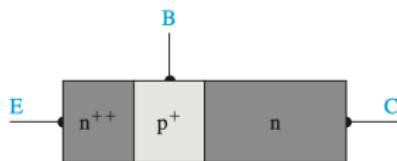


Figure: pnp BJT

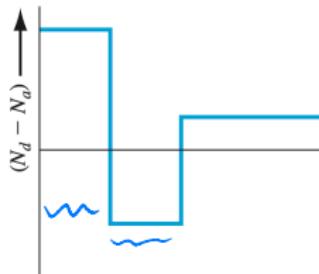
Basic Principle of Operation

1. Typical impurity doping concentrations in the emitter, base, and collector may be on the order of 10^{19} cm^{-3} , 10^{17} cm^{-3} , and 10^{15} cm^{-3}

++ and +



$N_d - N_a$



emitter: $N_d = 10^{19} \text{ cm}^{-3}$ $N_a = 0$

Figure: Idealized doping profile of a npn BJT

base : $N_d = 0$ $N_a = 10^{15} \text{ cm}^{-3}$

Basic Principle of Operation

2. In the normal bias configuration: the B-E pn junction is forward biased and the B-C pn junction is reversed biased
(forward-active operation mode)

- **Brainstorm:** Can you explain how electrons move in this mode?

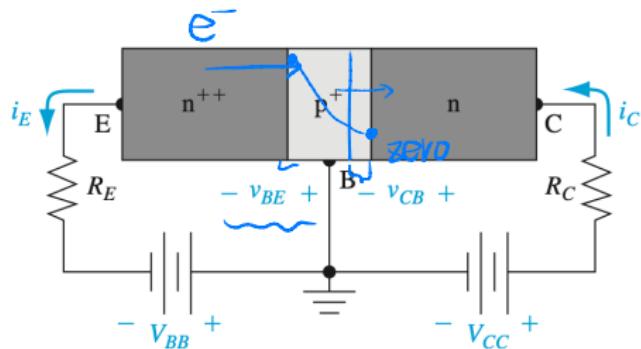


Figure: Biasing of an npn BJT in the forward-active mode

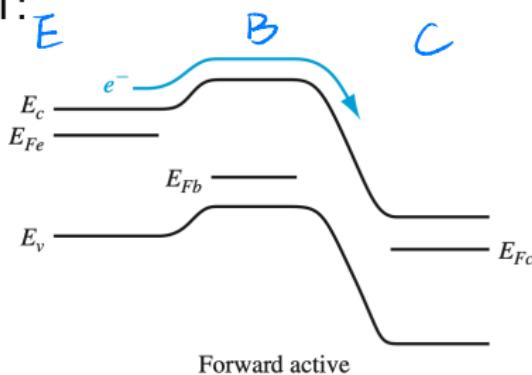
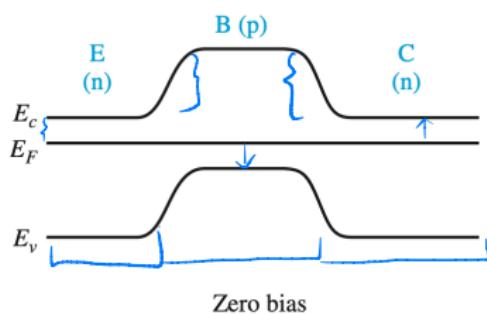
Basic Principle of Operation

B-E junction forward biased

voltage barrier ↓

B-C reversed
biased

3. Energy-band diagram of npn BJT:



① E_F is flat

② electrons in the emitter or in the collector
can not enter the base region

Basic Principle of Operation

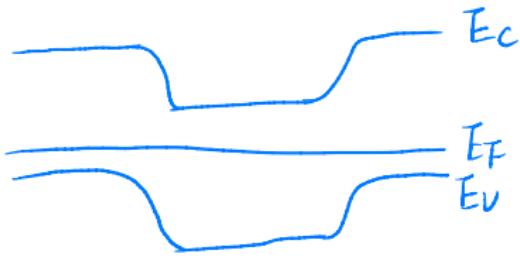
pnp. 

B-E forward biased

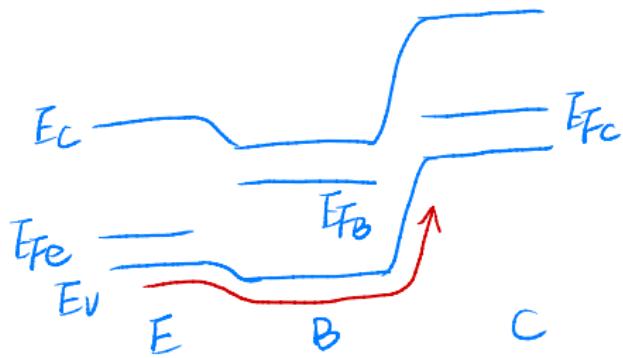
B-C reverse biased

4. Quiz: Energy-band diagram of pnp BJT:

① zero bias



② forward-active

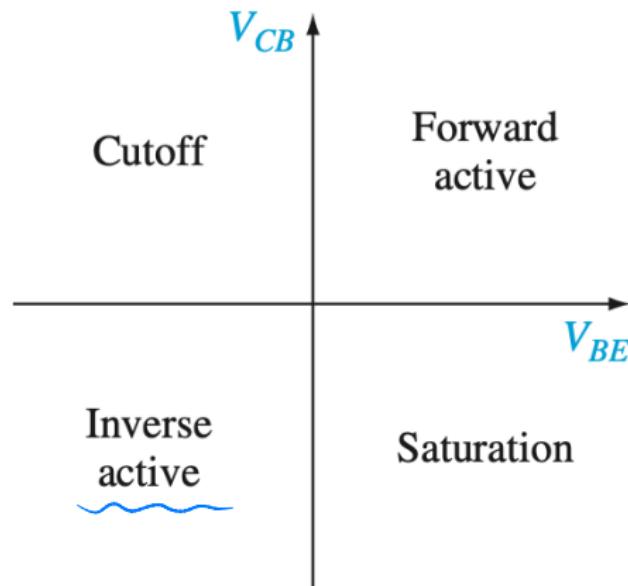


Operation Mode



1. Cutoff: $V_{BE} \leq 0$, so $i_E = i_C = 0$
2. Forward-active: $V_{BE} > 0$, $V_{CB} > 0$, so $i_C = \beta i_B$
3. Saturation: $V_{BE} > 0$, $V_{CB} \leq 0$, both pn junction are forward biased, i_C doesn't depend on V_{BE}

$$V_{BE}=0 \quad V_{BE}<0$$



possible

B-E reversed

B-C forward

Current-Voltage Characteristic

We need to use Kirchoff's voltage law.

$$V_{CE} = V_{CC} - I_C R_C$$

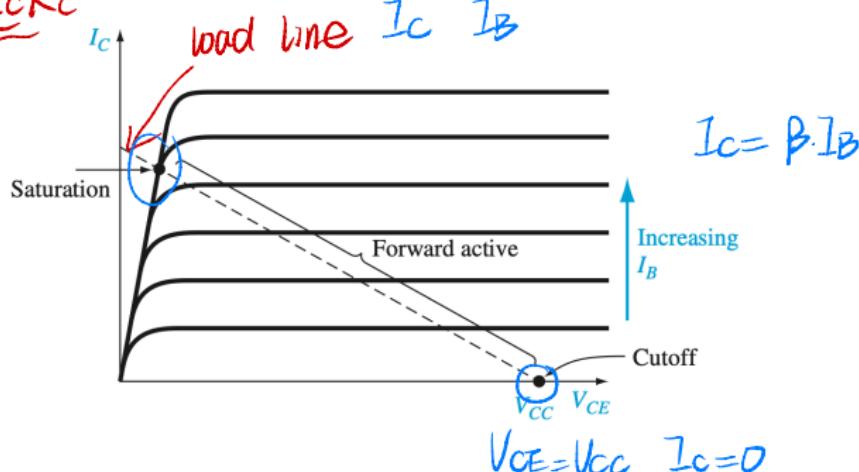


Figure: BJT common-emitter current–voltage characteristics

Minority Carrier Distribution

i_{E1} : electrons

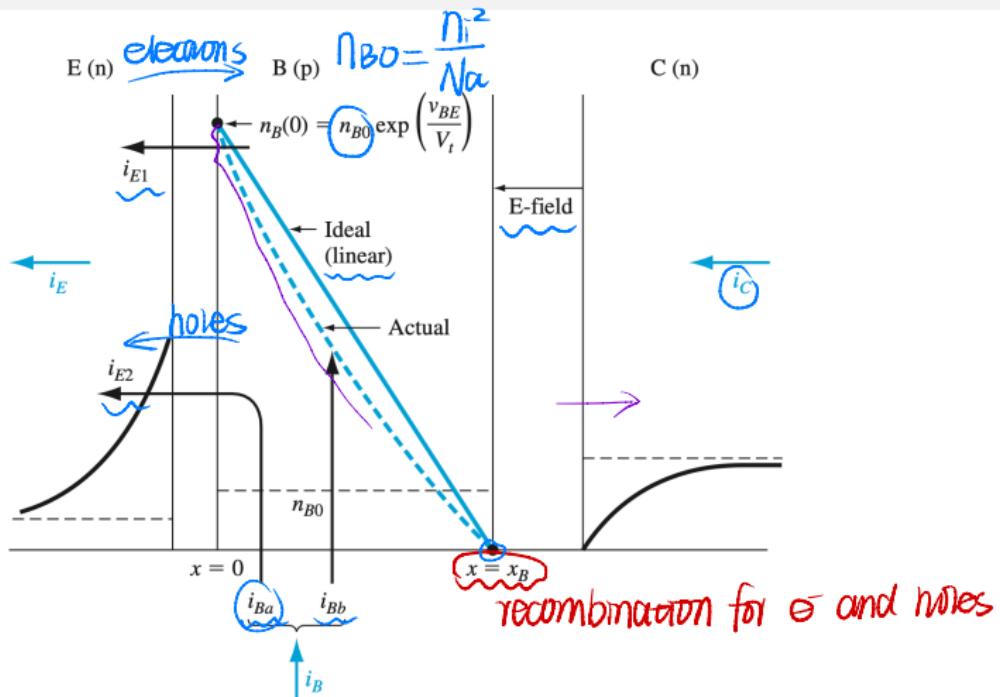


Figure: Minority carrier distributions and basic currents in a forward-biased npn BJT

Minority Carrier Distribution

1. Parameters:

$$\begin{cases} \alpha : \text{common-base current gain } (\alpha \sim 1) \\ \beta : \text{common-emitter current gain } (\beta \gg 1) \end{cases}$$

$\frac{i_C}{i_E} = \alpha$
 $\frac{i_C}{i_B} = \beta$

2. Collector current: i_C is controlled by v_{BE}

★

$$|J_C| = \underbrace{\frac{eD_B n_{B0}}{x_B} \exp\left(\frac{v_{BE}}{V_t}\right)}$$

$$i_C = \underbrace{\frac{-eD_n A_{BE}}{x_B} n_{B0}} \exp\left(\frac{v_{BE}}{V_t}\right) = I_s \exp\left(\frac{v_{BE}}{V_t}\right)$$

3. Emitter current: $\frac{i_C}{i_E} = \alpha$. $i_E = I_{SE} \exp\left(\frac{v_{BE}}{V_t}\right)$ $i_E = i_{E1} + i_{E2}$

4. Base current: $\frac{i_C}{i_B} = \beta$. $i_B \propto \exp\left(\frac{v_{BE}}{V_t}\right)$ $i_B = i_{Ba} + i_{Bb}$

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Early Effect

- Previous assumption: base width x_B is constant
- In reality: x_B changes with V_{CB} . The width of the space charge region extending into the base region varies with B-C voltage, i.e.,

$$V_{CB} \uparrow \implies \text{BC space charge region width} \uparrow \implies \underline{x_B} \downarrow$$

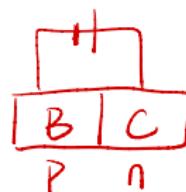
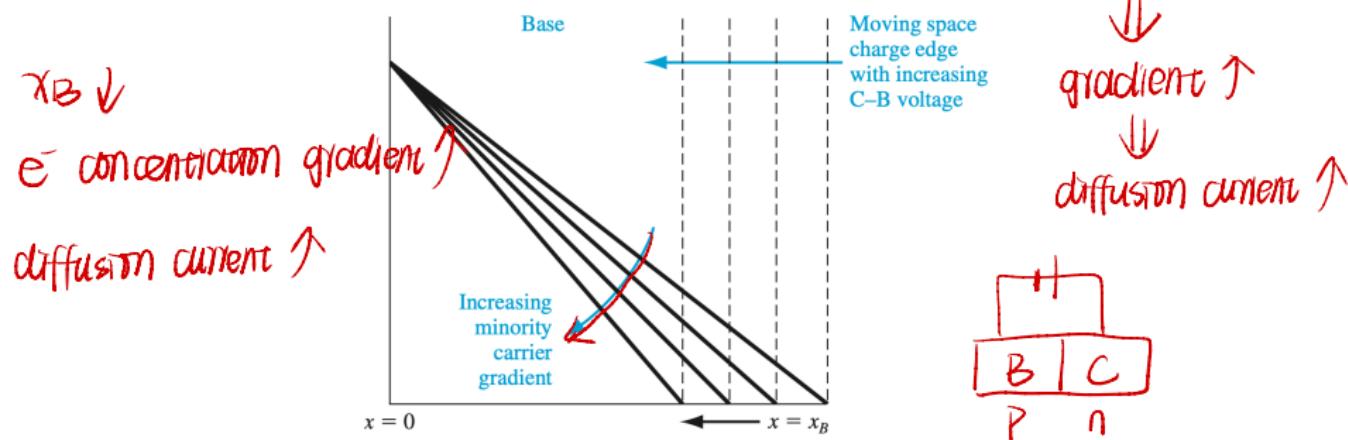


Figure: Change of x_B and minority carrier gradient as the B-C space charge width changes

Early Effect

1. Current-voltage characteristic: I_C changes with V_{CE}/V_{CB}
2. g_o : output conductance, r_o : output resistance:

$$\left\{ \begin{array}{l} \frac{dI_C}{dV_{CE}} \equiv g_o \stackrel{\approx 0}{=} \frac{I_C}{V_{CE} + V_A} = \frac{1}{r_o} \\ I_C = g_o(V_{CE} + V_A) = \frac{1}{r_o}(V_{CE} + V_A) \end{array} \right.$$

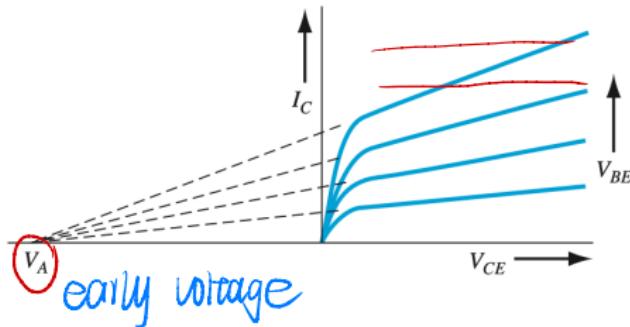
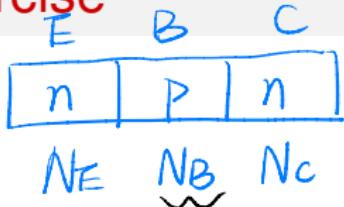


Figure: The collector current versus C-E voltage showing the Early effect and Early voltage.

Exercise



Early Effect

A uniformly doped silicon n-p-n bipolar transistor at $T=300\text{ K}$ has parameters $N_E = 2 \times 10^{18}\text{ cm}^{-3}$, $N_B = 2 \times 10^{16}\text{ cm}^{-3}$, $N_C = 2 \times 10^{15}\text{ cm}^{-3}$, $x_{B0} = 0.85\mu\text{m}$, and $D_B = 25\text{ m}^2/\text{s}$. Assume $x_{B0} \ll L_B$ and let $V_{BE} = 0.650\text{ V}$.

(a) Determine the electron diffusion current density for $V_{CB} = 4\text{ V}$ and $V_{CE} = 12\text{ V}$.

(b) Estimate the Early voltage. V_A

$$a) |J_C| = \frac{e D_B N_{B0}}{x_B} \exp\left(\frac{V_{BE}}{V_t}\right)$$

$$b) \frac{dI_C}{dV_{CE}} = \frac{I_C}{V_{CE} + V_A}$$

Answer

- (a) $J_C = 52.16 A/cm^2$ for $V_{CB} = 4 V$
 $J_C = 61.85 A/cm^2$ for $V_{CB} = 12 V$
- (b) $V_A = 38.4 A$

Answer

a) $|J_C| = \frac{e D_B N_{B0}}{(X_B)} \exp\left(\frac{V_{BE}}{V_t}\right)$

B-C reverse biased.

$$X_{dB} = \left\{ \frac{2 \epsilon s (V_{bi} + V_{CB})}{e} \left[\frac{N_c}{N_B} \frac{1}{(N_B + N_c)} \right]^{1/2} \right. \\ \left. = \left\{ \frac{2 \times 11.7 \times 8.85 \times 10^{-14}}{1.6 \times 10^{-19}} (V_{bi} + 4) \left[\frac{2 \times 10^{15}}{2 \times 10^{16}} \frac{1}{(2 \times 10^{16} + 2 \times 10^{15})} \right]^{1/2} \right\} \right.$$

$$V_{bi} = V_t \ln \left(\frac{N_B N_c}{n_i^2} \right) = 0.6709 \text{ V}$$

$$X_{dB} = 0.1658 \mu\text{m}$$

b) Early effect: $X_B \downarrow$

$$X_B = X_{B0} - X_{dB} = 0.6842 \mu\text{m}$$

Answer

c) J_c

$$n_{BO} = \frac{n_i^2}{N_B} = 1.25 \times 10^4 \text{ cm}^{-3}$$

$$J_c = \frac{n_{BO}}{x_B} = 52.16 \text{ A/cm}^2$$

$$V_{CB} = 12 \text{ V}, J_{c(12)} = 61.85 \text{ A/cm}^2$$

$$\Rightarrow \frac{dI_c}{dV_{CE}} = \frac{J_c}{V_{CE} + V_A} \quad \frac{\Delta I_c}{\Delta V_{CE}} = \frac{J_c}{V_{CB} + V_{BE} + V_A}$$

$$\frac{61.85 - 52.16}{12 - 4} = \frac{52.16}{4 + 0.65 + V_A} \Rightarrow V_A = 38.4 \text{ V}$$

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Exercise

$\phi_m \phi_s$

Homework 7.5 (9.30 in textbook)

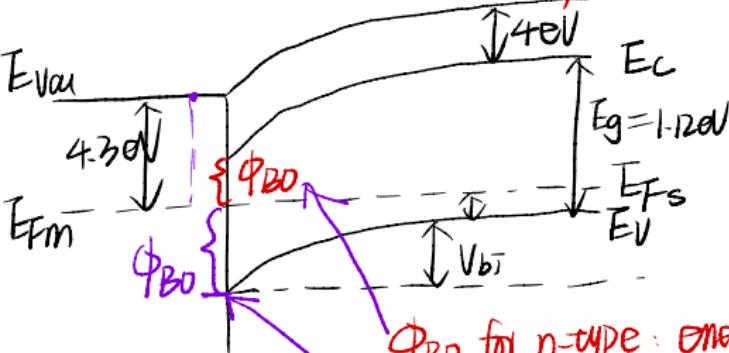
- 9.30** A metal–semiconductor junction is formed between a metal with a work function of 4.3 eV and p-type silicon with an electron affinity of 4.0 eV. The acceptor doping concentration in the silicon is $N_a = 5 \times 10^{16} \text{ cm}^{-3}$. Assume $T = 300 \text{ K}$. (a) Sketch the thermal equilibrium energy-band diagram. (b) Determine the height of the Schottky barrier. (c) Sketch the energy-band diagram with an applied reverse-biased voltage of $V_R = 3 \text{ V}$. (d) Sketch the energy-band diagram with an applied forward-bias voltage of $V_a = 0.25 \text{ V}$.

ϕ_{BO}

Answer

① Determine it is a schockley diode

$$E_F - E_V = -kT \ln \frac{N_d}{N_V}$$
$$= 0.18 \text{ eV}$$



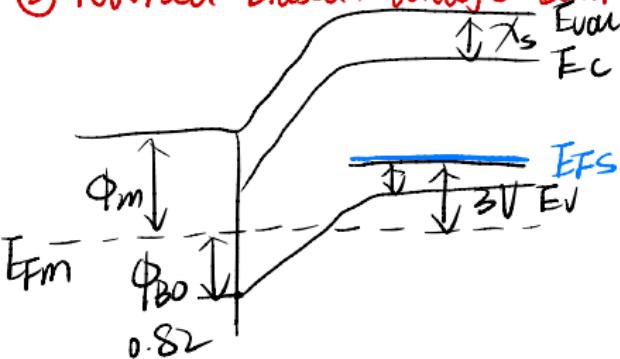
ϕ_{B0} for n-type: energy difference between E_F and E_C

② schockley barrier ϕ_{B0} for p-type: energy difference between E_F and E_V

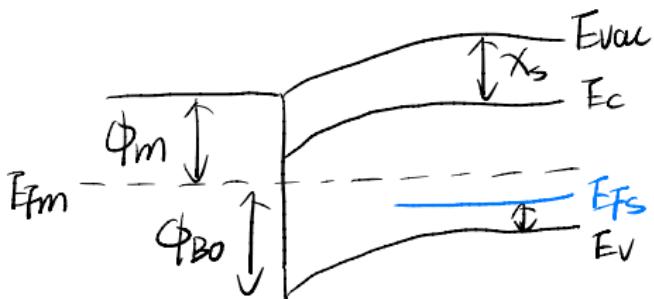
$$\phi_{B0} = \chi_s + E_g - \phi_m = 0.82 \text{ V}$$

Answer

③ reversed biased: voltage barrier ↑. ϕ_{BO} remains the same



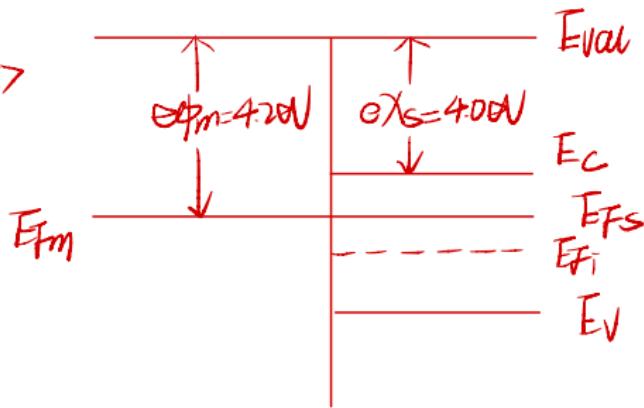
④ forward biased: voltage barrier ↓. ϕ_{BO} —



Answer

Homework 74

a>



Please notice in the question, it says no space region exists

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Conclusion

- **Minute paper:** Please take a piece of paper and list all the concepts we talk about today as much as possible.