





```

module MUX(
    input sel,I1,I0,
    output reg s
);
    always@(*)begin
        if (sel == 0) s <= I0;
        else s <= I1;
    end
endmodule

module TestBench;
    reg sel, I1, I0;
    wire s;
    MUX mux_sample(sel,I1,I0,s);

    initial begin
        #0 sel = 0; I0 = 0; I1 = 1;
        #10 sel = 1;
        #10 I1 = 0;
        #10 sel = 0;
        #10 I0 = 1;
    end
    initial #50 $stop;
endmodule

```

Name	Value	0.000 ns	10.000 ns	20.000 ns	30.000 ns	40.000 ns
 sel	0					
 I1	0					
 I0	1					
 s	1					

Here I just copy and paste the code in VS code and you can have a screenshot of your code and attach it in your submission. No matter how you attach your code, please make sure that source code (including the testbench) and the simulation result are both included in your submission.