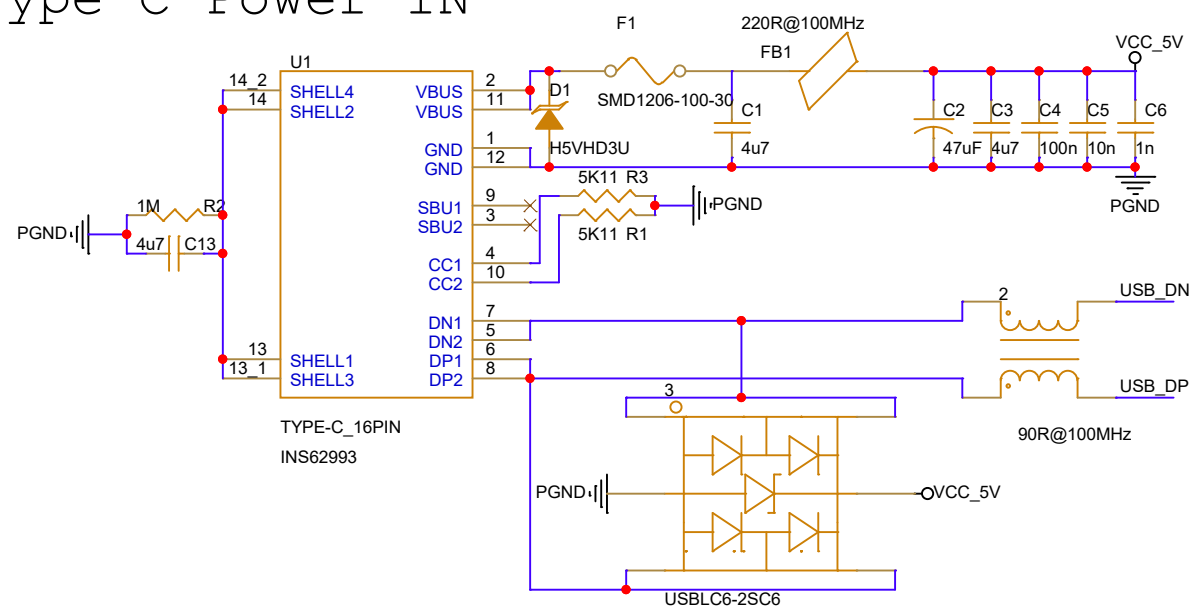
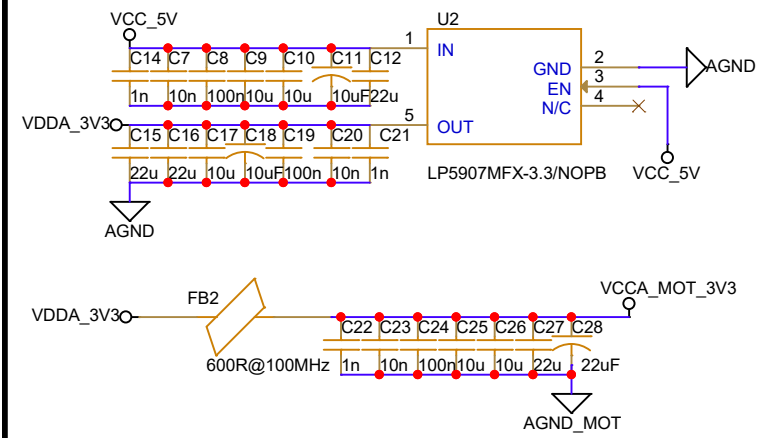


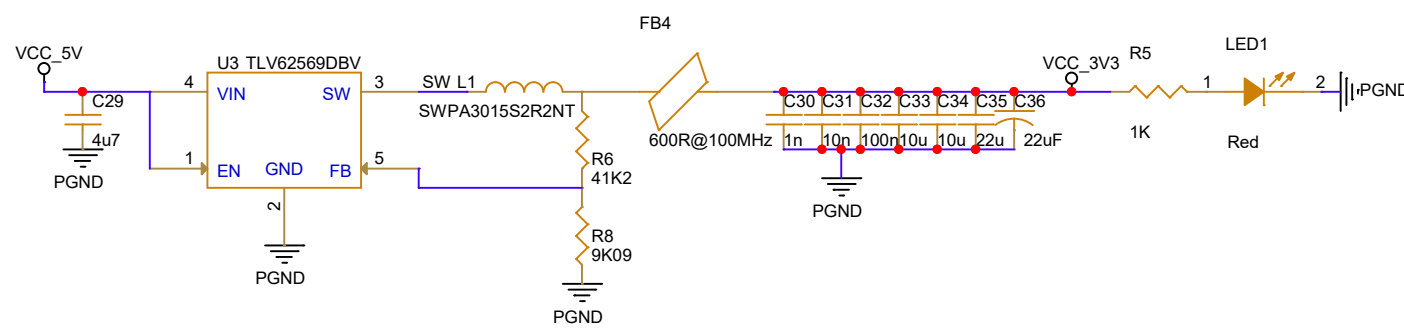
Type-C Power IN



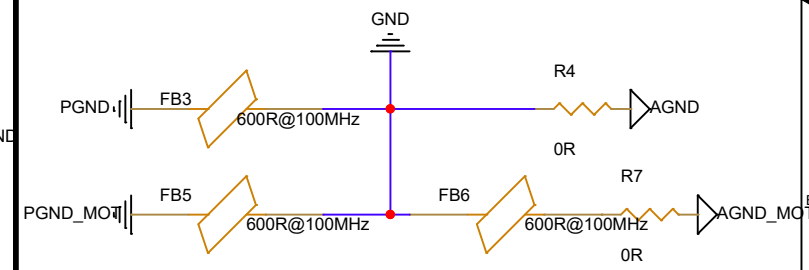
LDO 5V IN to 3V3 AC



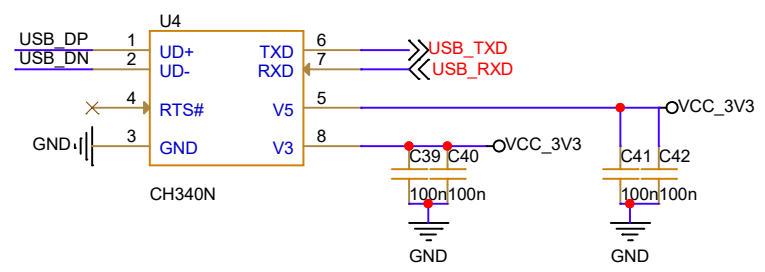
DCDC 5V IN to 3V3 DC



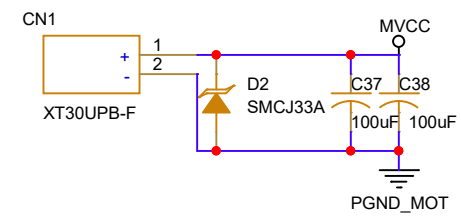
PGND to GND to AGND



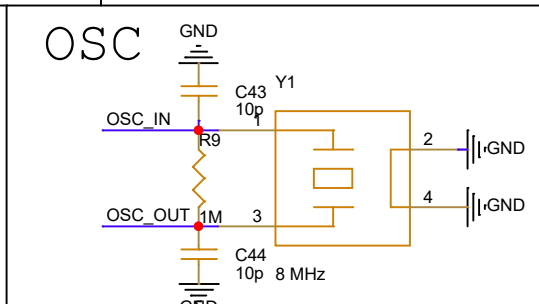
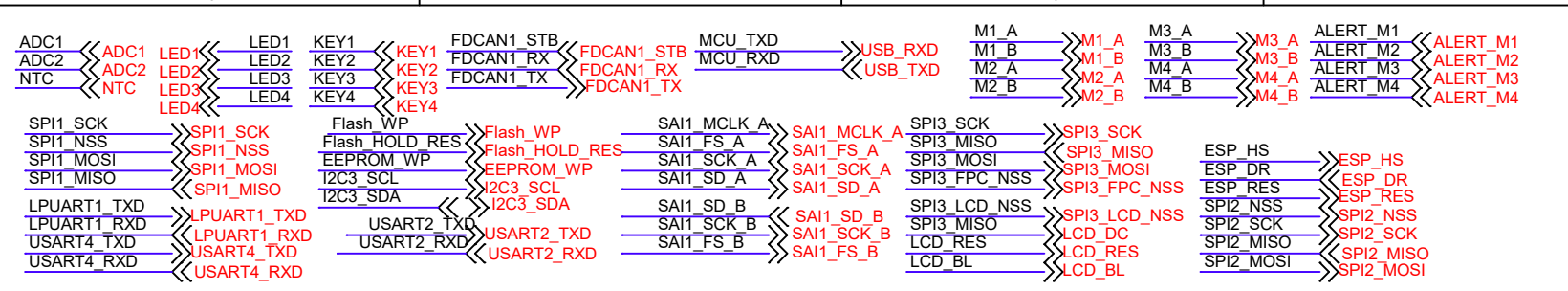
CH340N USB to UART



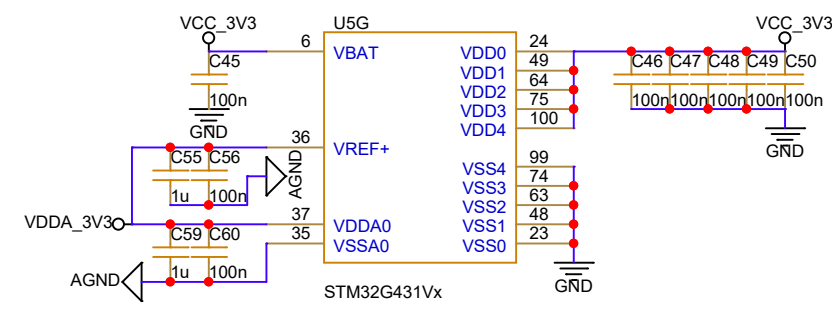
Motor Power IN



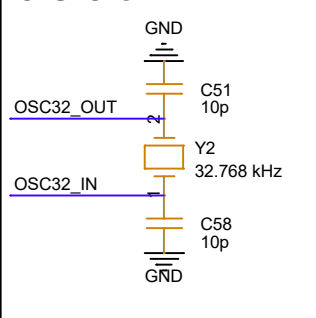
Title		
1.USB_Power_UART		
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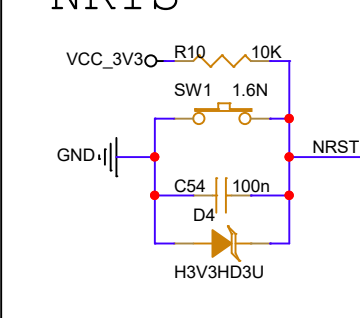
MCU Power



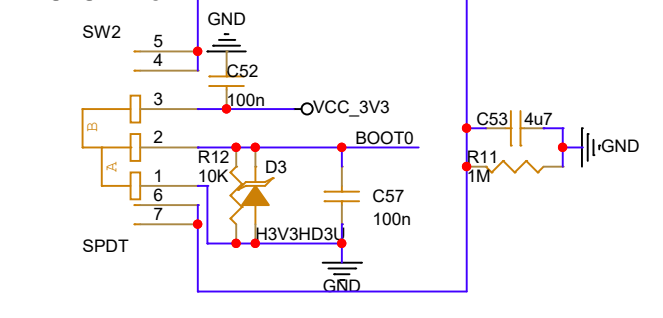
OSC32



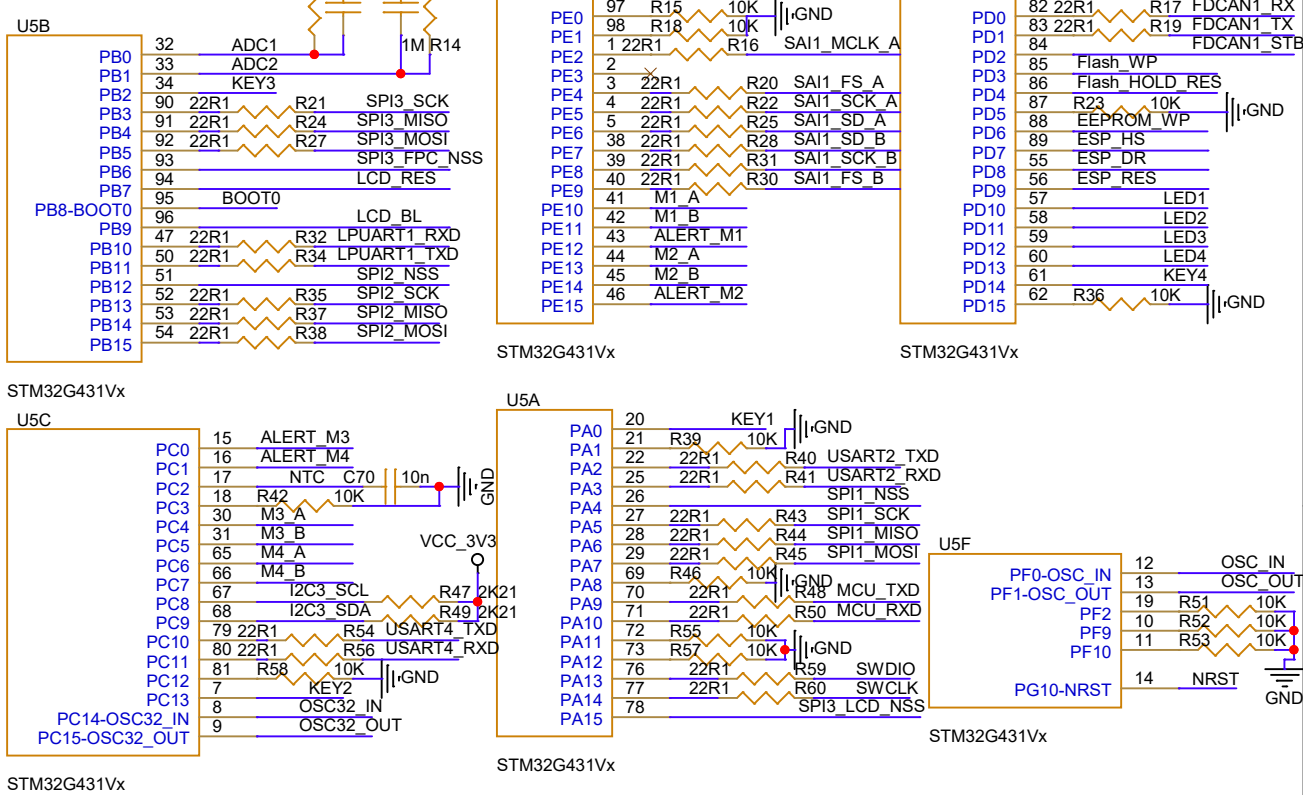
NRTS



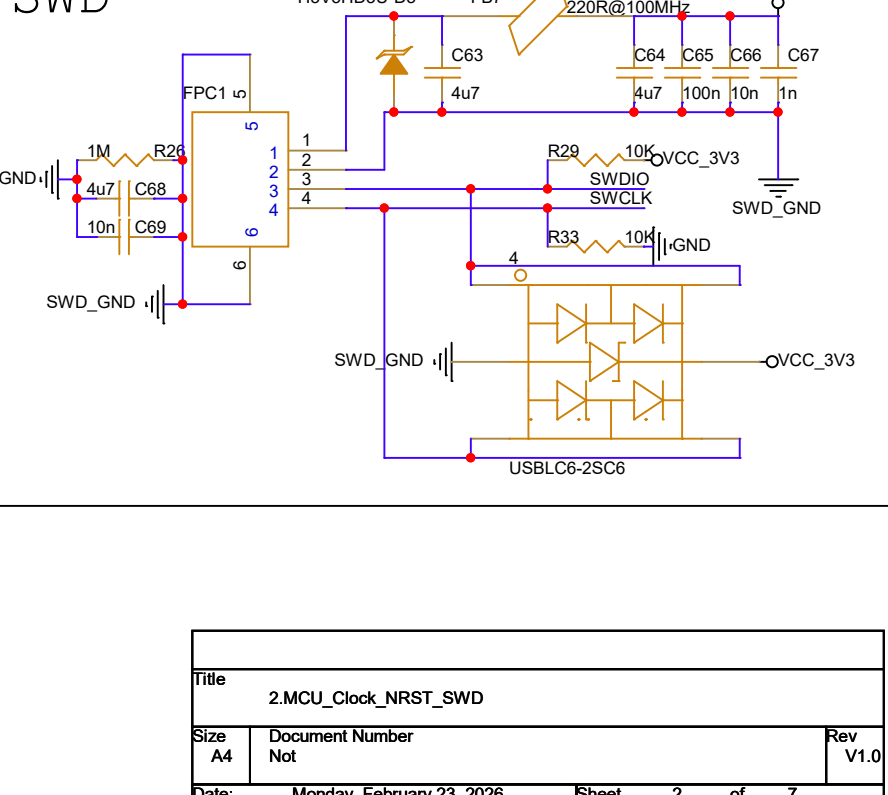
BOOT0



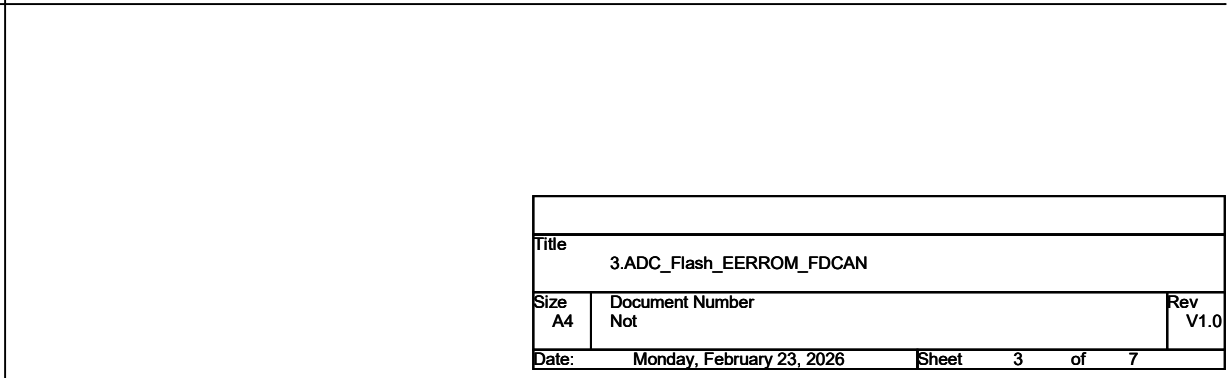
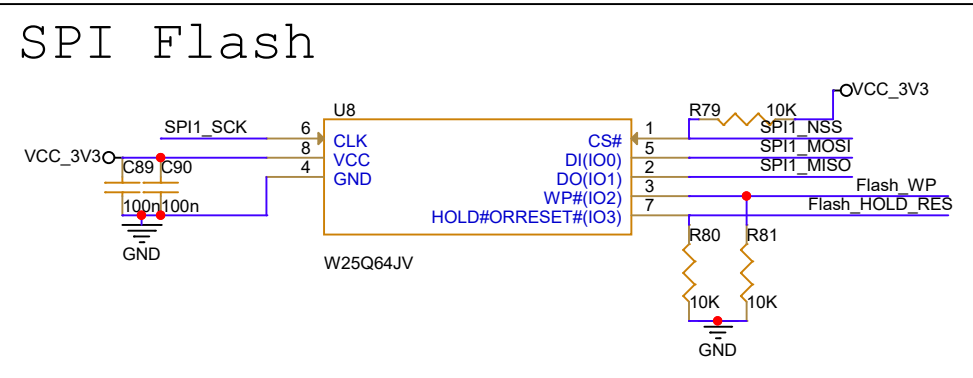
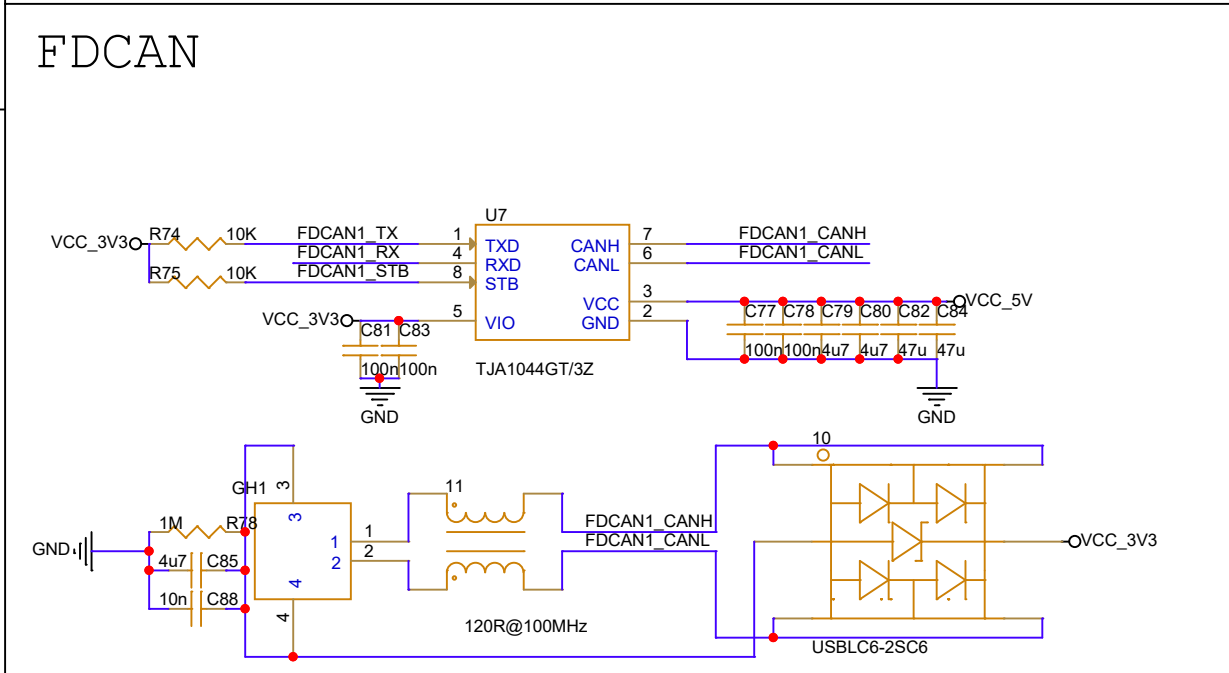
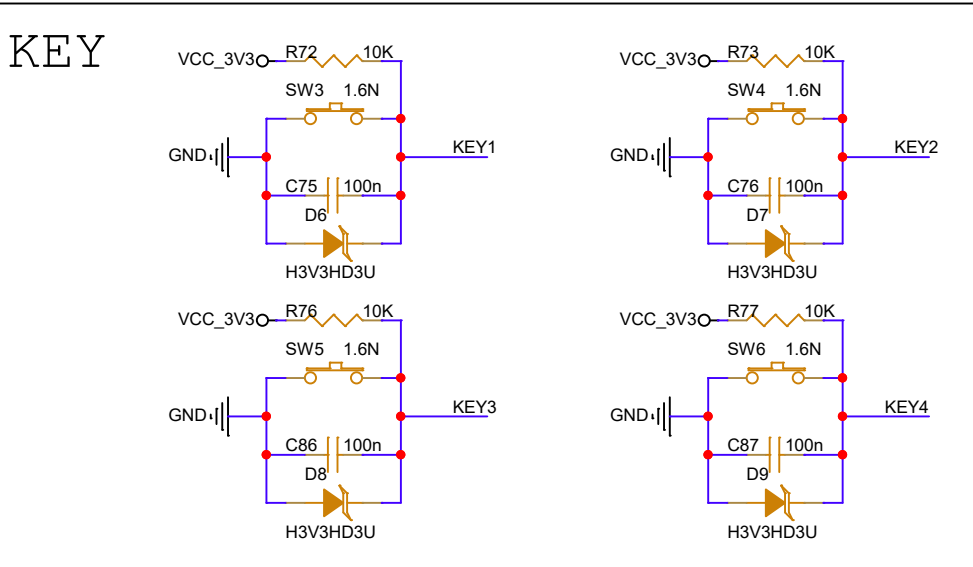
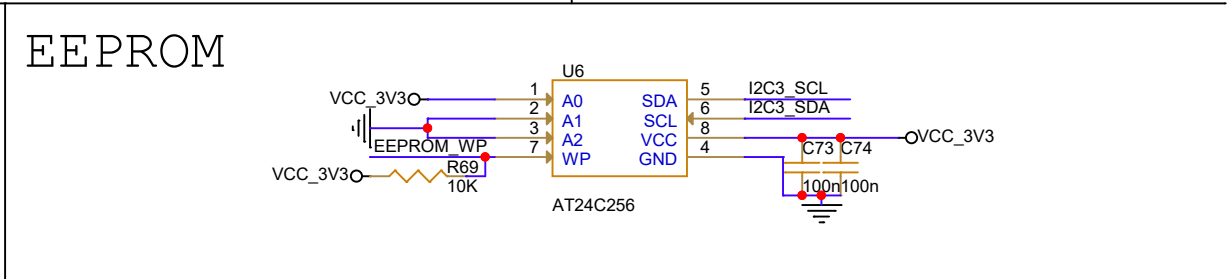
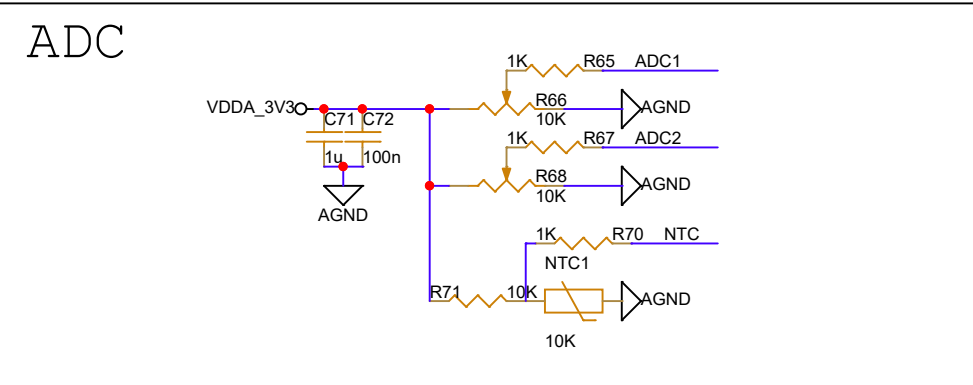
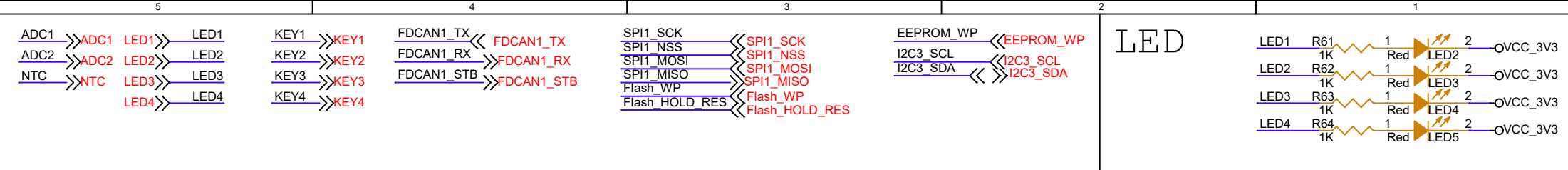
MCU IO



SWD

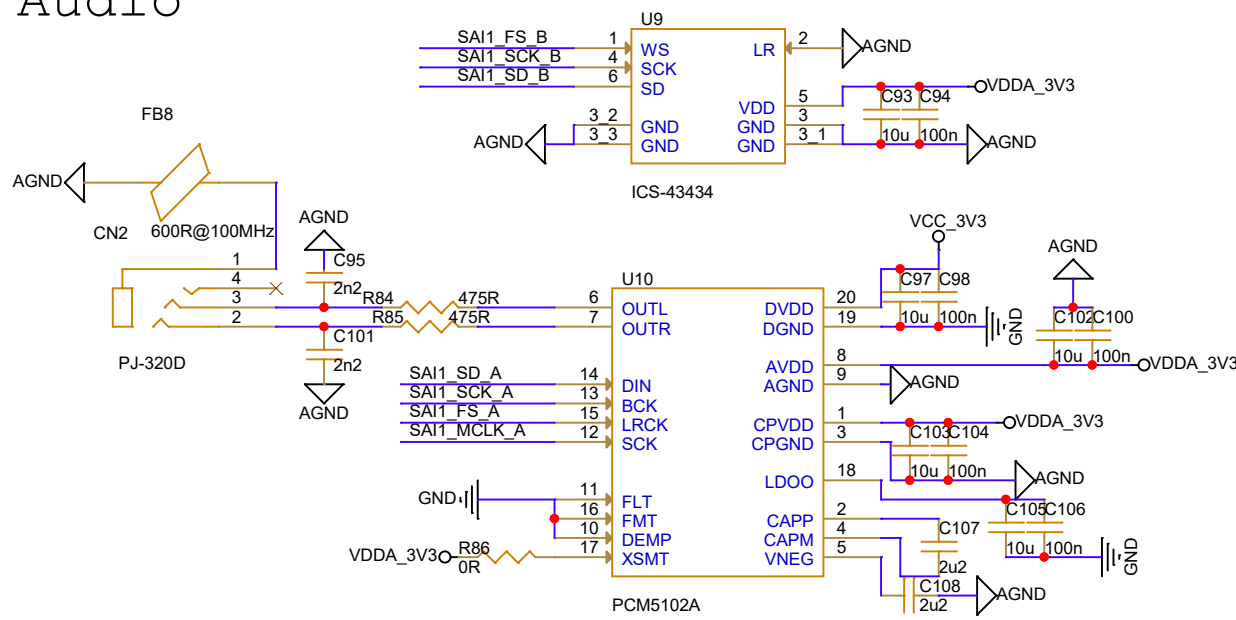


Title		
2.MCU_Clock_NRST_SWD		
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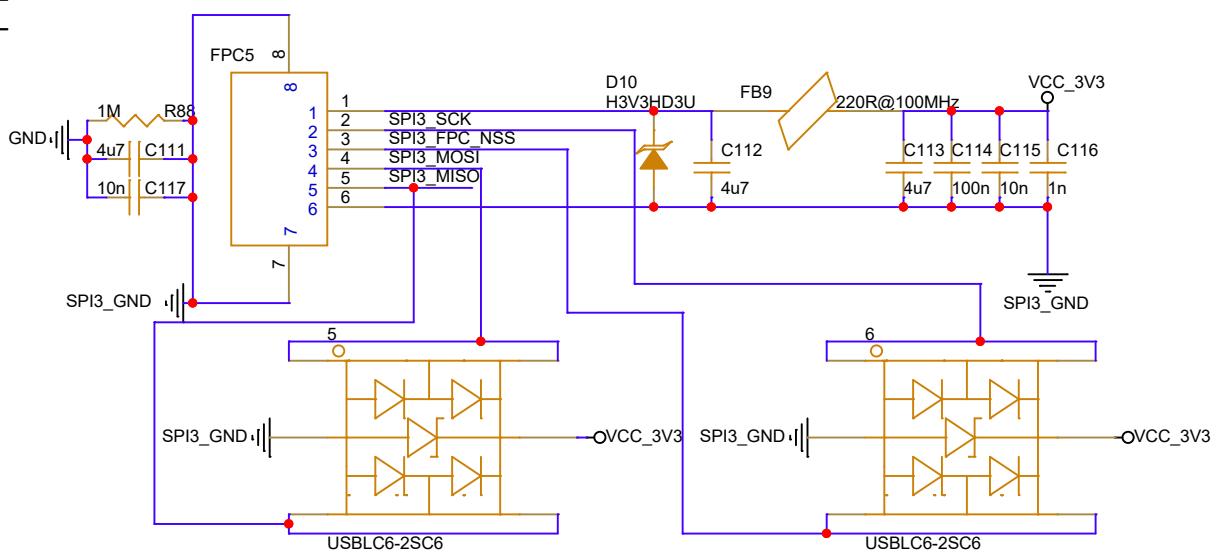


Title		
3.ADC_Flash_EERROM_FDCAN		
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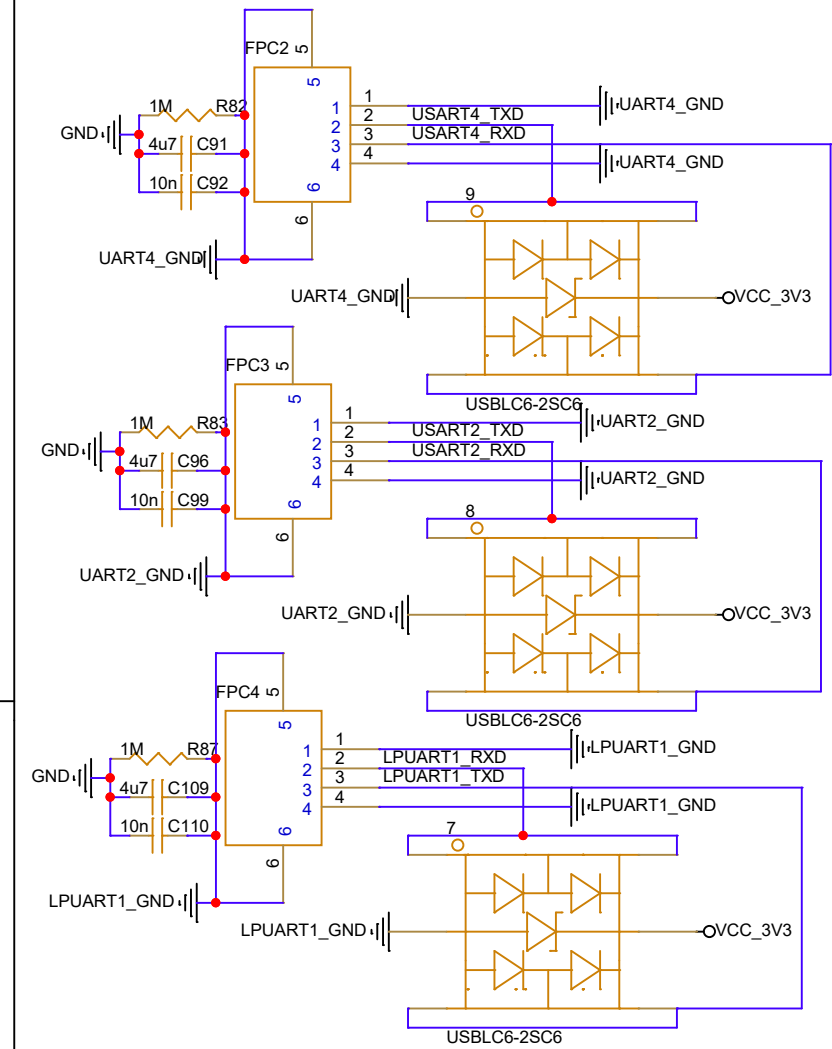
Audio



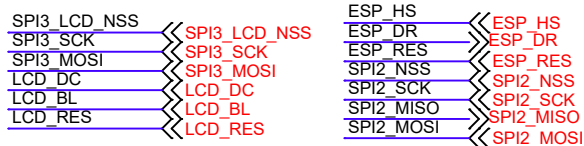
SPI



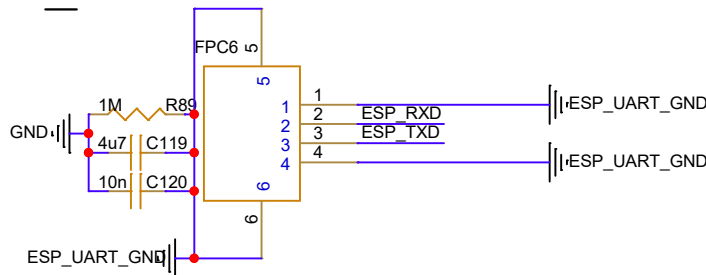
UART



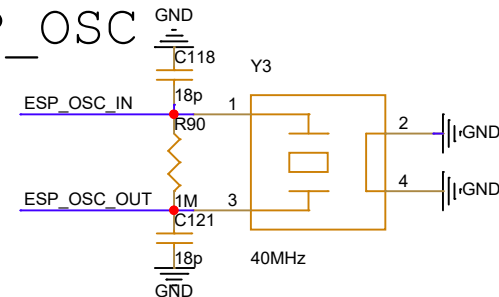
Title					
4.Audio_UART_SPI					
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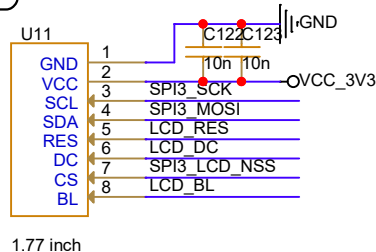
ESP_USART



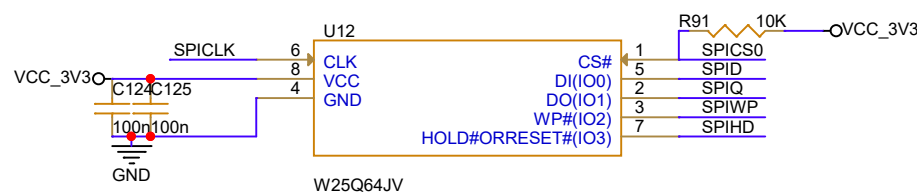
ESP_OSC



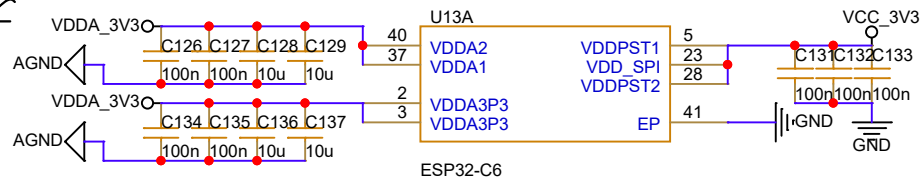
1.77 inch LCD



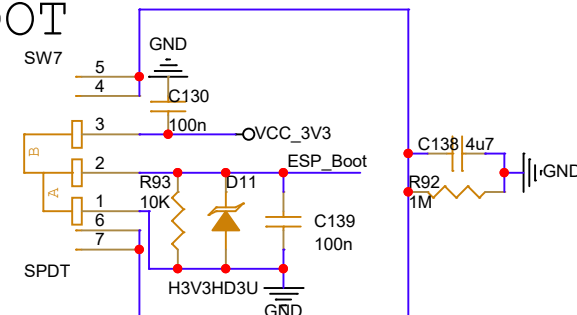
ESP Flash



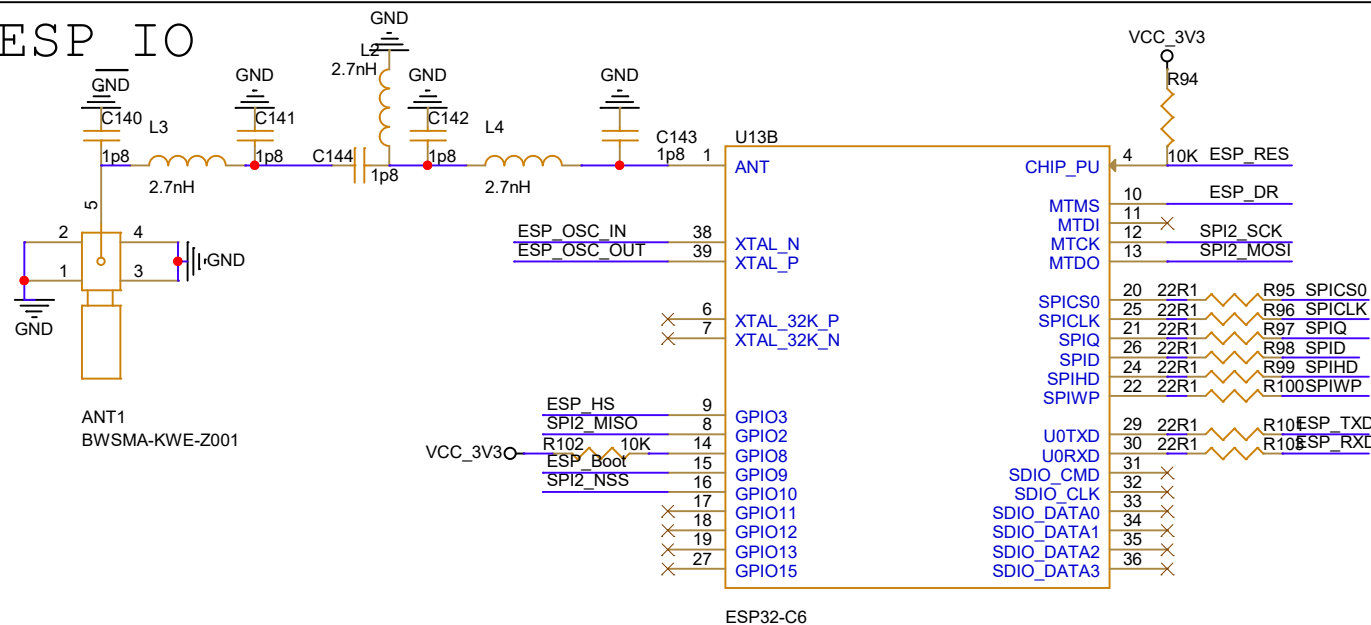
ESP_Power



ESP_BOOT



ESP_IO



Title		
5.WIFI_LCD		
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