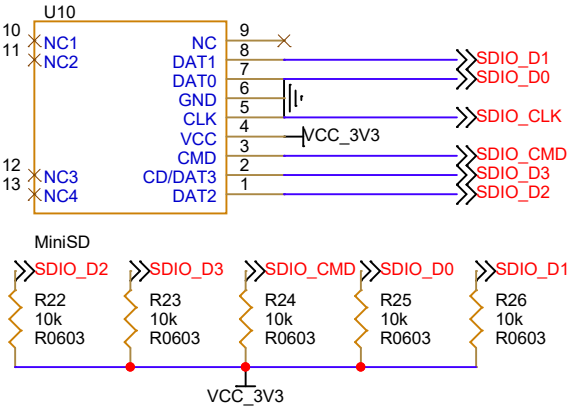
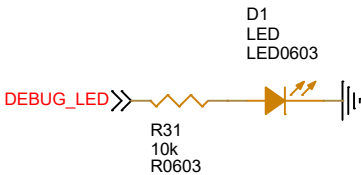


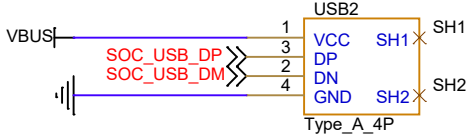
Mini SD



LED



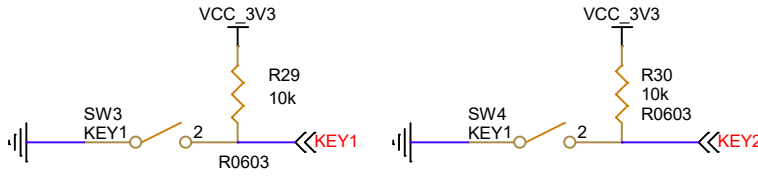
USB A



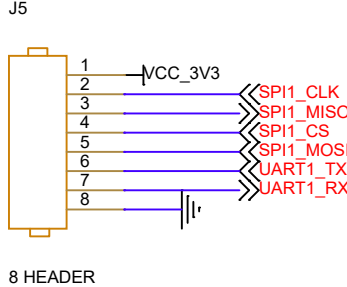
固定孔



KEY

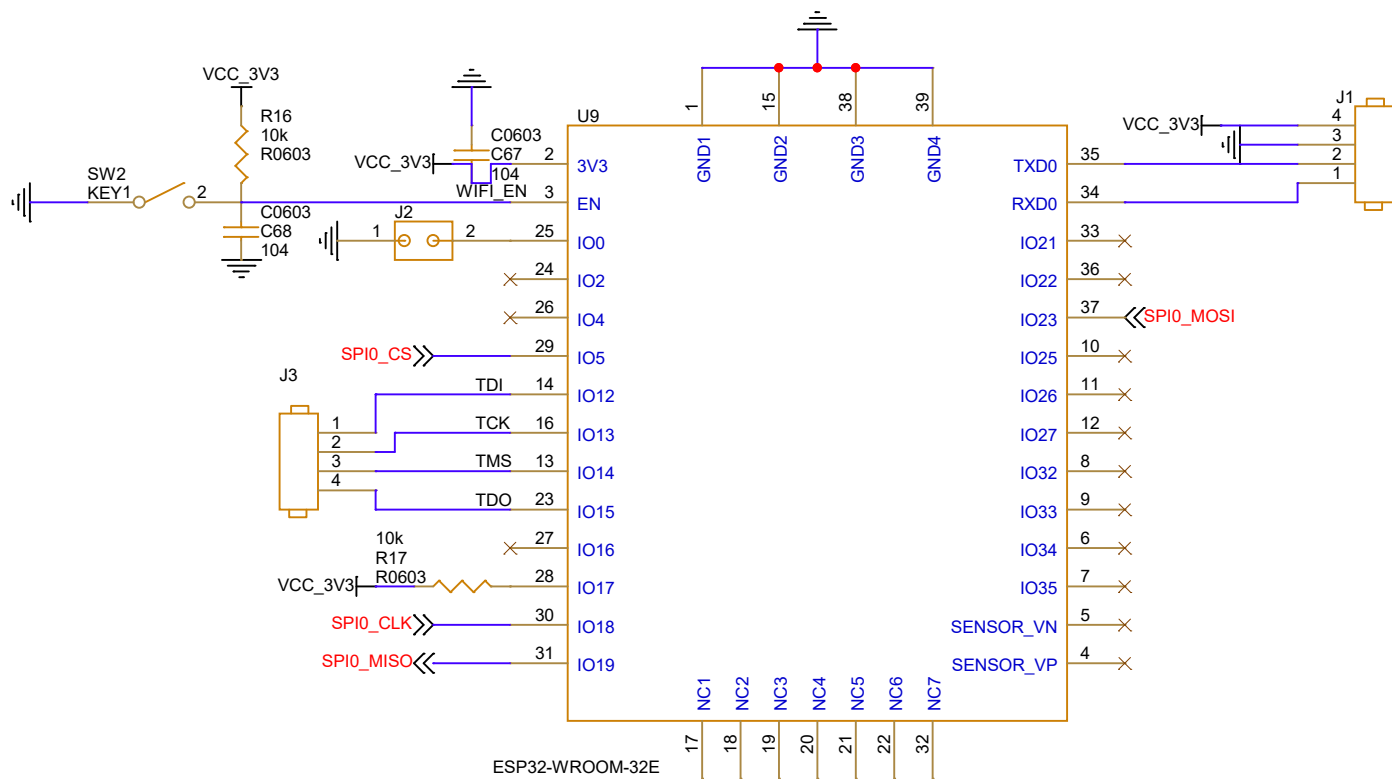


UART1/SPI1 PHY



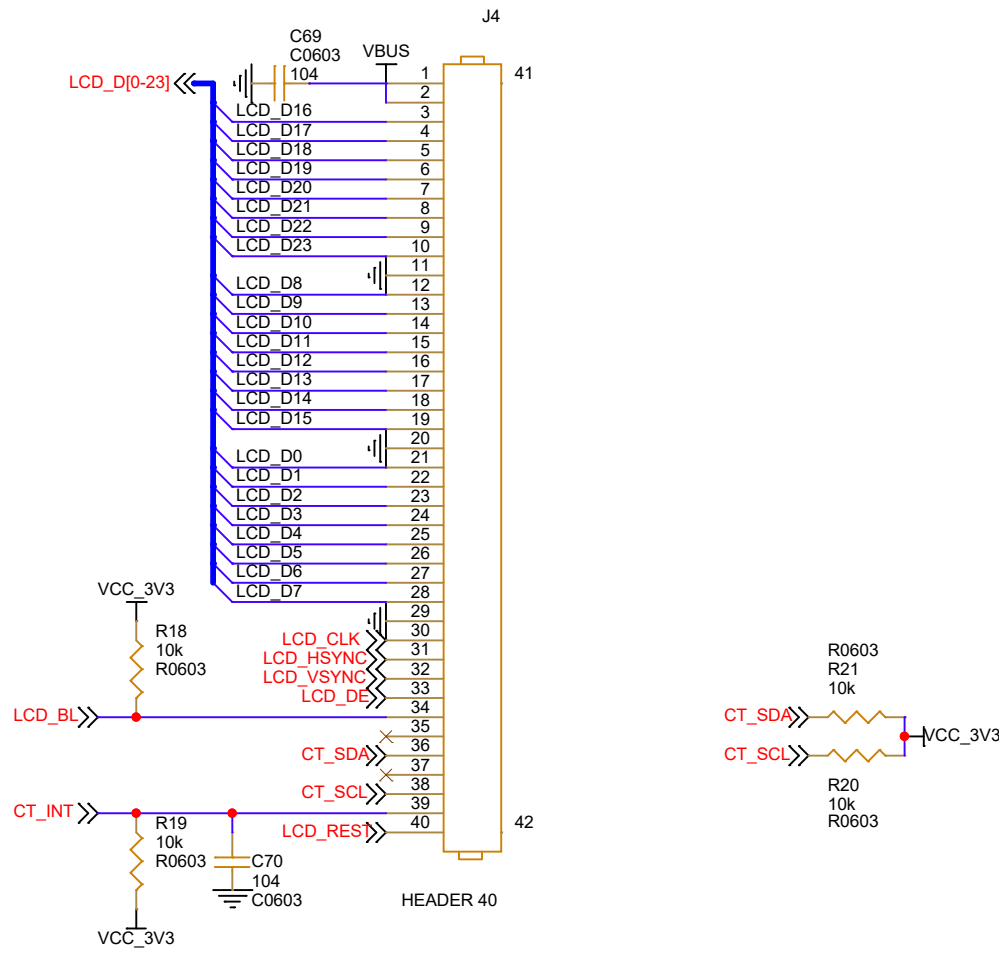
项目名称	F1C200S Core board		
版本	1.0.0		
描述	SD/LCD/KEY/USB A/SPI1/UAET1		
作者	TZH	日期	2024/6/9

WiFi ESP32



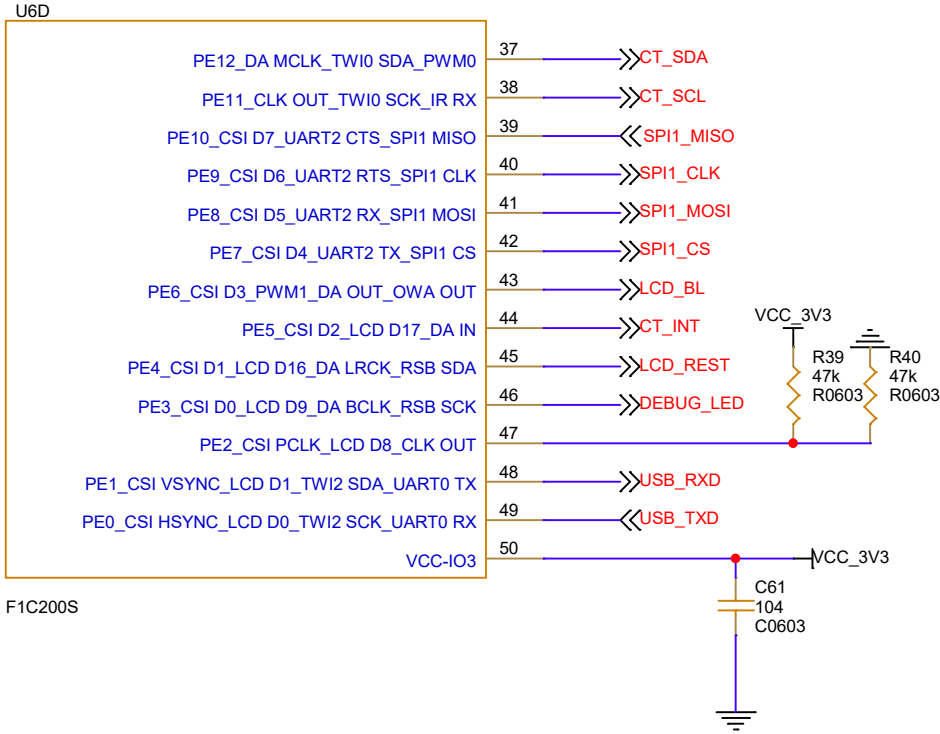
项目名称	F1C200S Core board		
版本	1.0.0		
描述	WiFi ESP32		
作者	TZH	日期	2024/6/9

LCD PHY

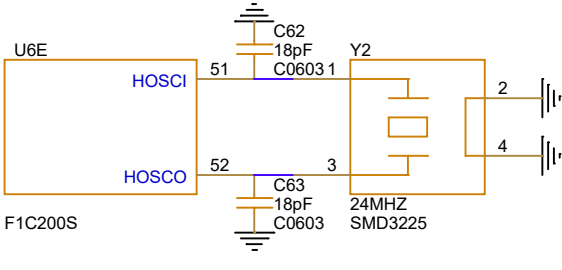


项目名称	F1C200S Core board		
版本	1.0.0		
描述	LCD PHY		
作者	TZH	日期	2024/6/9

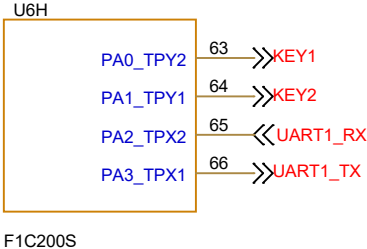
SPI IO



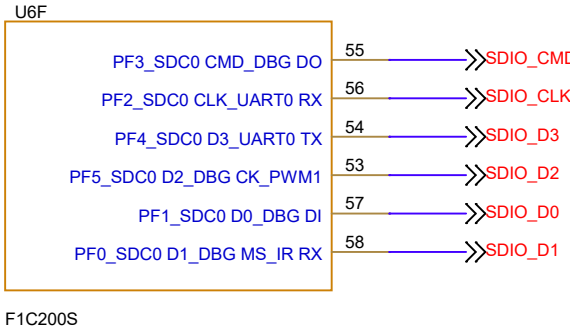
OSC



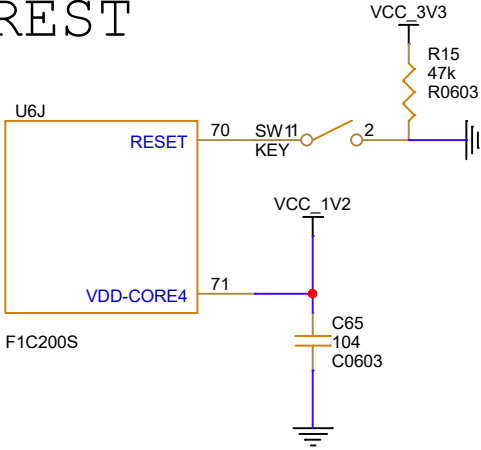
UART IO



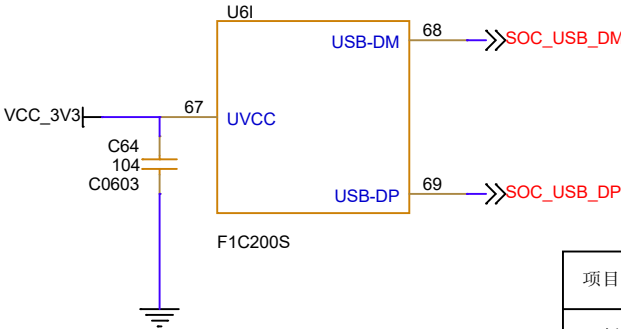
SDIO



REST

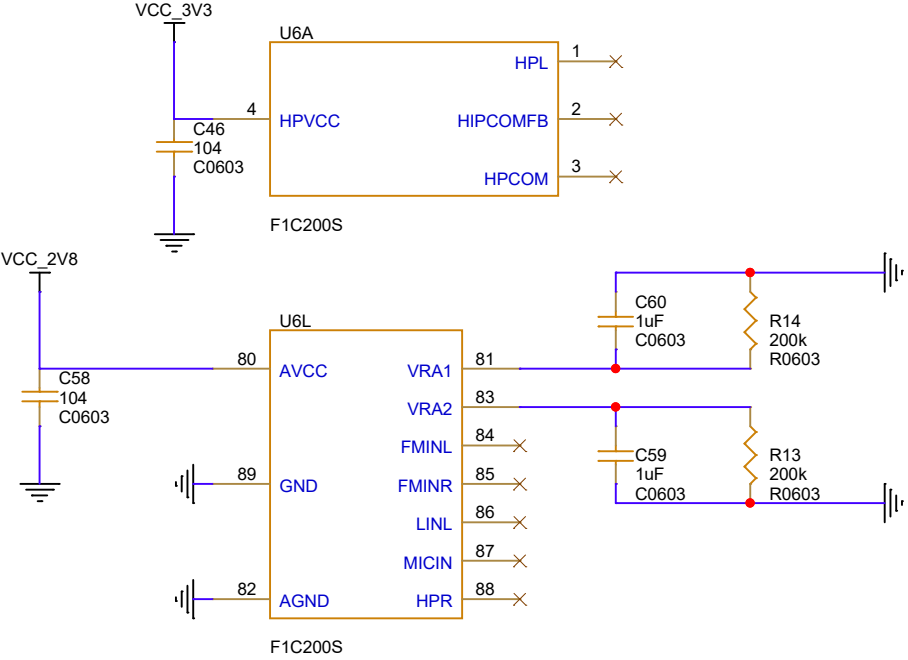


SOC USB

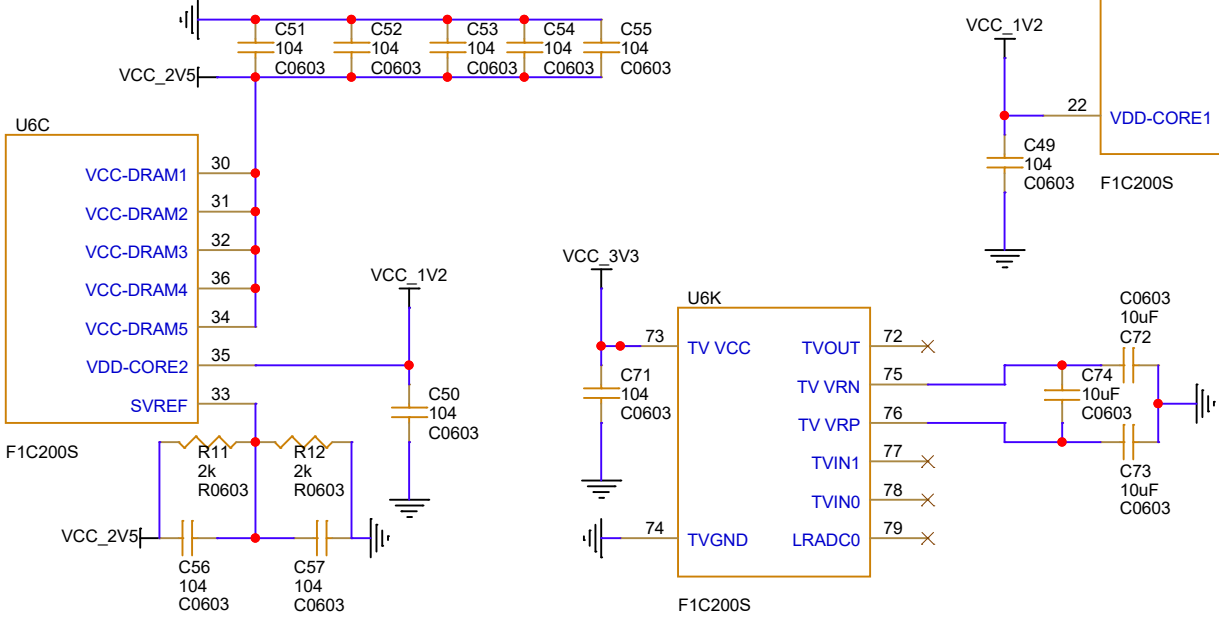


项目名称	F1C200S Core board		
版本	1.0.0		
描述	SPI/USART/SDIO/SOC/USB/REST IO		
作者	TZH	日期	2024/6/7

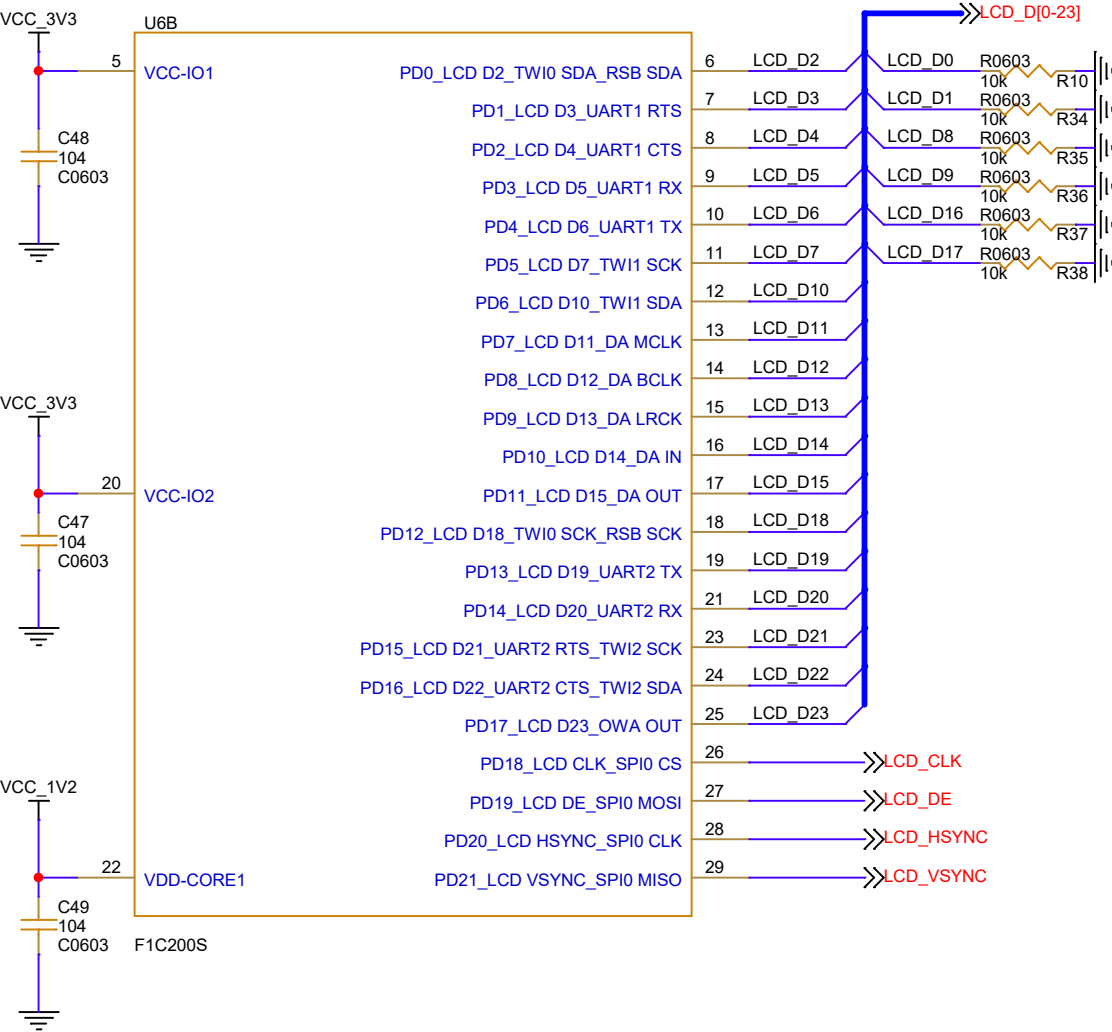
Audio



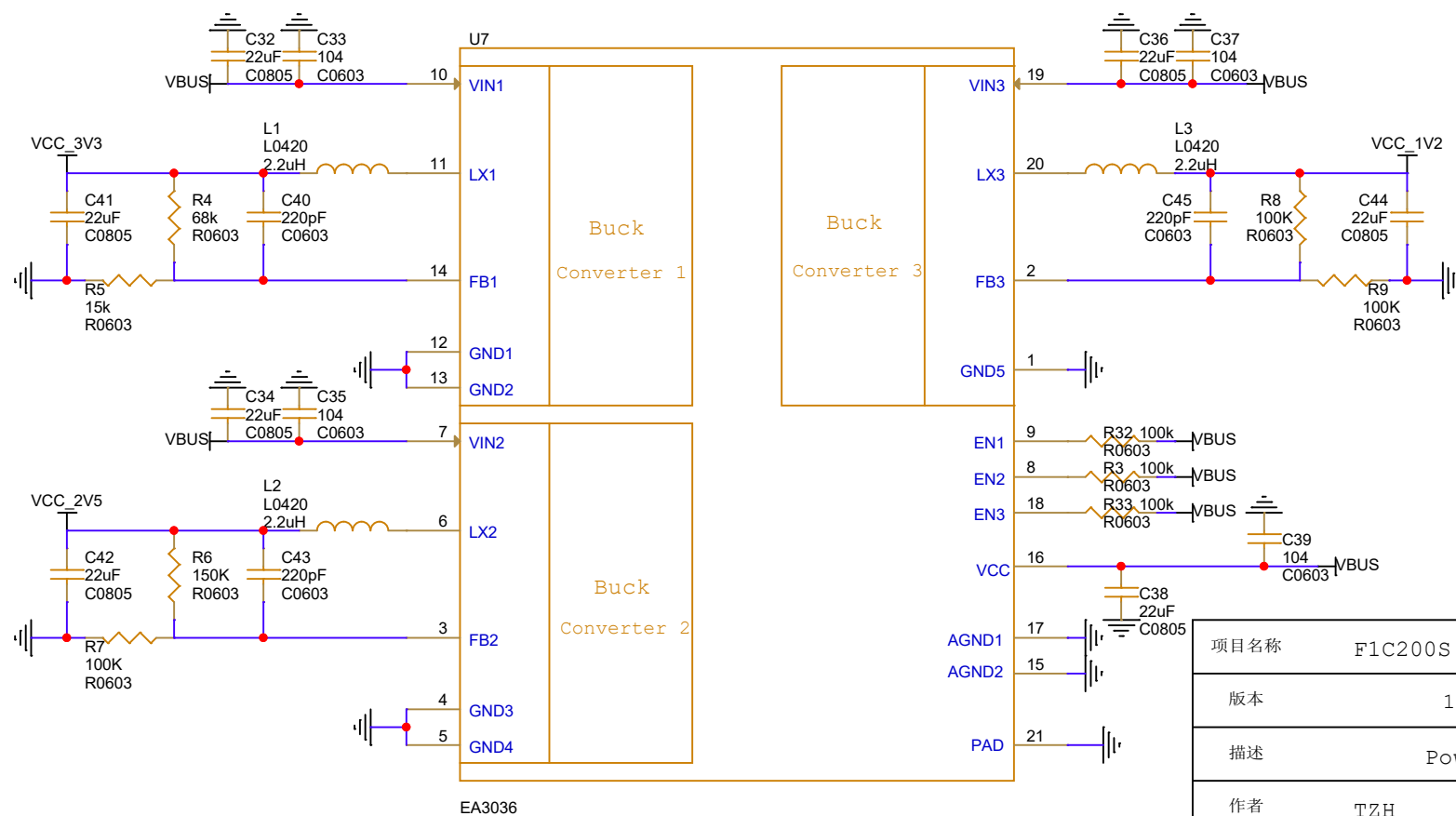
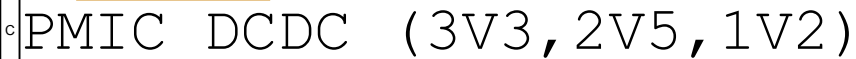
DRAM POWER



LCD IO



项目名称	F1C200S Core board		
版本	1.0.0		
描述	Audio/DRAM_POWER/LCD_IO		
作者	TZH	日期	2024/6/7



项目名称	F1C200S Core board		
版本	1.0.0		
描述	Power/UART		
作者	TZH	日期	2024/5/31

F1C200S Core board

- 1) PMIC: EA3036

2) RAM: DDR1 64MB

3) ROM: SD

4) Support: USB2.0 HOST

5) Support: WiFi ESP32 (SPI)

6) Support: 1 x UART

7) Support: 1 x SPI

8) Support: 1 x RGB LCD 40P

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Revision History

Version	Date	Change Description
V1.0	2024-5-31	1:Revision preliminary version

Power Summary

Power Supply	Channel	Supply Limit	Power Name	Default Voltage
VBUS	BUCK	3.0A	VCC_3V3	3.3V
VBUS	BUCK	3.0A	VCC_2V5	2.5V
VBUS	BUCK	3.0A	VCC_1V2	1.2V
VBUS	LDO	0.3A	VCC_2V8	2.8V