ΣΧΕΔΙΑΣΗ ΨΗΦΙΑΚΩΝ ΣΥΣΤΗΜΑΤΩΝ(2021)

Ομάδα:DSD105

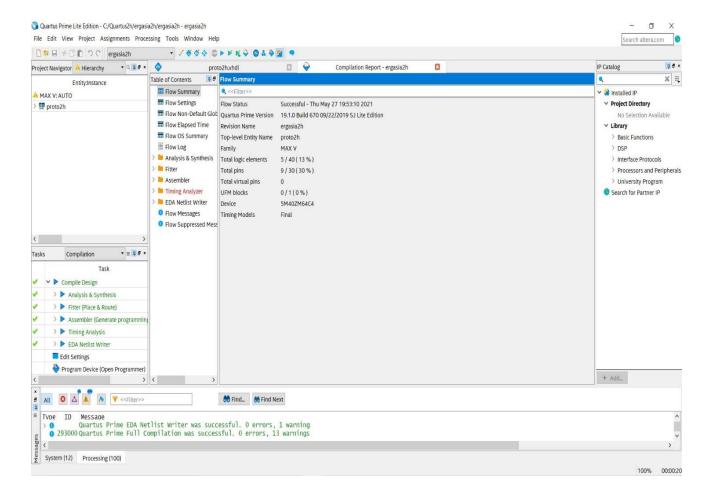
Μέλη:

Δημήτριος Ρουσάλης(p3200166@aueb.gr)

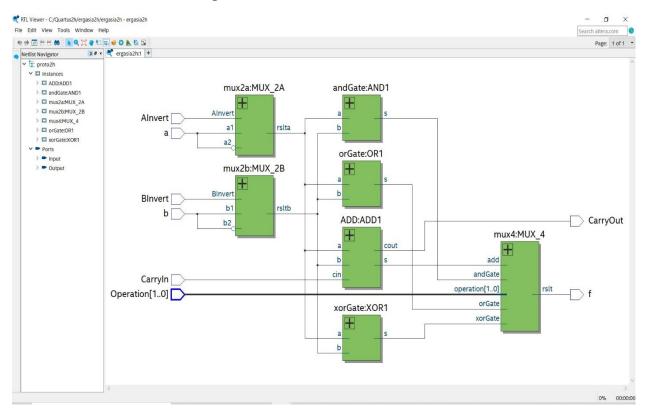
Τσίντζος Ιωαννης(p3200211@aueb.gr)

MEPOΣ 1°

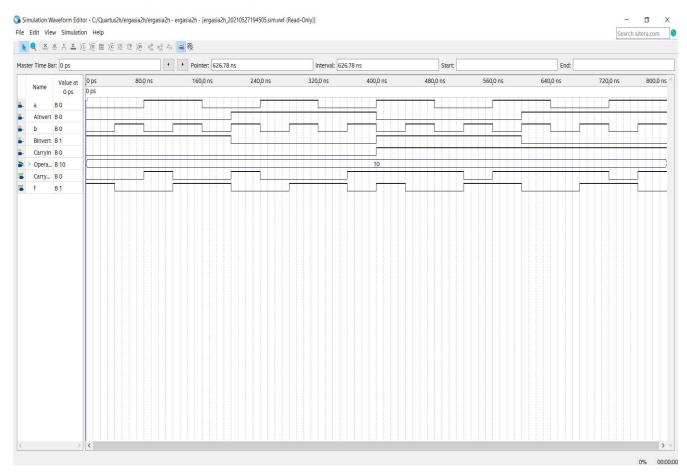
• Compilation του vhdl κωδικα:



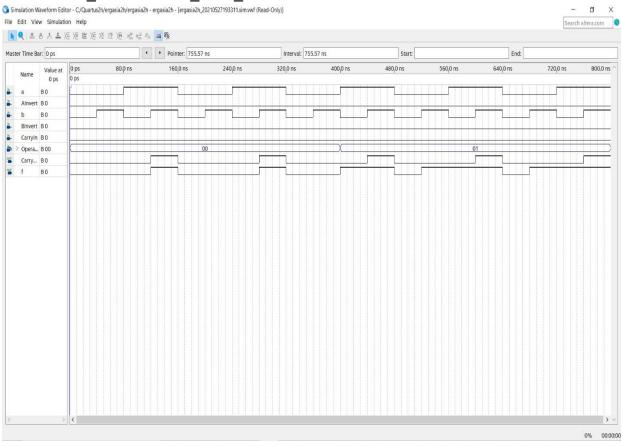
• Screenshot του RTL diagram:



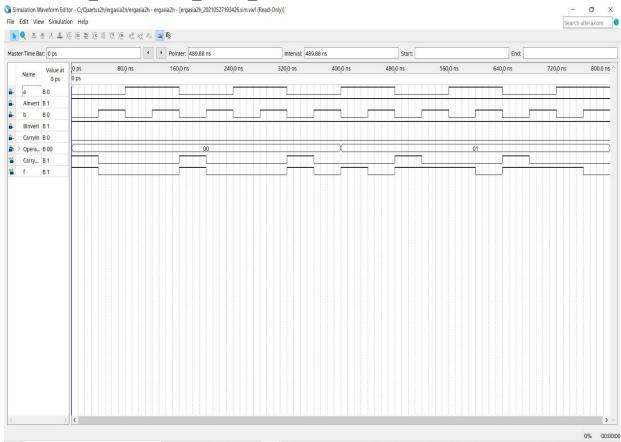
Screenshot_WAVEFORM_ADD_SUB



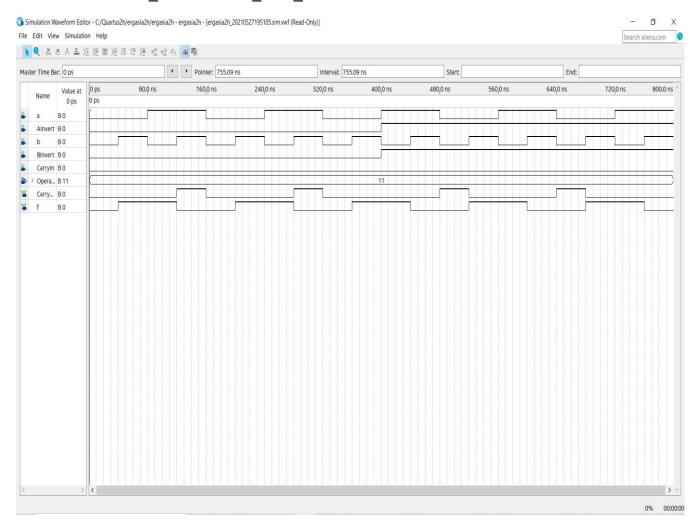
Screenshot_WAVEFORM_AND_OR



Screenshot_WAVEFORM_NAND_NOR

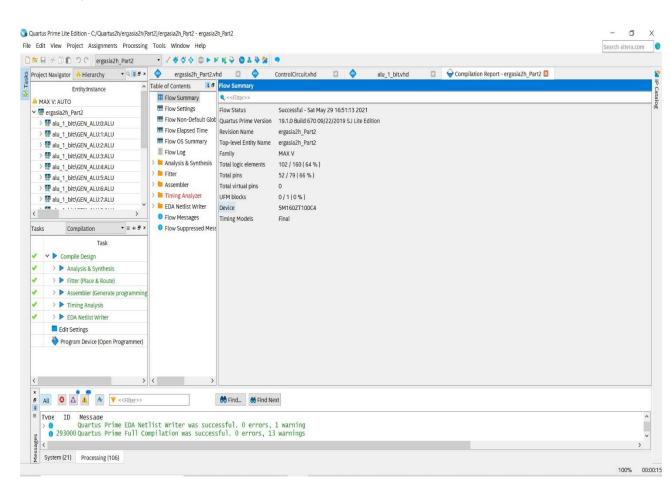


• Screenshot_WAVEFORM_XOR_XNOR

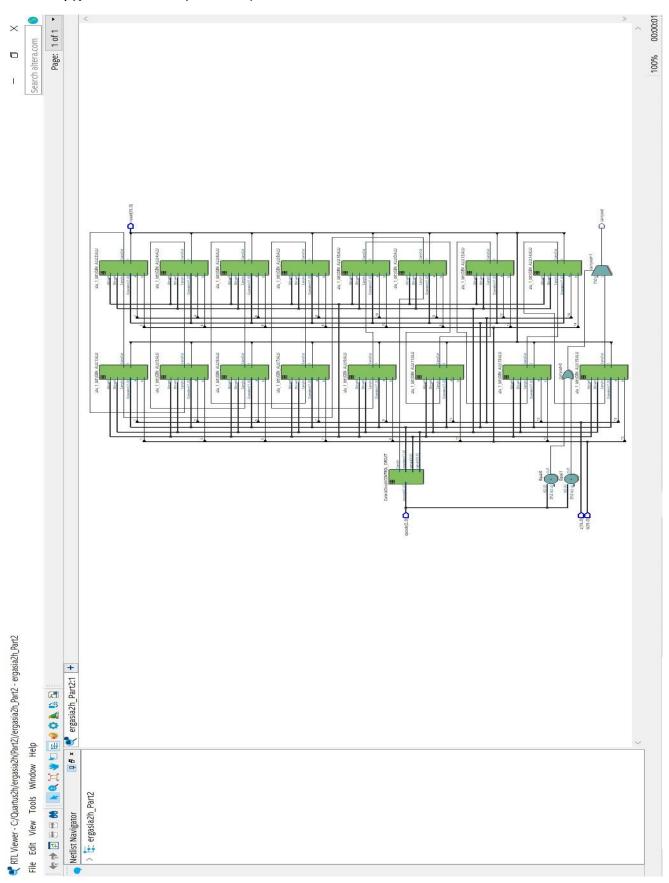


ΜΕΡΟΣ 2°

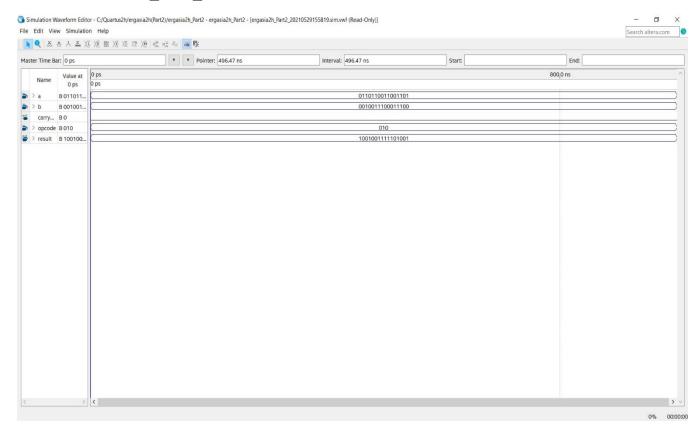
• Compilation του vhdl κωδικα:



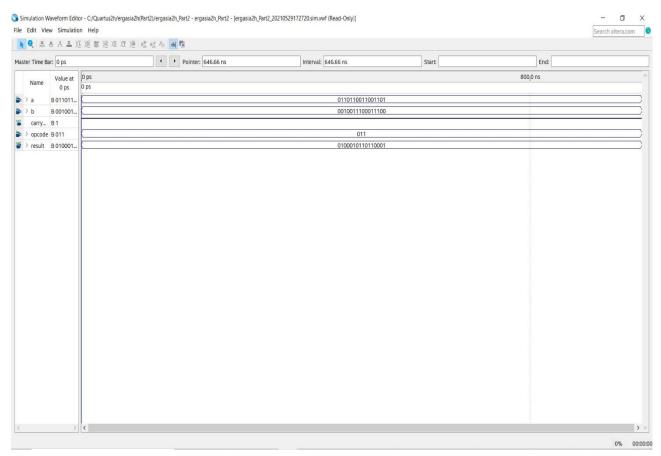
 Screenshot του RTL diagram: (λεπτομερεστερα φαινεται στο αρχειο JPEG του φακελου)



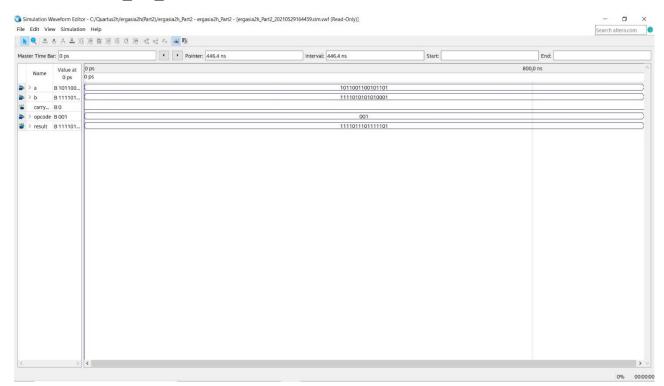
Screenshot_ADD_Part2



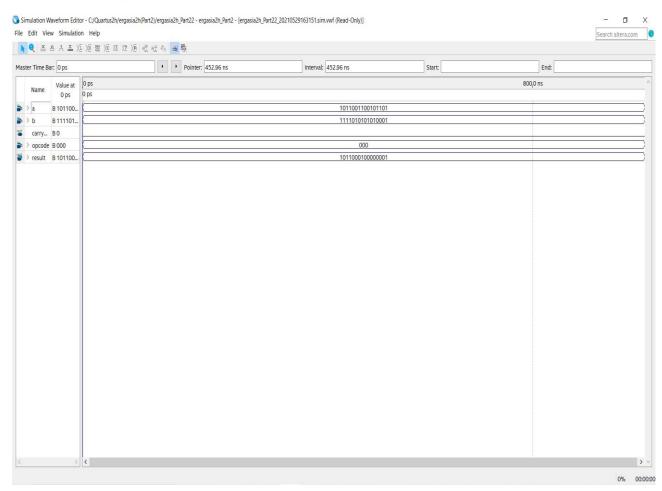
• Screenshot_SUB_Part2



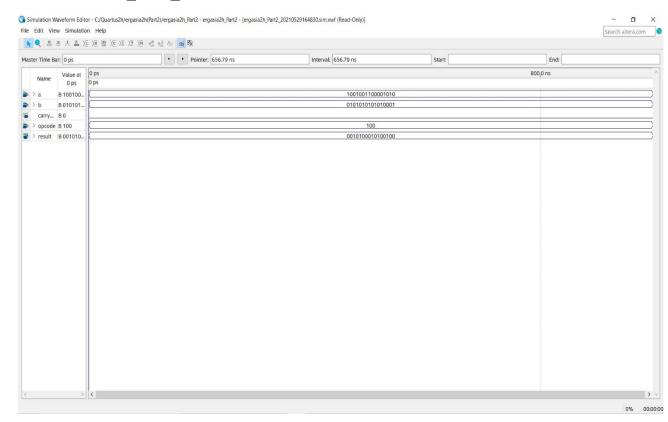
Screenshot_OR_Part2



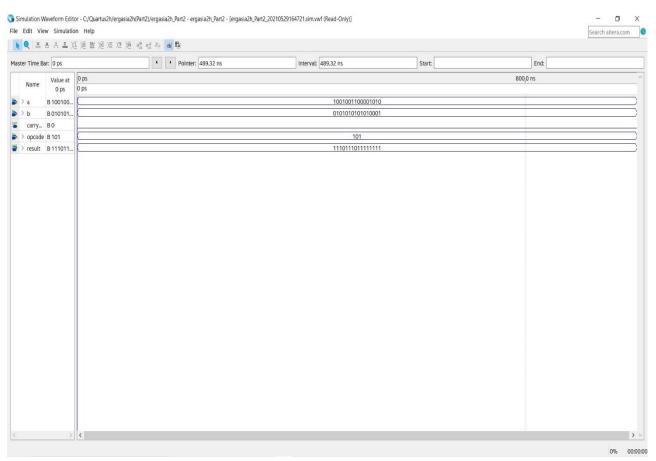
• Screenshot_AND_Part2



Screenshot_NOR_Part2



Screenshot_NAND_Part2



• Screenshot_XOR_Part2

