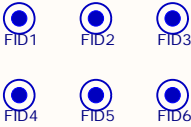
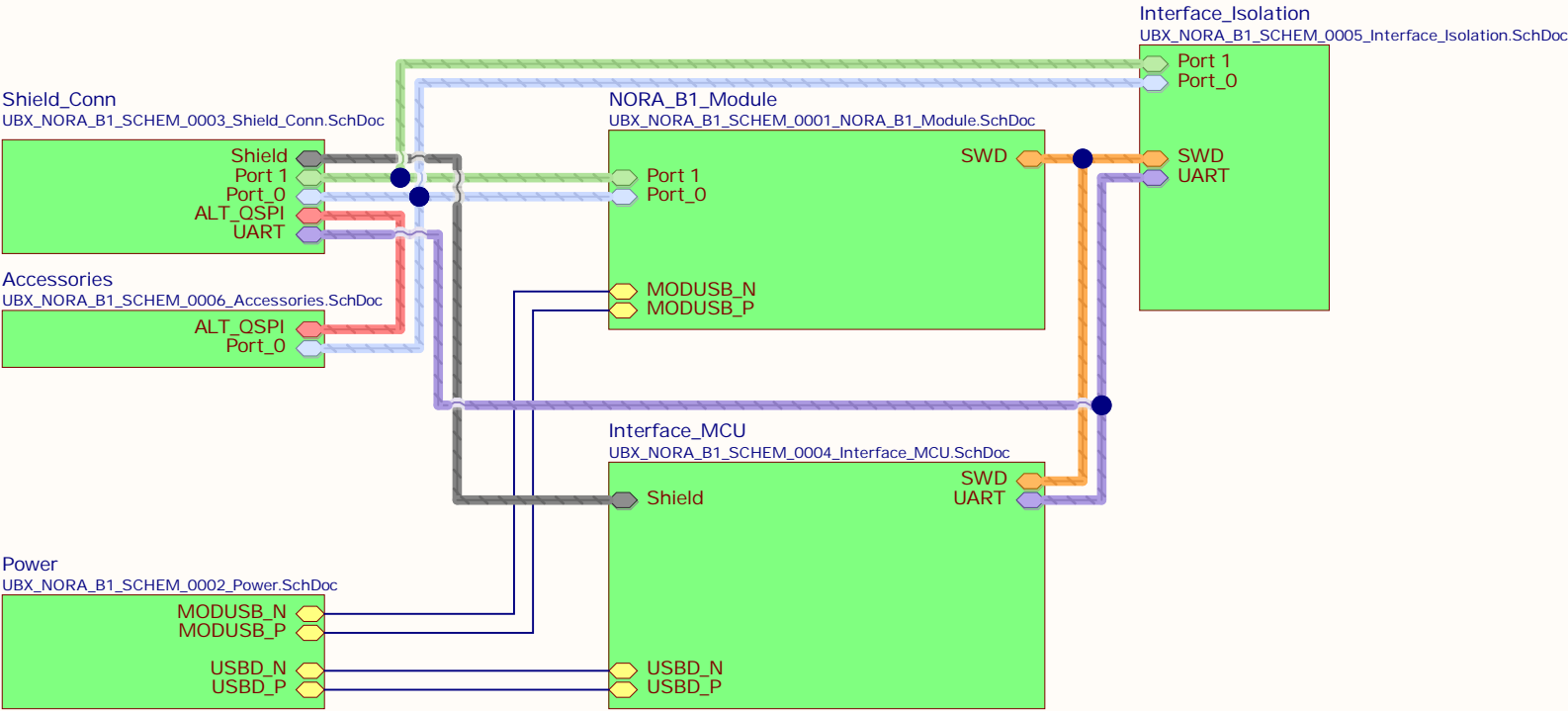

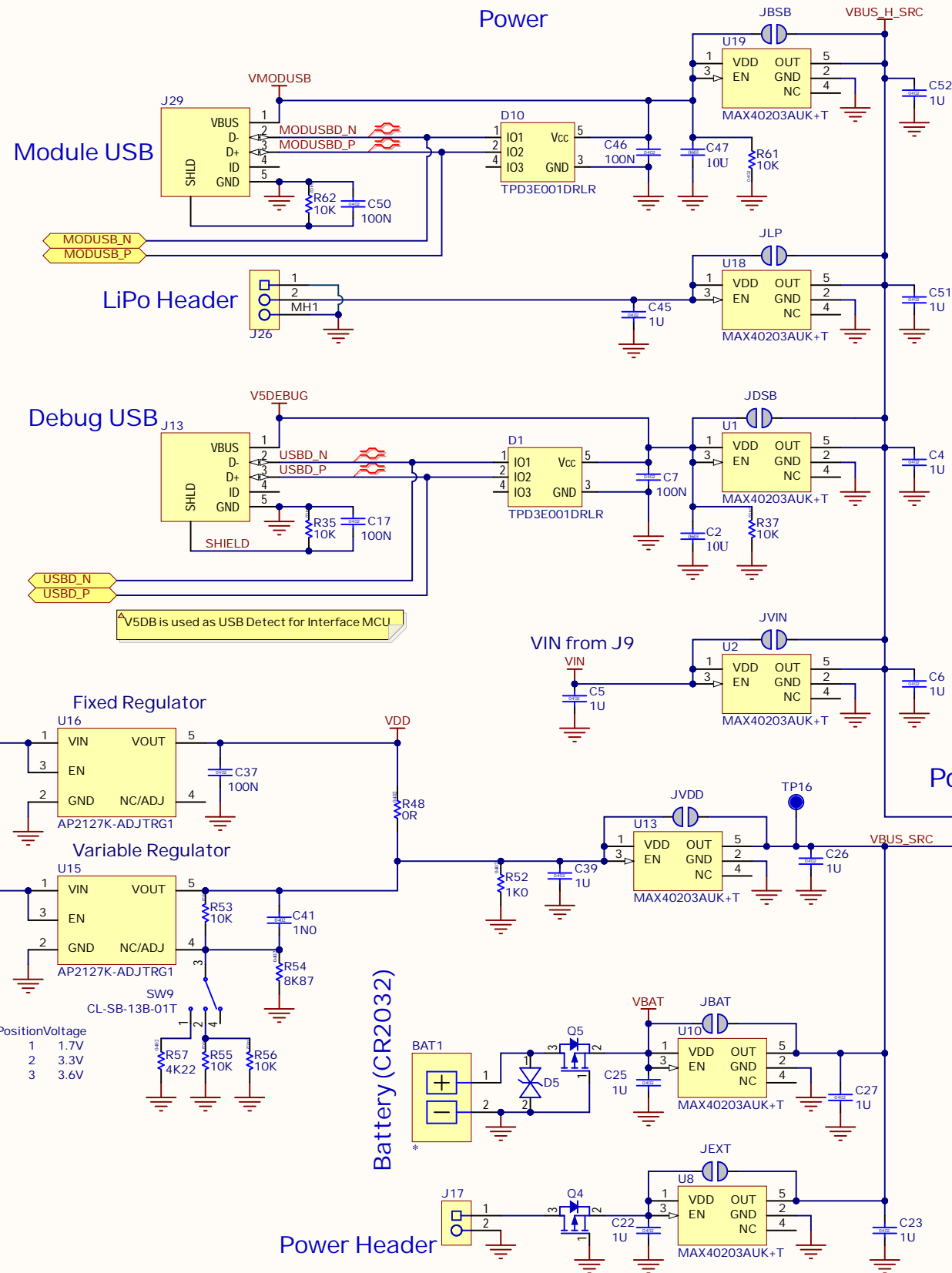


Schematic/Assembly Revision			
Rev.	Description	Date	By

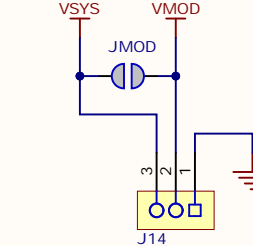


 C1-Public	Project: TE-RB1	EVK-NORA-B10	PCB:	Revision: Rev E
	Part Number: [No Variations]		PCBA:	Size: A3
	Author: EMOO	UBX_NORA_B1_SCHEM_0000_Topsheet.SchDoc		Page:
	Team: u-blox AG	Sch Num: UBX_NORA_B1_SCHEM	Release Date: 24 Jul 2020	0 / 6

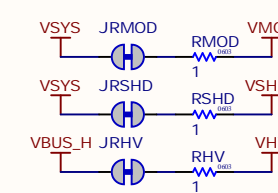


Current Measurement

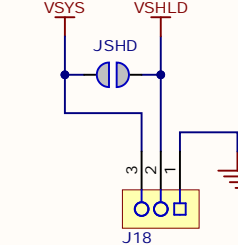
Module Current Measurement



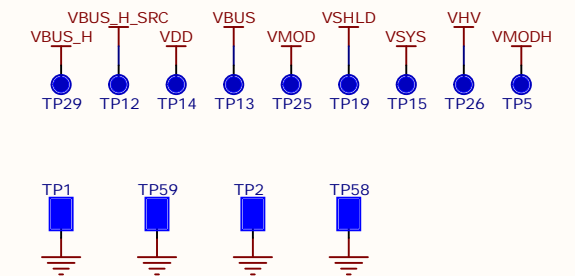
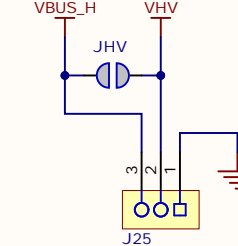
Sense Resistor Disable



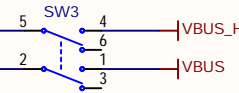
Shield Current Measurement



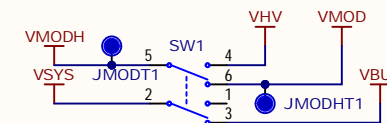
Module HV Current Measurement



Power Switch



HV Mode Switch

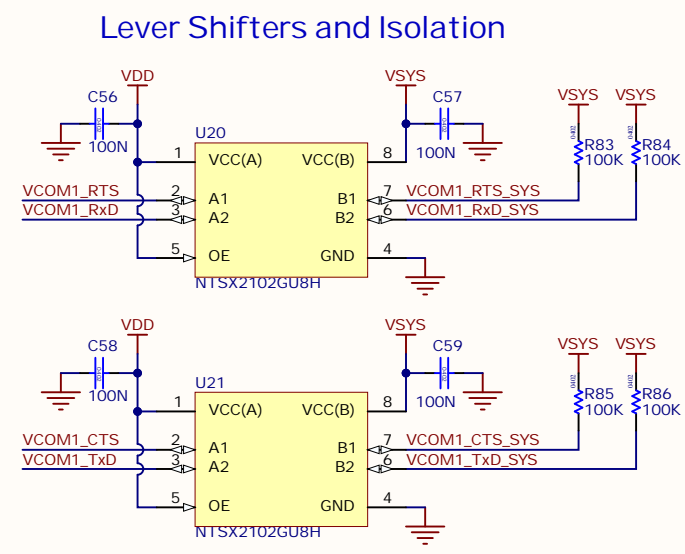
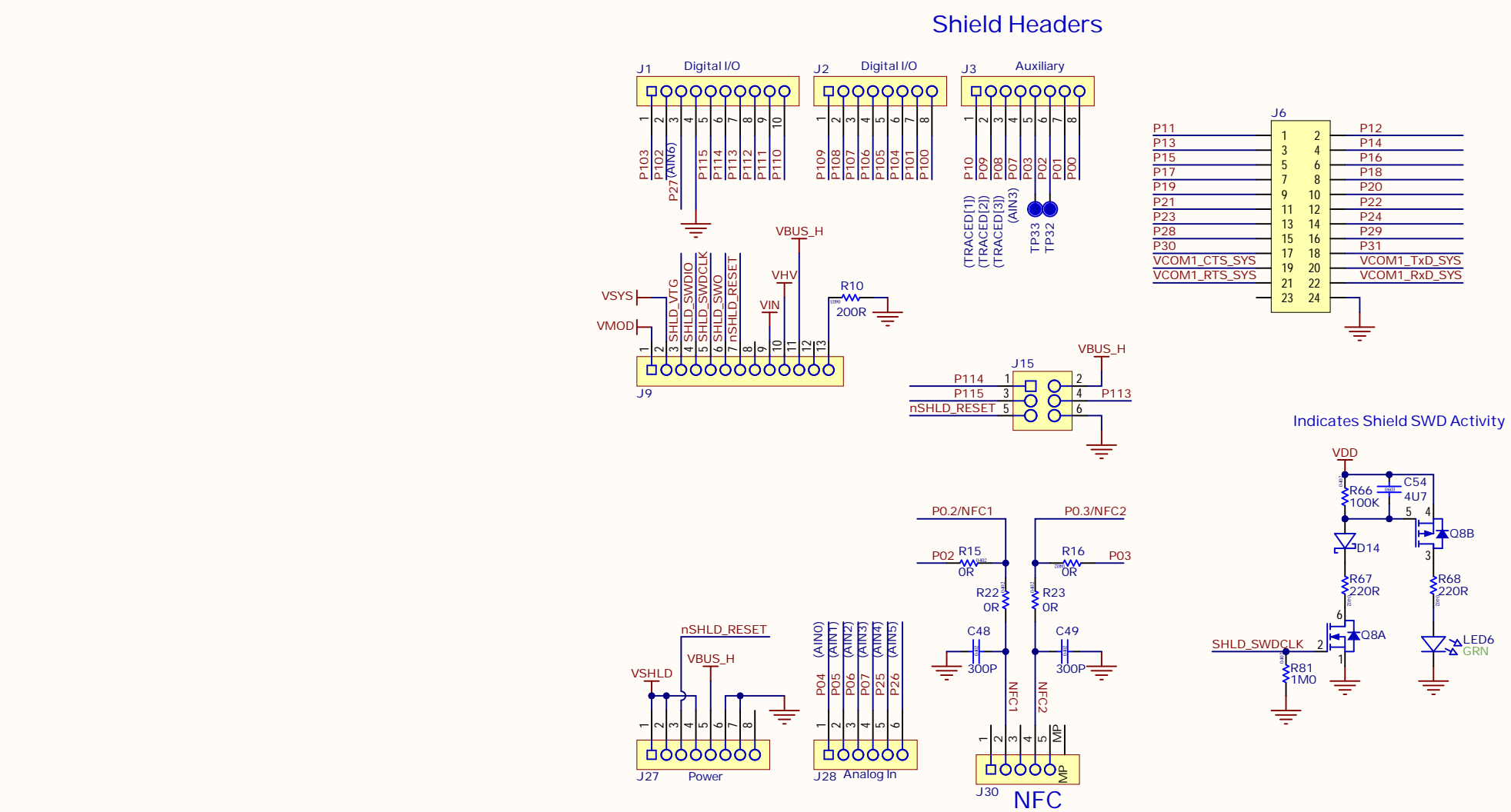
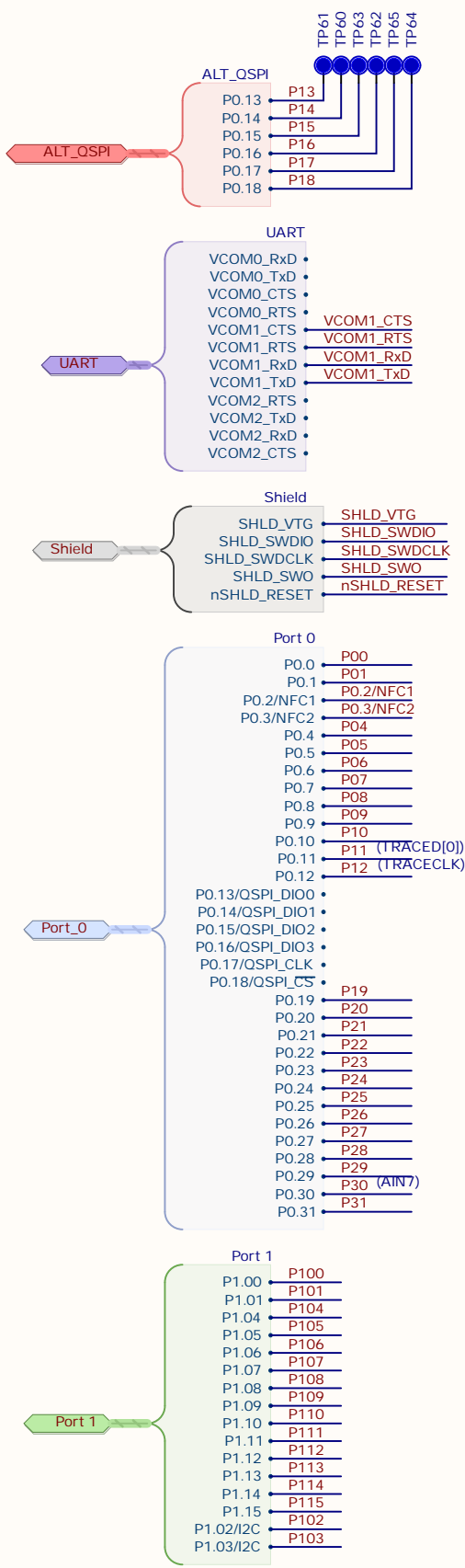


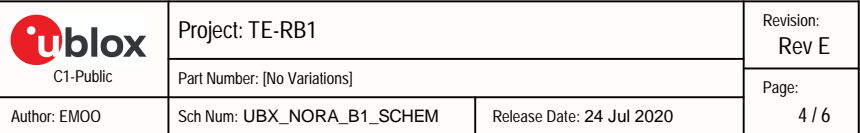
HV Mode: Module is powered from VMODH by VHV (VBUS_H) rail, VSYS is supplied by the module from VMOD
LV Mode: Module is powered from VMOD by VSYS, VSYS is supplied by VBUS

Power Rails:
 VMODUSB = 5V (from Module USB connector)
 V5DEBUG = 5V (from Debug USB connector)
 VBUS_H_SRC = 3.8 to 5V (from VMODUSB, V5DEBUG, or Lipo Header)
 VBUS_H = 3.8 to 5V (VBUS_H_SRC via SW6)
 VDD = 3.3V (via LDO from VBUS_H)
 VBUS = 1.7 to 3.6V (via coin cell, VDD, or header)

In Low Voltage Mode (LV)
 VSYS = VBUS (via SW7)
 VSHLD = VSYS (via current sense)
 VMOD = VSYS (via current sense)
 VMODH = VMOD (via SW7)

In High Voltage Mode (HV)
 VMOD = 1.8 to 3.3V supply from module
 VSYS = VMOD (via current sense)
 VSHLD = VSYS (via current sense)
 VHV = VBUS_H (via current sense)
 VMODH = VHV (via SW7)





Lever Shifters and Isolation

