

# ***Release Notes for AT32F415 Firmware Library Drivers***

## **V2.0.5-2024/12/13**

1. Updated CAN driver.
2. Updated USB driver.
3. Updated some notes.

## **V2.0.4-2024/08/08**

1. Updated “i2c\_flag\_clear” and “i2c\_init” function.
2. Updated some notes.

## **V2.0.3-2024/01/05**

1. Fixed the IAR7.4 compilation warning issue of some drivers.
2. Updated the corresponding ERTC interrupt function names in iar/gcc startup file.

## **V2.0.2-2023/10/26**

1. Added the function interrupt\_flag\_get to each of the IP sections
2. Optimized system clock switching procedure

## **V2.0.1-2023/08/04**

1. Added CRC polynomial support
2. Updated the process of *adc\_ordinary\_channel\_set* and *adc\_preempt\_channel\_set*
3. Updated relevant notes

## **V2.0.0-2022/11/18**

1. The CRM\_HICK48\_NODIV is considered the default setting when HICK is used as PLL clock source