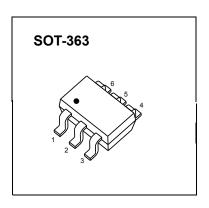


# **SOT-363 Plastic-Encapsulate MOSFETs**

## **Dual N-channel MOSFET**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	I <sub>D</sub>
601/	5Ω@10V	
60 V	7Ω@5V	115mA



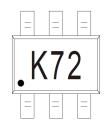
## **FEATURE**

- High density cell design for low R<sub>DS(ON)</sub>
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability

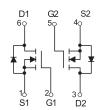
## **APPLICATION**

- Load Switch for Portable Devices
- DC/DC Converter

#### **MARKING**



## **Equivalent Circuit**



## MAXIMUM RATINGS (T<sub>a</sub>=25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-Source voltage	60	V
V <sub>GS</sub>	Gate-Source voltage	±20	V
I <sub>D</sub>	Drain Current	115	mA
P <sub>D</sub>	Power Dissipation	150	mW
R <sub>OJA</sub>	Thermal Resistance from Junction to Ambient	833	°C/W
TJ	Junction Temperature	150	$^{\circ}$
T <sub>stg</sub>	Storage Temperature	-55-150	℃



## $T_a$ =25 $^{\circ}C$ unless otherwise specified

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0 V, I <sub>D</sub> =250 μA	60			V
Gate-threshold voltage *	$V_{th(GS)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μA	1	1.6	2.5	V
Gate-body leakage	I <sub>GSS</sub>	V <sub>DS</sub> =0 V, V <sub>GS</sub> =±20 V			±80	nA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =60 V, V <sub>GS</sub> =0 V			80	nA
Drain course on registeres *	R <sub>DS(on)</sub>	V <sub>GS</sub> =10 V, I <sub>D</sub> =500mA		1.1	5	0
Drain-source on-resistance *		V <sub>GS</sub> =5 V, I <sub>D</sub> =50mA		1.2	7	Ω
Forward transconductance *	g <sub>fs</sub>	V <sub>DS</sub> =10 V, I <sub>D</sub> =200mA	80			ms
Duein course on valteurs *	V <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =500mA			3.75	V
Drain-source on-voltage *		V <sub>GS</sub> =5V, I <sub>D</sub> =50mA			0.375	V
Diode forward voltage	$V_{SD}$	I <sub>S</sub> =115mA, V <sub>GS</sub> =0 V	0.55		1.2	V
Input capacitance **	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz			50	
Output capacitance **	Coss				25	pF
Reverse transfer capacitance **	C <sub>rss</sub>				5	

## **SWITCHING TIME**

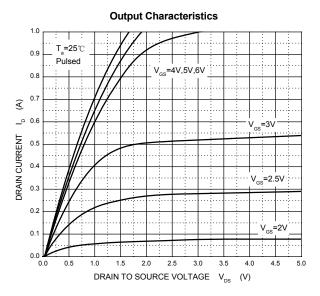
Turn-on time **	$t_{d(on)}$	$V_{DD}$ =25 V, $R_L$ =50 $\Omega$		20	ns
Turn-off time **	$t_{d(off)}$	$I_D$ =500mA, $V_{GEN}$ =10 $V_{G}$ =25 $\Omega$		40	110

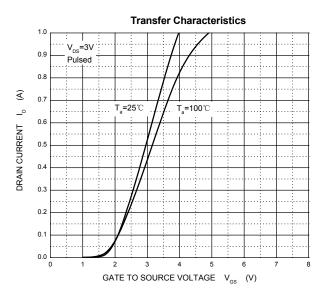
<sup>\*</sup> Pulse Test: Pulse width ≤300µs,duty cycle≤2%.

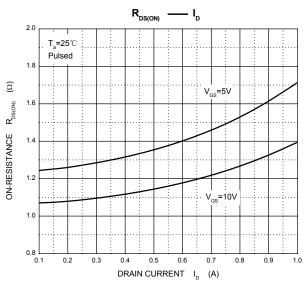
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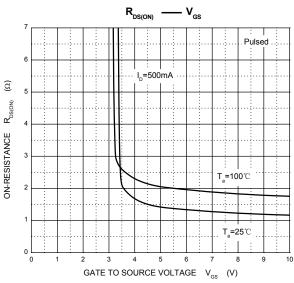
<sup>\*\*</sup> These parameters have no way to verify.

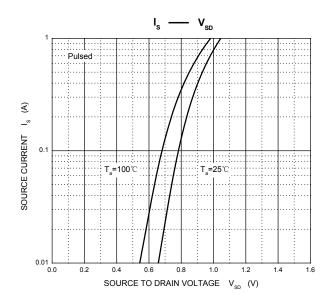


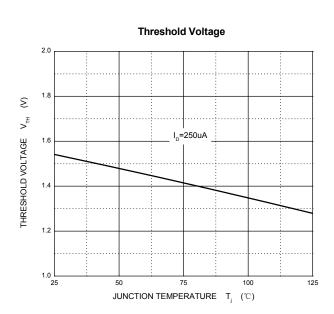








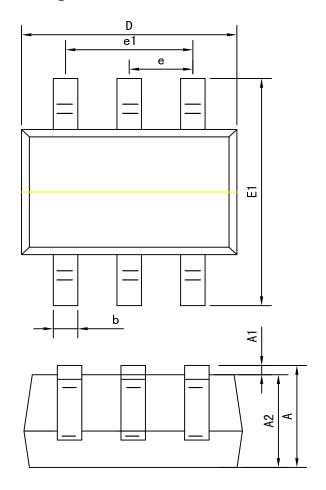


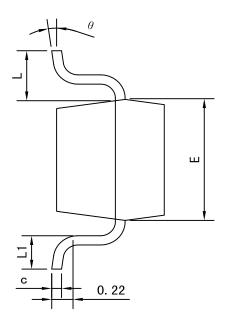




# Package outline dimensions

## **SOT-363**





Symbol	Dimension in Millimeters				
	Min	Max			
А	0.900	1.100			
A1	0.000	0.100			
A2	0.900	1.000			
b	0.150	0.350			
С	0.080	0.150			
D	2.000	2.200			
E	1.150	1.350			
E1	2.150	2.450			
е	0.650	0.650 TYP			
e1	1.200	1.400			
L	0.525 REF				
L1	0.260	0.460			
θ	0°	8°			