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Document version

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1.1 \

WiFi Kit 32 -ibsoaardcldaessignoeldo T&A uphterovomdautcieodn b(yTMH)e it's a highly integrated-FpiroadnudεPtoΒLboΕaa)st, et deLrioyn management system, 0.96" OLED are also incl smart farms, smart home, and loT makers. On this byaesrissi, onthheas V3been upgraded as follo

	‡ 7 M · · · †	‡ 7 M · · · †
U #y	ES P 30 20	ES P 35 23
yo" o	Micro USB	Туре С
0	8 0 0 u A	<1 0 u A

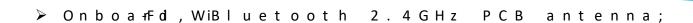
1.2 h

- ➤ Microproce-Ss 3s FoMt &t e (nEsS-bPi3 f2 B X 7 dual core proce pipeline rack Structure, main frequency u
- ► TypCe USB interface with a complete voltag circuit protection, RF shielding, and oth
- Onbrode SH21.b2a5ttery interface, integrated I system (charge and discharge management,

detection, USB / battery power automatic

https://heltec.org

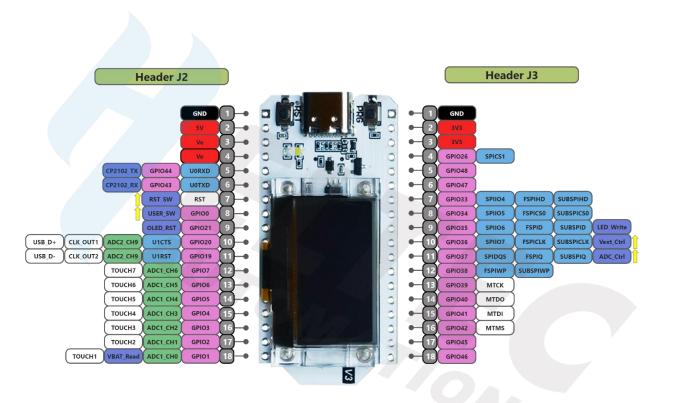
Tabl1: ele 1 sion compariso

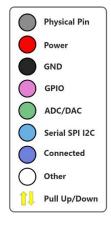


- Onboaf-idnOh9128*64 dot matrix OLED display debugging information, battery power, and
- ▶ Integrated CP2102 USB to serial port chip debugging information printing;
- Compatible Merdwuitho develop ment environment

2. Pin Definition

2.1 h





HT-WB32_V3 Pin map





2.2 h

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Tab 2.2e1: Pin description

			_
V	V	u	7
	GND	Р	Ground.
	5V	Р	5 V Power Supply.
	Ve	Р	Output 3.3V, power supp
	Ve	Р	Output 3.3V, power supp
	RX	I/ O	GPIO44, coUn On ReXcDt, ed to CP21
	TX	I/ O	GPIO43, UORXD, connecte
	RS T	I	CHIP_PU. Connect to RST
	0	I/ O	GPIOO, connect to PRG sv
	21	I/ O	GPIO21, OLED RST.
	20	I/ O	GPIO20, U1CTS, ADC2_1CH9
	19	I/ O	GPI 019, U1RTS, ADC2 <u>-</u> 2CH8
	7	I/ O	GPIO7, ADC1_CH6, TOUCH7
	6	I/ O	GPIO6, ADC1_CH5, TOUCH6
	5	I/ O	GPIO5, ADC1_CH4, TOUCH5
	4	I/ O	GPIO4, ADC1_CH3, TOUCH4
	3	I/ O	GPIO3, ADC1_CH2, TOUCH3
	2	I/ O	GPIO2, ADC1_CH1, TOUCH2

 $^{^{1}\,\}mathsf{DP}$ pin connectable to USB socket, solder R29

² DN pin connectable to USB socket, solder R3



·K

Tab 2.2e2: Pin description

V	V	u	7
	GND	Р	Ground.
	3V 3	Р	3.3V Power Supply.
	3V 3	Р	3.3V Power Supply.
	26	I/ O	GPIO26, SPICS1.
	48	ı/ o	GPIO48, SPICLK_N_DIFF,
	47	ı/ o	GPIO47, SPICLK_P_DIFF,
	33	ı/ o	GPIO33, SPIIO4, FSPIHD,
	34	ı/ o	GPIO34, SPIIO5, FSPICSO
	35	I/ O	GPIO35, SPIIO6, FSPID,
	36	I/ O	GPIO36, SPIIO7, FSPICLK
	37	I/ O	GPIO37, SPIDQS, FSPIQ,
	38	I/ O	GPIO38, FSPIWP, SUBSPIW
	39	I/ O	GPIO39, MTCK.
	40	I/ O	GPIO40, MTDO.
	41	I/ O	GPIO41, MTDI.
	42	I/ O	GPIO42, MTMS.

³ ADC1_CHO is used to read the lithbiauetmtybatst:ery voltage, th $V_{BAT} = 100 \ / \ (100 + 390) * V_{ADC_IN1}$

45	I/ O	G P I 5O 4
46	I/ O	GPI 0 4 6.

3. Specifications

3.1 8

Tab3l1eGeneral specificat

h)
U '#	ESP-S332FXX118 (ens-bai®t32lx7 dua)l c
y o" · · · o · ·#	C P 2 1 0 2
‡ 7	802.11 b/g/n, up to 150 M
п	Blueto oBiltuhet LoEBoltuhet 50,0 th mes
= 'k	7 * ADC1 + 2 * ADC2; 7 * Touk h; e
U	384 K B R O M; 512 K B S R A M;
	Flash
@	Ty ₁ Ce USB; 2 * 1.25 lith 8*u2m
	Header Pin
п	3.7V lithium battery pow
h '	Deepsleep 10 uA
`	-2 0 ~°♂ 0
)	50.2 *1.02m52n.5 *



3.2 h

Except when USB or 5V Pin is connected sep to charge it. In other cases, only a single

Tabl.2ePo3wer supply

h	U	u	U	#
u #'yo" °	4 . 7	5	6	V
O	3 . 3	3 . 7	4 . 2	V
†	4 . 7	5	6	V
†	2 . 7	3 . 3	3 . 5	V

3.3 h

Tab3l3 ePower output

\	U	u	U	#
† 'h			5 0 0	m A
† 'h ' y o" 'h			5 0 0	m A
† 'h			3 5 0	m A

3.4 h

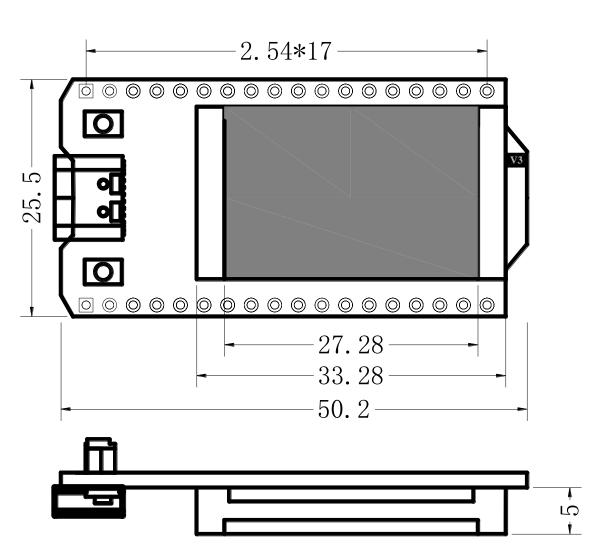
Tabl4: ePo3we Imaracteristics

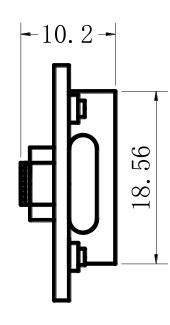
U	#	U	u	U	#
‡ 7 o	USB powered		1 1 5		m A
‡ 7 h	USB powered		150		m A
" u	USB powered		11 5		m A

USB powered	2	m A
VBAT/battery	1 5	uA
3.3V header	1 0	uA

4. Hardware resource

4.1 h







5.1 k k

- Source Code
 - <u>Helte(cESEPS3P2 & ESP8</u> (2A6)) e af drya mie nworkladed Hel LoRa WAN library)
 - <u>Heltec ES</u>P32 library
- <u>Schemati</u>c diagram
- Pi n map
- <u>Downloadab</u>le resource

5.2 # '@

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